

isc Silicon NPN Power Transistor
2SD533
DESCRIPTION

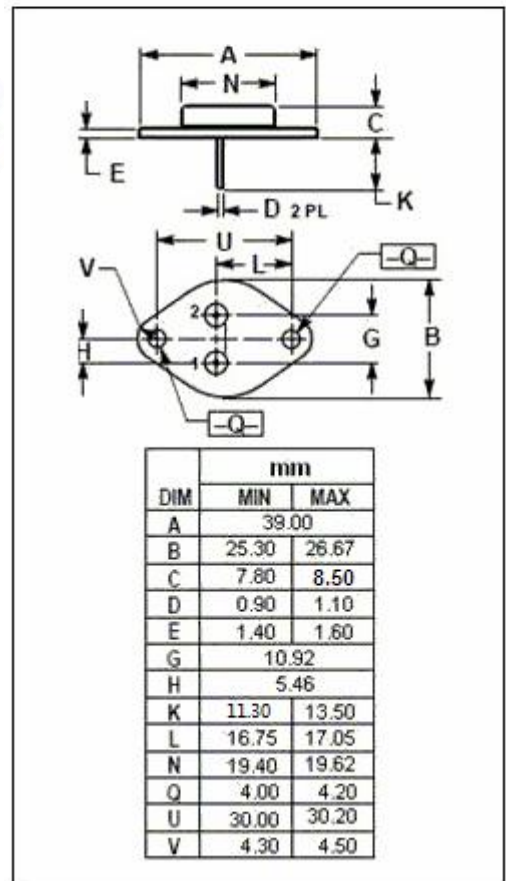
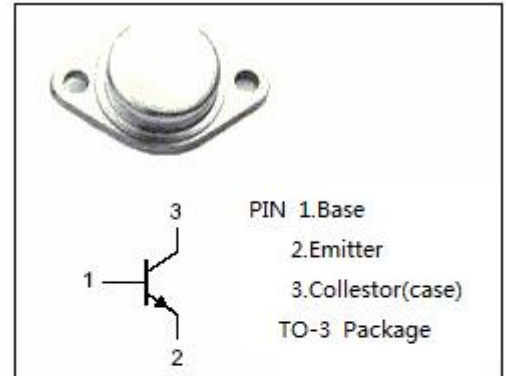
- Collector-Emitter Sustaining Voltage-
: $V_{CEO(SUS)} = 90V(\text{Min})$
- Excellent Safe Operating Area
- High Current Capability
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for use in switching-control amplifiers, power gates, switching regulators, converters, and inverters.

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	270	V
V_{CEO}	Collector-Emitter Voltage	90	V
V_{EBO}	Emitter-Base Voltage	6	V
I_C	Collector Current-Continuous	10	A
I_{CM}	Collector Current-Peak	15	A
I_B	Base Current-Continuous	2	A
P_C	Collector Power Dissipation @ $T_c=25^\circ\text{C}$	100	W
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-65~150	$^\circ\text{C}$



isc Silicon NPN Power Transistor**2SD533****ELECTRICAL CHARACTERISTICS** $T_C=25^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
$V_{CEO(SUS)}$	Collector-Emitter Sustaining Voltage	$I_C=30\text{mA}; I_B=0$	90			V
$V_{CE(sat)-1}$	Collector-Emitter Saturation Voltage	$I_C=5\text{A}; I_B=0.5\text{A}$			0.5	V
$V_{CE(sat)-2}$	Collector-Emitter Saturation Voltage	$I_C=10\text{A}; I_B=1\text{A}$			1.5	V
$V_{BE(sat)-1}$	Base-Emitter Saturation Voltage	$I_C=5\text{A}; I_B=0.5\text{A}$			1.2	V
$V_{BE(sat)-2}$	Base-Emitter Saturation Voltage	$I_C=10\text{A}; I_B=1\text{A}$			1.5	V
I_{CBO}	Collector Cutoff Current	$V_{CB}=270\text{V}; I_E=0$			0.1	mA
I_{EBO}	Emitter Cutoff current	$V_{EB}=6\text{V}; I_C=0$			0.1	mA
h_{FE-1}	DC Current Gain	$I_C=1\text{A}; V_{CE}=2\text{V}$	50		200	
h_{FE-2}	DC Current Gain	$I_C=5\text{A}; V_{CE}=5\text{V}$	30		120	
h_{FE-3}	DC Current Gain	$I_C=10\text{A}; V_{CE}=5\text{V}$	20			
f_T	Current-Gain—Bandwidth Product	$I_C=0.5\text{A}; V_{CE}=10\text{V}; f_{test}=1\text{MHz}$	8			MHz