

Applicable Standards

- Transient Voltage Suppressor Diode Especially Designed For Load Dump Protection
- Compliant With Main Standards
Such As :
 - >ISO 16750-2 Test A 24v System
 - >JASO



APPLICATIONS

- >Auto powers system
- >Automotive instrument
- >Can-bus
- >Bluetooth
- >ABS powers
- >Car GPS
- >Car audio and video

Electrical Characteristics

Symbol	Parameter	Min.	Typ.	Max.	Unit
V_R	Stand-off voltage.		33		V
V_{BR}	Breakdown voltage.	36		41	V
V_C	Clamping voltage.		40		V
I_R	Leakage current at V_R			10	μA
I_T	Test Current		5		mA

Test ISO 16750-2 Test A

24V system

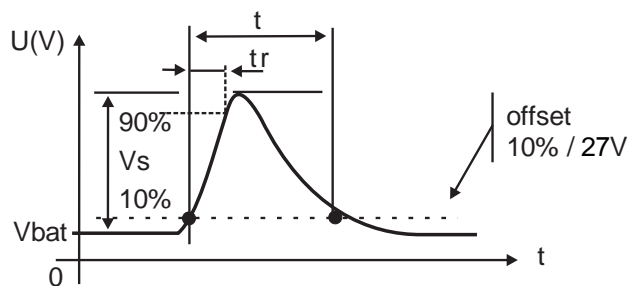
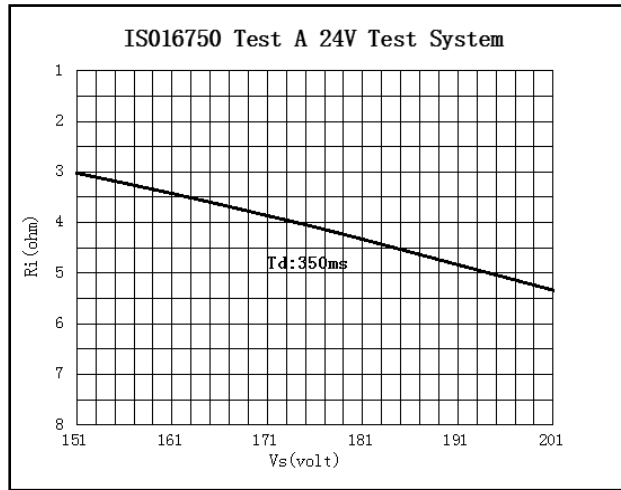


Table 1

Impulse	N°5
V_s (V)	174.0
V_{bat} (V)	27.0
R_i (Ω)	4
t (ms)	350
tr (ms)	<10
Number	10

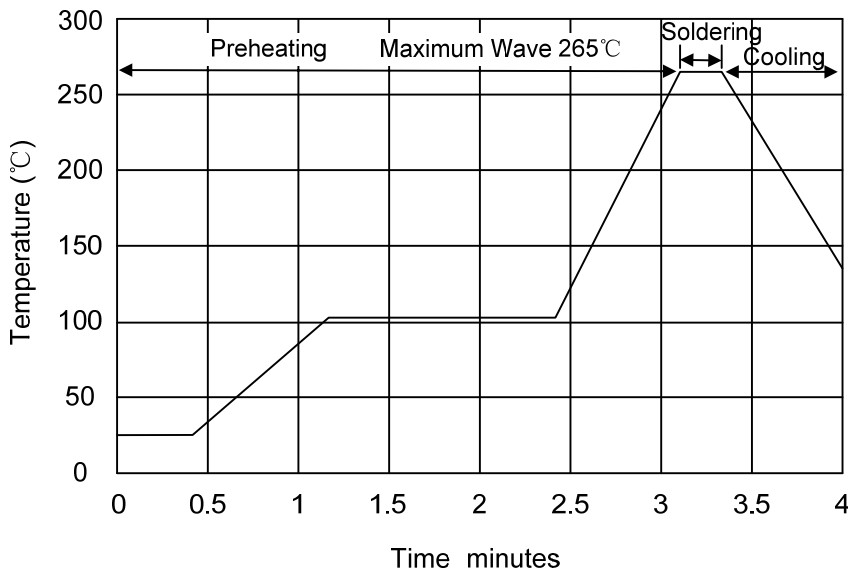
60s between each pulse

Characteristic Cure ($T_A = 25^\circ\text{C}$ unless otherwise noted)



Ri-Vs chart for ISO-16750-2 Test A : 24V System

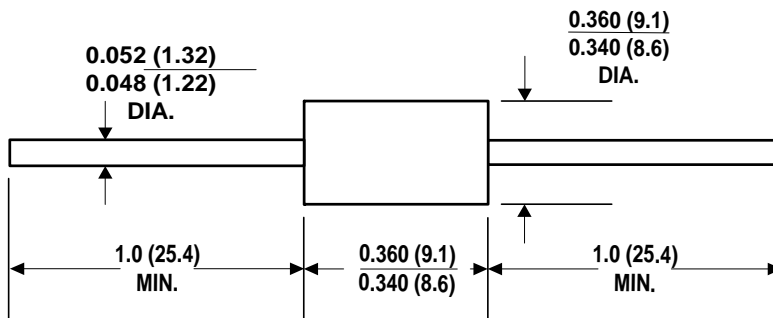
Soldering Parameters



Item	Conditions
Peak Temperature	265 °C
Dipping Time	10 seconds
Soldering	1 time

Package Dimensions

Case Style P600



Dimensions in inches and(millimeters)

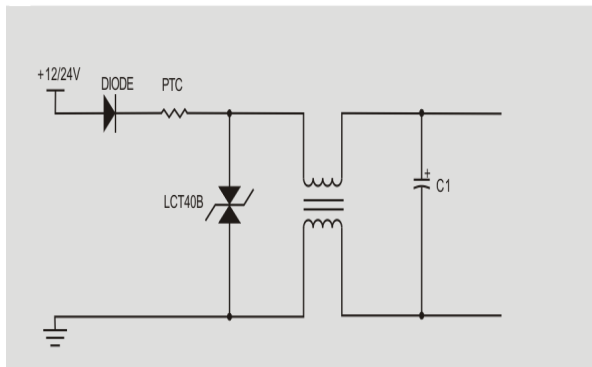
New Features

Because of the lower clamping voltage, it can satisfy 24 v systems withstand voltage value of the power supply chip demand, and it can through the following criteria:

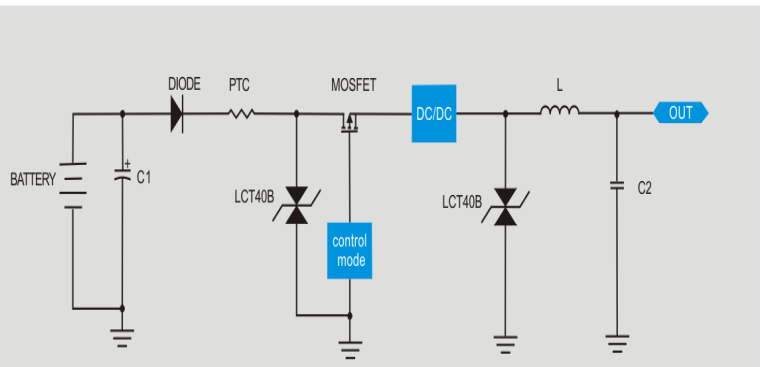
- 1. 24V input voltage to the DUT of all the relevant inputs, last 60S + 10%
- 2. 36V input voltage to the DUT of all the relevant inputs, last 60min

Typical applications circuit

Option one



Option two



Tape and Reel Specification

