DCR5840H42

Phase Control Thyristor

DS6062-1 April 2011 (LN28302)

FEATURES

- Double Side Cooling
- High Surge Capability

APPLICATIONS

- High Power Drives
- High Voltage Power Supplies
- Static Switches

VOLTAGE RATINGS

Part and Ordering Number	Repetitive Peak Voltages V_{DRM} and V_{RRM} V	Conditions
DCR5840H42 DCR5840H40 DCR5840H38 DCR5840H36 DCR5840H34 DCR5840H32 DCR5840H30	4200 4000 3800 3600 3400 3200 3000	T_{vj} = -40°C to 125°C, I_{DRM} = I_{RRM} = 700mA, V_{DRM}, V_{RRM} t_p = 10ms, V_{DSM} & V_{RSM} = V_{DRM} & V_{RRM} +100V respectively

Lower voltage grades available.

ORDERING INFORMATION

When ordering, select the required part number shown in the Voltage Ratings selection table.

For example:

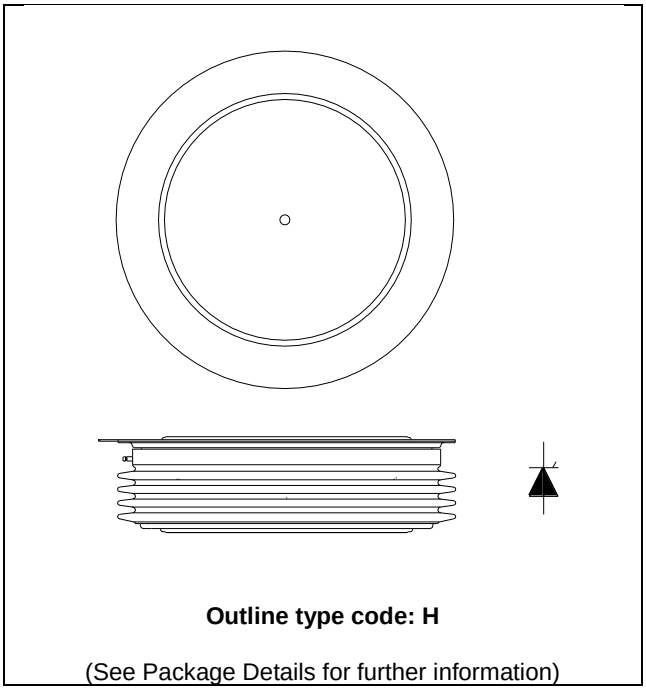
DCR5840H42

Note: Please use the complete part number when ordering and quote this number in any future correspondence relating to your order.

KEY PARAMETERS

V_{DRM}	**4200 V**
$I_{T(AV)}$	**5840 A**
I_{TSM}	**83500 A**
dV/dt*	**1000 V/µs**
dI/dt	**200 A/µs**

* **Higher dV/dt selections available**



Outline type code: H

(See Package Details for further information)

Fig. 1 Package outline

CURRENT RATINGS

T_{case} = 60°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
Double Side Cooled				
$I_{T(AV)}$	Mean on-state current	Half wave resistive load	5840	A
$I_{T(RMS)}$	RMS value	-	9170	A
I_T	Continuous (direct) on-state current	-	8260	A

SURGE RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
I_{TSM}	Surge (non-repetitive) on-state current	10ms half sine, T_{case} = 125°C	83.5	kA
I^2t	I^2t for fusing	V_R = 0	34.86	MA^2s

THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Test Conditions		Min.	Max.	Units
$R_{th(j-c)}$	Thermal resistance – junction to case	Double side cooled	DC	-	0.004	°C/W
$R_{th(c-h)}$	Thermal resistance – case to heatsink	Double side cooled	DC	-	0.0008	°C/W
T_{vj}	Virtual junction temperature	Blocking V_{DRM} / $_{VRRM}$		-40	125	°C
T_{stg}	Storage temperature range			-40	140	°C
F_m	Clamping force			110	130	kN

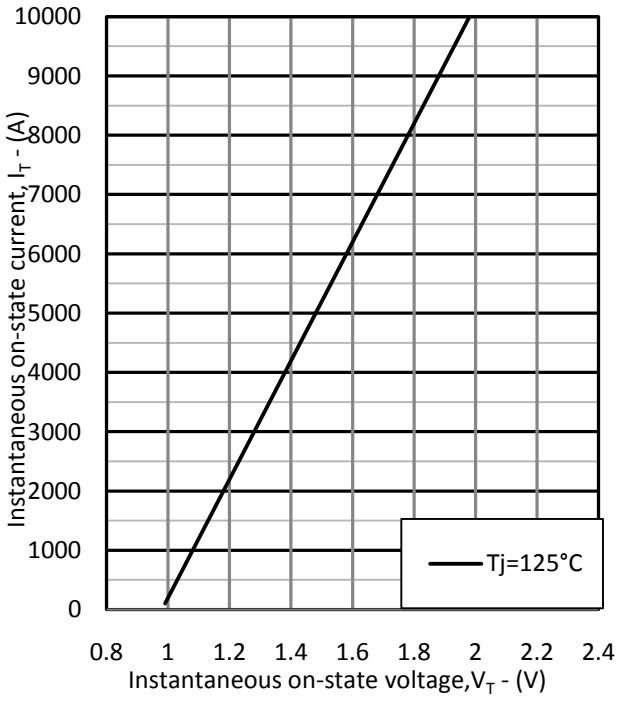
DYNAMIC CHARACTERISTICS

Symbol	Parameter	Test Conditions		Min.	Max.	Units
I_{RRM}/I_{DRM}	Peak reverse and off-state current	At V_{RRM}/V_{DRM}, T_{case} = 125°C		-	700	mA
dV/dt	Max. linear rate of rise of off-state voltage	To 67% V_{DRM}, T_j = 125°C, gate open		1000	-	V/µs
dI/dt	Rate of rise of on-state current	From 67% V_{DRM} to 5000A	Repetitive 50Hz	-	200	A/µs
		Gate source 30V, 10Ω, t_r < 0.5µs, T_j = 125°C	Non-repetitive	-	1000	A/µs
V_T	On-state voltage	I_T = 6000A, T_{case} = 125°C			1.58	V
$V_{T(TO)}$	Threshold voltage	T_{case} = 125°C		-	0.98	V
r_T	On-state slope resistance	T_{case} = 125°C		-	0.10	mΩ
t_{gd}	Delay time	V_D = 67% V_{DRM}, gate source 30V, 10Ω t_r = 0.5µs, T_j = 25°C		-	3.0	µs
t_q	Turn-off time	T_j = 125°C, V_R = 100V, dI/dt = 1.5A/µs, dV_{DR}/dt = 20V/µs linear to 67% V_{DRM}		-	800	µs
Q_S	Stored charge	I_T = 2000A, tp = 1000us, T_j = 125°C, dI/dt =1.5A/µs,		- -	5500	µC
I_L	Latching current	T_j = 25°C,		-	1	A
I_H	Holding current	T_j = 25°C,		-	200	mA

GATE TRIGGER CHARACTERISTICS AND RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
V_{GT}	Gate trigger voltage	V_{DRM} = 5V, T_{case} = 25°C	2.6	V
V_{GD}	Gate non-trigger voltage	At 40% V_{DRM}, T_{case} = 125°C	TBD	V
I_{GT}	Gate trigger current	V_{DRM} = 5V, T_{case} = 25°C	400	mA
I_{GD}	Gate non-trigger current	At 40% V_{DRM}, T_{case} = 125°C	TBD	mA

CURVES



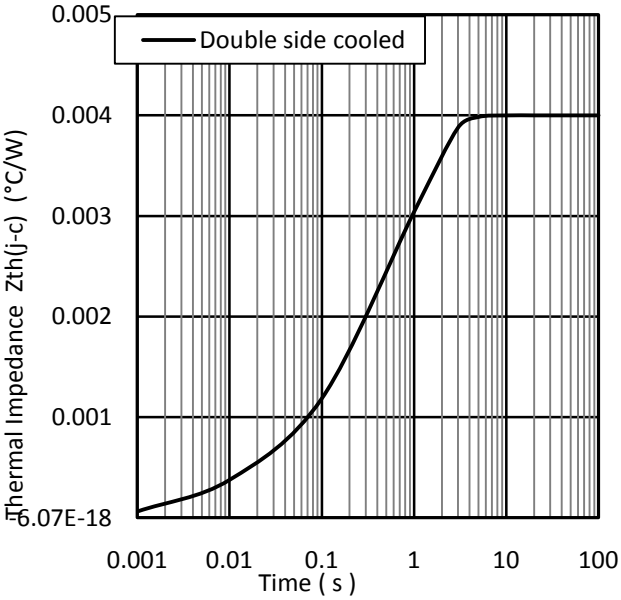
V_{TM} EQUATION

$V_{TM} = A + B\ln(I_T) + C.I_T + D.\sqrt{I_T}$

Where A = 0.853913
B = 0.0289831
C = 0.000115657
D = -0.00280603

These values are valid for T_j = 125°C

Fig.2 Maximum &minimum on-state characteristics



$$R_{thjc}(t) = \sum_{i=1}^{n} R_{thi} \cdot \left(1 - e^{-\frac{t}{\tau_i}}\right)$$

i	τ_i (s)	R_{thi} (°C/kW)
1	0.9692	2.695
2	0.1332	0.814
3	0.0177	0.33
4	0.0042	0.162

Fig.3 Maximum (limit) transient thermal impedance – junction to case (°C/W)

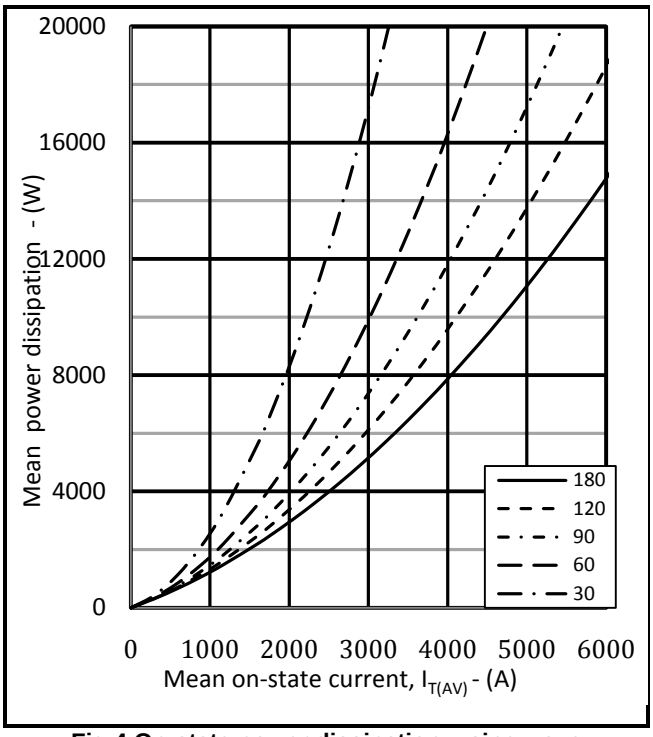


Fig.4 On-state power dissipation – sine wave

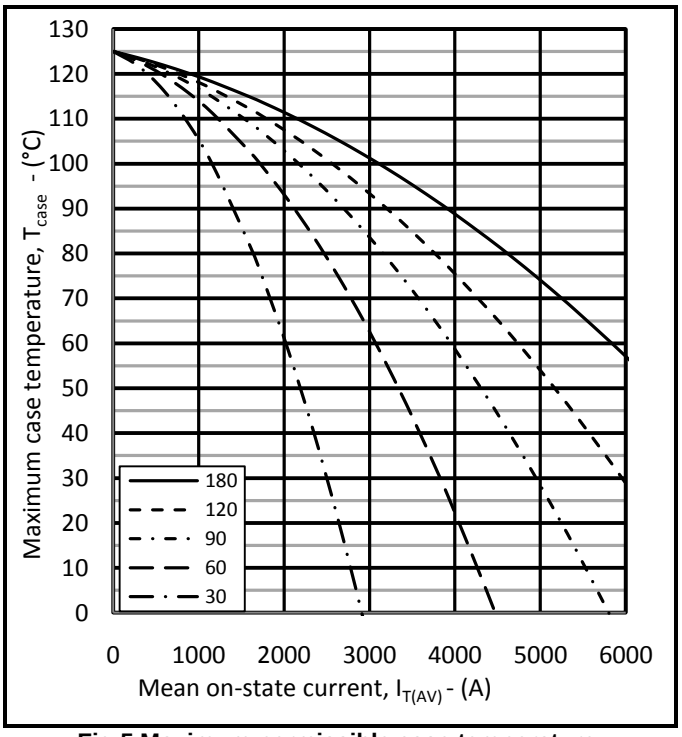


Fig.5 Maximum permissible case temperature, double side cooled – sine wave

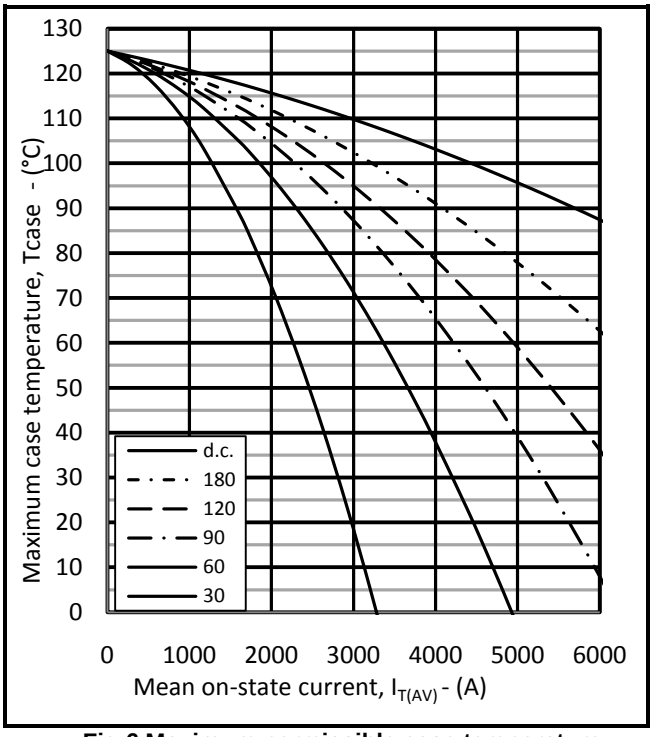


Fig.6 Maximum permissible case temperature, double side cooled – rectangular wave

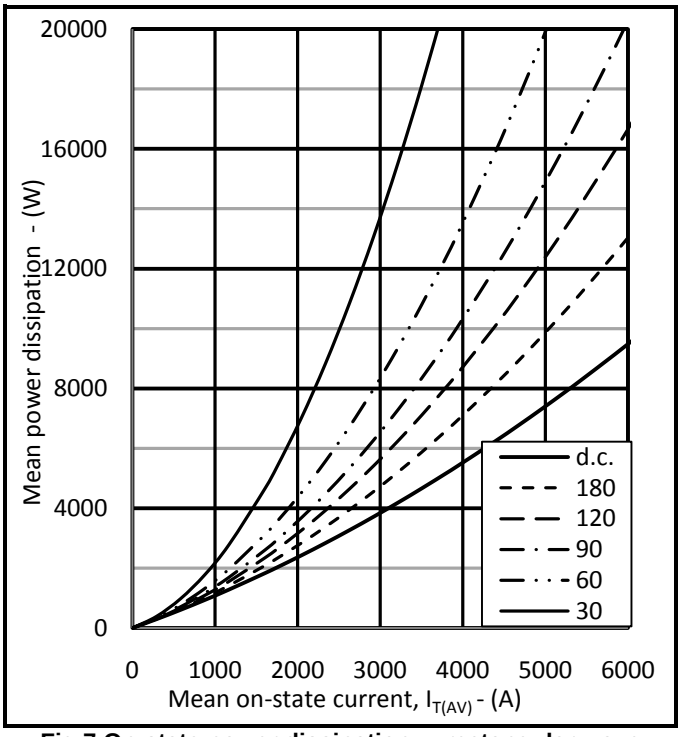


Fig.7 On-state power dissipation – rectangular wave

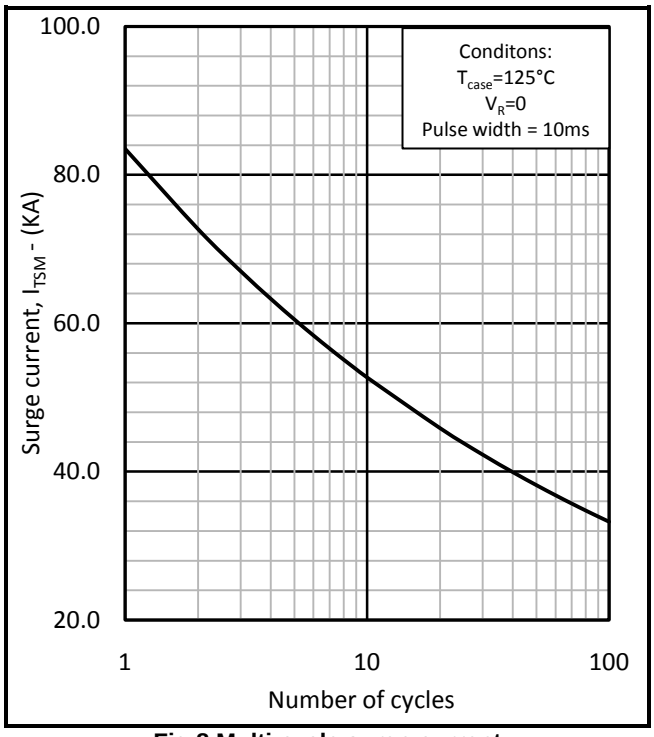


Fig.8 Multi-cycle surge current

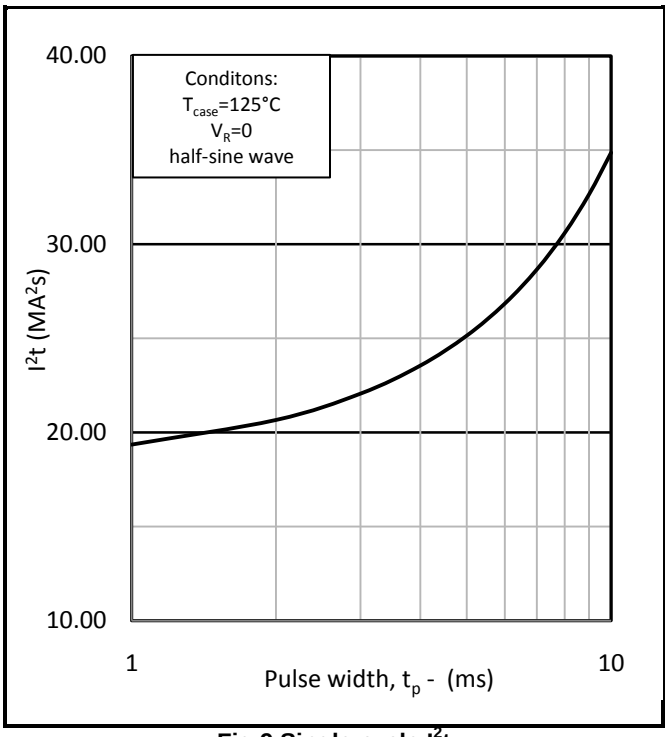


Fig.9 Single-cycle I^2t

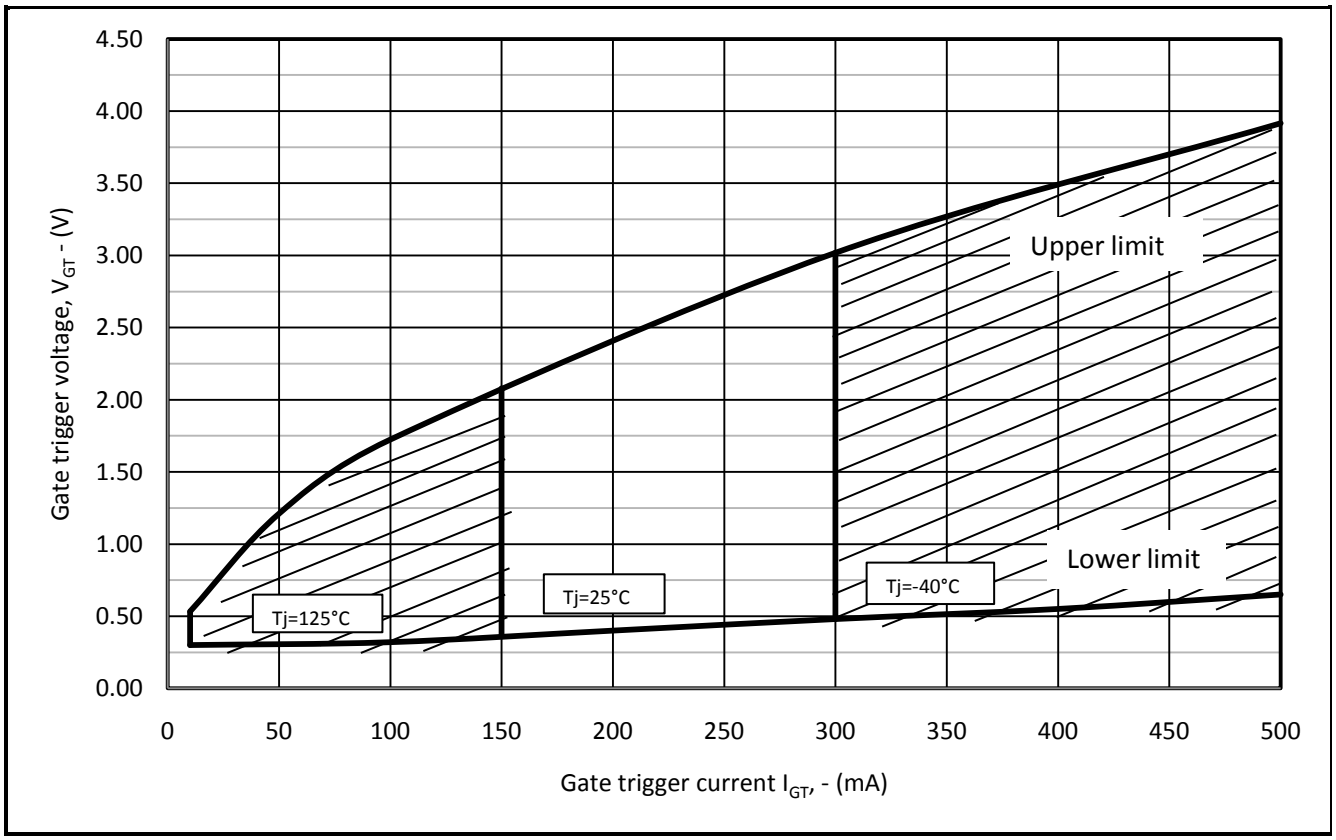
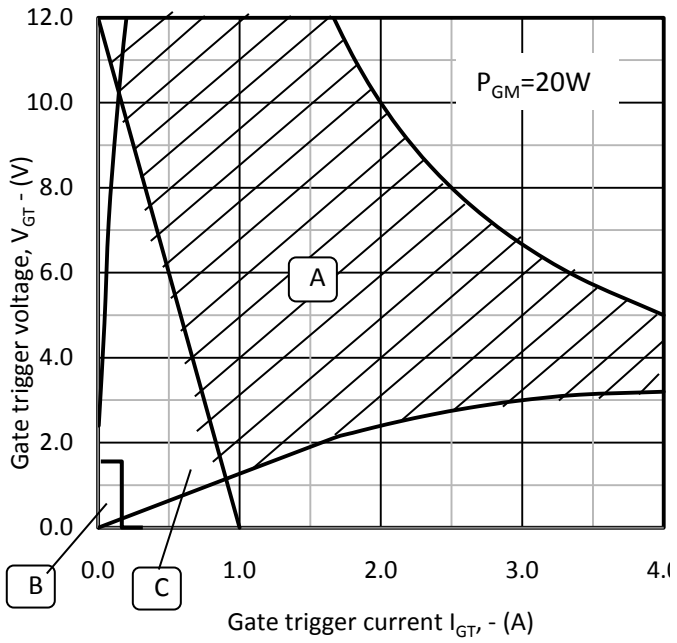


Fig.10 Gate characteristics

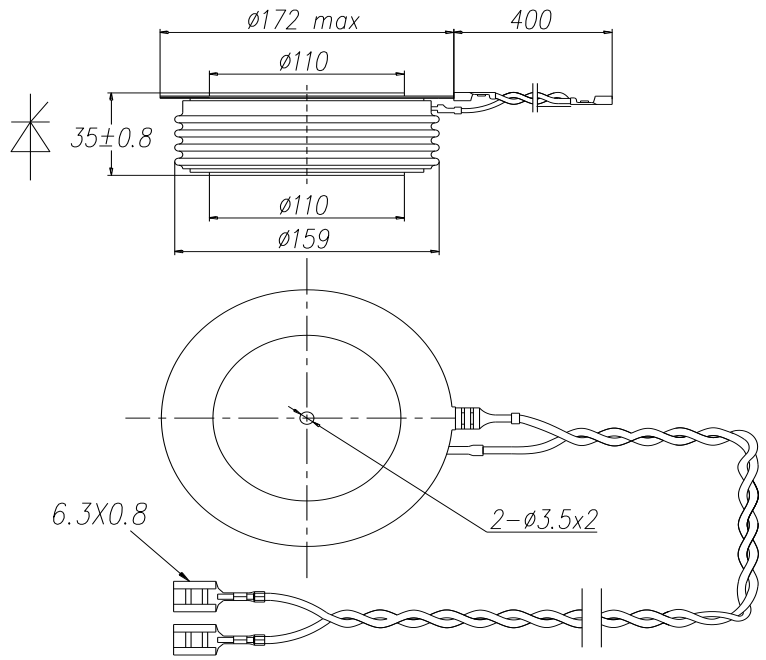


A is Recommended Triggering Area.
B is Unreliable Triggering Area.
C is Recommended Gate Load Line.

Fig.11 Gate characteristics

PACKAGE DETAILS

For further package information, please contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.



Package outline type code: H

Fig.12 Package outline

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