

November, 2008

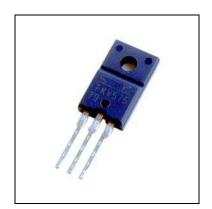
■Features

- High Current Capability (ID:75A (DC))
- •Low on-resistance (RDS(on):7m Ω typ)
- Avalanche energy guarantee

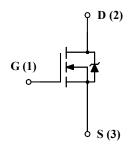
■Applications

- DC-DC Converters
- High speed switching

■Package----FM20(TO220 Full Mold)



■Equivalent circuit



Absolute maximum ratings

(Ta=25°C)

Characteristic	Symbol	Rating	Unit
Drain to Source Voltage	VDSS	50	V
Gate to Source Voltage	VGSS	±20	V
Continuous Drain Current	ID	±75A	A
Pulsed Drain Current	ID(pulse) 1)	±200A	A
Maximum Power Dissipation	PD	40 (Tc=25°C)	W
Single Pulse Avalanche Energy	EAS 2)	300	mJ
Avalanche Current	IAS	75	A
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	-55 to 150	°C

- 1) PW≤100μs, duty cycle≤1%
- 2) VDD=20V,L=64 μ H, ILp=75A,unclamped,RG=50 Ω , See Fig.1

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Electrical characteristics

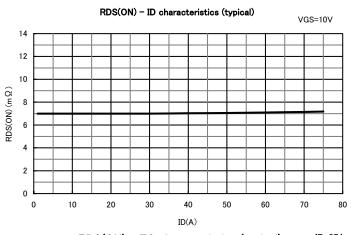
(Ta=25°C)

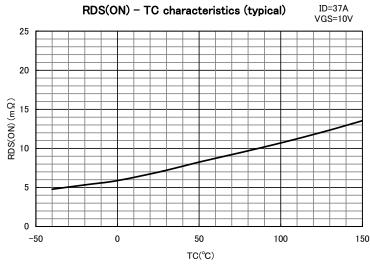
				(1a=25°C)				
Characteristic	Symbol	Test Conditions	Limits			Unit		
		165t Conditions	MIN.	TYP.	MAX.	Ullit		
Drain to Source breakdown Voltage	V(BR)DSS	ID=100μA,VGS=0V	50			V		
Gate to Source Leakage Current	IGSS	VGS=±20V			±100	nA		
Drain to Source Leakage Current	IDSS	VDS=50V, VGS=0V			100	μΑ		
Gate Threshold Voltage	VTH	VDS=10V, ID=250μA	3.0		4.5	V		
Forward Transconductance	Re(Yfs)	VDS=10V, ID=37A	30			S		
Static Drain to Source On-Resistance	RDS(on)	ID=37A, VGS=10V		7	10	mΩ		
Input Capacitance	Ciss	VDS=10V VGS=0V		3200		pF		
Output Capacitance	Coss			1800				
Reverse Transfer Capacitance	Crss	f=1MHz		750				
Turn-On Delay Time	td(on)	ID=37A, VDD \approx 25V RL=0.7 Ω , VGS=10V RG=10 Ω		40		ns		
Rise Time	t r			280				
Turn-Off Delay Time	td(off)			130				
Fall Time	t f	See Fig.2		120				
Total Gate Charge	Qg	ID=37A, VDD≈25V		70				
Gate to Source Charge	Qgs	RL= 0.7Ω , VGS= $10V$		15		nC		
Gate to Drain Charge	Qgd			30				
Source-Drain Diode Forward Voltage	VSD	ISD=75A,VGS=0V		1.0	1.5	V		

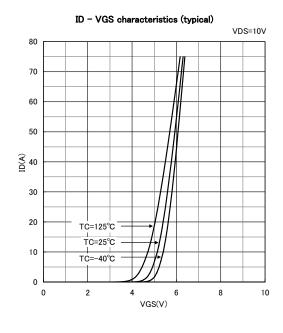


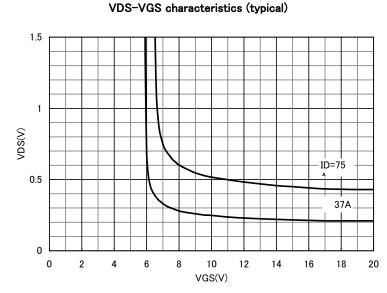
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Characteristic Curves (Tc=25°C)









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Re(yfs)(S)

0.1 L 0.1

FKV575



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Characteristic Curves (Tc=25°C)

Re(yfs) - ID characteristics (typical)

VDS=10V

Tc=-40°C

Tc=125°C

Tc=125°C

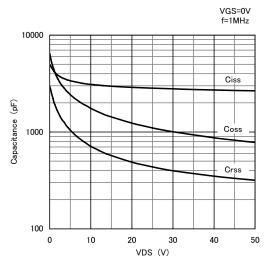
10

ID(A)

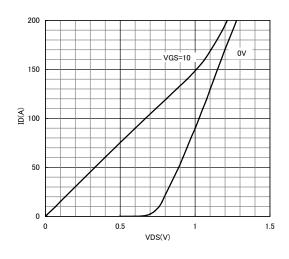
100

1000

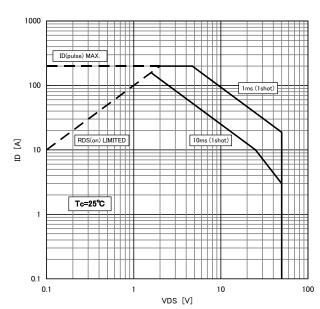
Capacitance - VDS characteristics (typical)



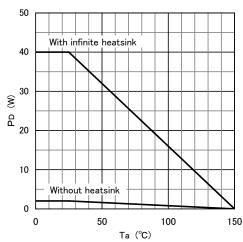
IDR-VSD characteristics (typical)



SAFE OPERATING AREA



PD — Ta characteristics



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Fig.1 Unclamped Inductive Test Method

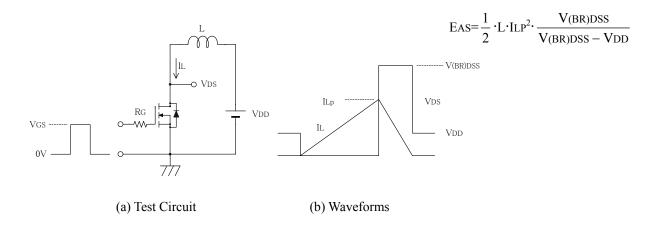
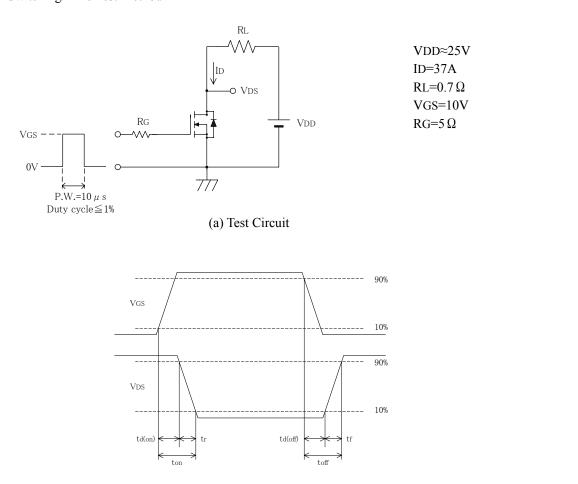


Fig.2 Switching Time Test Method



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(b) Waveforms

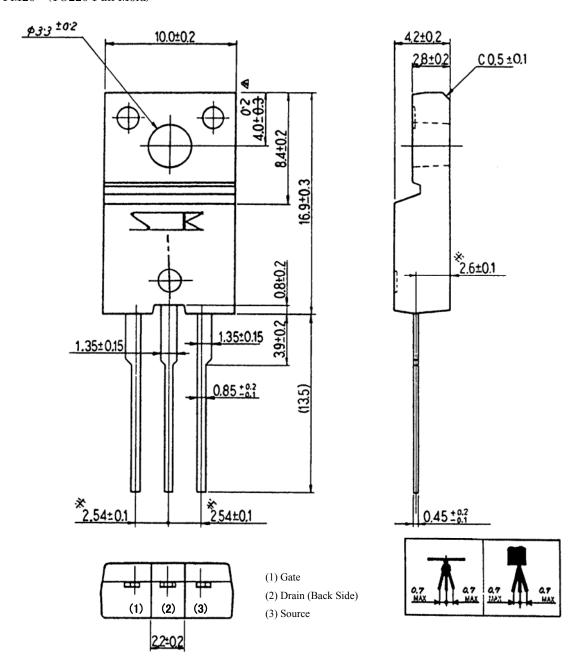
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Outline

FM20 (TO220 Full Mold)



Weight Approx. 2g



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MOS FET

FKV575



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