



## **SPECIFICATIONS FOR LCD MODULE**

## Module No. JHG12864D

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## LCM Number System





## **1. GENERAL DESCRIPTION**

The JHG12864D is a 128 x 64 Dots Graphic LCD module. It has a FSTN panel composed of 128 segments and 64 commons. The LCM can be easily accessed by micro-controller via parallel interface.

## 2. FEATURES

| Hight-Transflective and positive                  |
|---|
| FSTN module B-W mode                              |
| Graphic 128x64 dots                               |
| 8 bit parallel data & I8080 timing input from MPU |
| 1/65 Duty   |
| 1/9 Bias  |
| 12 O'clock  |
| ST7565P (COG)                                     |
| LED/WHITE   |
|   |

## **3. MECHANICAL SPECIFICATION**

| Item                | Specifications       | Unit |
|---------------------|----------------------|------|
| Dimensional outline | 79 x 52.9 x 5.4(max) | mm   |
| Resolution          | 128segs x 64coms     | dots |
| Viewing area        | 70.7(W) x 38.8(H)    | mm   |
| Active area         | 66.52(W) x 33.24(H)  | mm   |
| Dots pitch          | 0.52(W)×0.52(H)      | mm   |
| Dots size           | 0.48(W)×0.48(H)      | mm   |



## 4. MECHANICAL DIMENSION

#### 4.1 LCM DIMENSION .





#### 4.2 BACKLIGHT DIMENSION AND SPECIFICATON.



|  | 符号  |                         | 0                       | 条件                      |                             |   | 值<br>Deting                 | 单位    |
|--|---|-------------------------|-------------------------|-------------------------|-----------------------------|---|-----------------------------|-------|
| * 极限直流正同电流<br>Absolute maximum forware current   | Symbel                                      |                         | Coi                     | nditions                |                             |   | Rating                      | Unit  |
| Hoborate maximum rorware carrent   | Ifm   |                         |                         |                         |                             |   | 100                         | mA    |
| * 脉冲驱动极限正向电流<br>Peak forward current   | Ifp   | ]                       | I msec肪<br>[ mseo p]    | k冲,1/10<br>lus10% Du    | )占空比<br>ıtg Cyele           |   | 300                         | mA    |
| 反向电压<br>Reversr Voltage  | Vr  |                         |                         |                         |                             |   | 5                           | V     |
| * 极限功耗<br>Power dissipation  | pd  |                         |                         |                         |                             |   | 310                         | mW    |
| 工作温度<br>Operating Temperature Bange  | Topr  |                         |                         |                         |                             |   | $-30^{\sim} +70$            | °C    |
| Defailing Temperature Range<br>贮存温度<br>Stewage Temperature Penge                         | Tstg  |                         |                         |                         |                             |   | $-40 \sim +80$              | °C    |
|  | Hour  |                         | Т                       | =25° C                  |                             |   | 100000                      | h     |
| Min lite<br>平均故障间隔时间   | Hour  | T=25°                   | C 湿度65                  | <u>-</u><br>% 产品电       | 流≪75 <b>M</b> 4             | 4 | 20000                       | <br>h |
| 5、元电行任 ELECTRICAL-OFFICAL CHARACTE<br>除非特別说明,环境温度 T=25°C Unless sg<br>项目<br>Item<br>E向电压 | RISTICS:<br>Decified,<br>符号<br>Symbel<br>Vf | ,The Amb<br>最小值<br>min. | ient tem<br>典型值<br>typ. | petature<br>最大值<br>max. | T=25°C)<br>单位<br>Unit.<br>V | ) | 条件<br>Conditio<br>If= 75 mA | n.    |
| `orward Voltage<br>反向电流  | T   | 2.9                     | 3.1                     | 5.5                     |                             |   |                             |       |
| Reverse Current<br>波长  | lr<br>d                                     |                         |                         | 50                      | μA                          |   | VI- 5 V                     |       |
| Wavelength   | $\lambda^{U}$                               |                         |                         |                         | nm                          |   | If= mA                      |       |
| 色坐标<br>Color Coordinate  | X<br>Y                                      | 0.237                   | 0.257                   | 0.277                   | nm                          |   | If= 75 mA                   |       |
| 频谱半波长  | Δλ  |                         | 30                      |                         | nm                          |   | If= 75 mA                   |       |
| Spectral width at half height  | Lv Sub.                                     | 500                     | 550                     |                         | cd/m²                       |   | If= 75 mA                   |       |
| Spectral width at half height<br>*亮度<br>Luminance  |   |                         |                         |                         |                             |   |                             |       |
| Spectral width at half height<br>*亮度<br>Luminance<br>均匀性<br>Unifromity                   |   | 75%                     |                         |                         |                             |   | MIN/MAX*10                  | 00%   |

#### 4.2.1 MTBF

The BACKLIGHT shall be designed to meet a minimum MTBF value of 20000 hours with normal. (25°C in the room without sunlight,  $(65 \pm 10)$  %RH)



## 5. MAXIMUM RATINGS

| Item                  | Symbol                | Min | Max  | Unit | Note |
|-----------------------|-----------------------|-----|------|------|------|
|                       | V <sub>DD</sub> - Vss | 0.3 | 3.6  | V    |      |
| Supply voltage        | $V_{LCD}$             | 0.3 | 14.5 | V    |      |
| Input Voltage         | V <sub>IN</sub>       | 0.3 | 3.6  | V    |      |
| Operating temperature | T <sub>OPR</sub>      | -20 | +70  | °C   |      |
| Storage temperature   | T <sub>STR</sub>      | -30 | +80  | °C   |      |
| Humidity              |                       |     | 90   | %RH  |      |

## 6. ELECTRICAL CHARACTERISTICS

| Item                                   |         | Symbol                    | Condition   | Min.               | Тур. | Max.               | Unit |
|--|---------|---------------------------|---|--------------------|------|--------------------|------|
| Supply Voltage                         | Logic   | $V_{\text{DD}}$           |   | 2.4                | 3.2  | 3.3                | V    |
| Lugat Valtage                          | H level | $V_{\mathrm{IH}}$         |   | $0.8V_{\text{DD}}$ |      | $V_{\text{DD}}$    | V    |
| Input vonage                           | L level | $V_{IL}$                  |   | V <sub>ss</sub>    |      | $0.2V_{\text{DD}}$ | v    |
| Current Consumption<br>(LCD DRIVER)    |         | $I_{DD}$                  | $V_{\text{DD}}=3.2V;$<br>$V_{\text{LCD}}=9.6V, T_{\text{amb}}=25^{\circ}C;$ |                    | 0.7  | 1.2                | mA   |
| LCD Driving Voltage                    |         | $V_{LCD}$                 | Bias=1/9<br>VLCD=V0-Vss   | 9.3                | 9.6  | 9.9                | V    |
| Power Supply f                         | or LED  | $\mathbf{V}_{\mathrm{f}}$ | $I_f = 75 mA$   | 2.9                | 3.1  | 3.3                | V    |
| Current Consumption<br>(LED BACKLIGHT) |         | $I_{\rm f}$               | $V_{DD}=3.2V;$<br>$V_{f}=3.1V,T_{amb}=25^{\circ}C;$                         |                    | 75   | 100                | mA   |



## 7. MODULE FUNCTION DESCRIPTION

### 7.1. PIN DESCRIPTION

| Pin No. | Symbol        | Description   |
|---------|---------------|---|
| 1       | / <b>CS</b> 1 | Chip select signal input, Low active.   |
| 2       | /RES          | Reset signal input, Low active  |
| 3       | A0            | Data/command select signal input, H: Data; L: Command                         |
| 4       | WR            | When use 8080 MPU, this is active Low and Write signal input pin.             |
| 5       | RD            | When use 8080 MPU, this is active Low and Read signal input pin.              |
| 6       | D0            |   |
| 7       | D1            |   |
| 8       | D2            |   |
| 9       | D3            | 9 hit hi dimetional Data hus  |
| 10      | D4            | 8-bit bi-difectional Data bus.  |
| 11      | D5            |   |
| 12      | D6            |   |
| 13      | D7            |   |
| 14      | VDD           | Power supply for Positive (+3V).  |
| 15      | VSS           | Power supply for Ground (0V).   |
| 16      | VOUT          | DC/DC voltage converter. Connect a capacitor between this pin and VDD or VSS. |
| 17      | CAP3+         | DC/DC voltage converter. Connect a capacitor between this pin and CAP1        |
| 18      | CAP1-         | DC/DC voltage converter. Connect a capacitor between this pin and CAP1+.      |
| 19      | CAP1+         | DC/DC voltage converter. Connect a capacitor between this pin and CAP1        |
| 20      | CAP2+         | DC/DC voltage converter. Connect a capacitor between this pin and CAP2        |
| 21      | CAP2-         | DC/DC voltage converter. Connect a capacitor between this pin and CAP2+.      |
| 22      | V1            | This is multi-level power supply for liquid crystal driver, and must          |
| 23      | V2            | maintain the relative magnitudes shown below:                                 |
| 24      | V3            | $V0 \ge V1 \ge V2 \ge V3 \ge V4 \ge VDD$                                      |
| 25      | V4            | ]   |
| 26      | V0            |   |



#### 7.2 TIMING CHARACTERISTICS 1.SYSTEM BUS READ/WRITE CHARACTERISTIC

A0 (CS1 (CS2="1") WR,RD D0 to D7 (Write) D0 to D7 (Read) Figure 37

System Bus Read/Write Characteristics 1 (For the 8080 Series MPU)

| - т | <b>`</b> م | ы  | L۵ | 24          |
|-----|------------|----|----|-------------|
|     | a          | D. | ıe | <u> 4</u> 4 |

|                              |          |        |             | (VDD = 3. | 3V, la = 25<br>in a | 5°C)  |
|------------------------------|----------|--------|-------------|-----------|---------------------|-------|
| Item                         | Signal   | Symbol | Condition   | Rat       | ing                 | Units |
|                              |          | -,     |             | Min.      | Max.                |       |
| Address hold time            |          | tan8   |             | 0         | —                   |       |
| Address setup time           | A0       | taws   |             | 0         | _                   |       |
| System cycle time            |          | tcyca  |             | 240       | —                   |       |
| Enable L pulse width (WRITE) | WP       | tcclw  |             | 80        | _                   |       |
| Enable H pulse width (WRITE) |          | tcchw  |             | 80        | _                   |       |
| Enable L pulse width (READ)  | PD       | tcclr  |             | 140       | _                   | Ns    |
| Enable H pulse width (READ)  | KD       | tcchr  |             | 80        |                     |       |
| WRITE Data setup time        |          | tds8   |             | 40        | _                   |       |
| WRITE Address hold time      | DO to DZ | tdh8   |             | 0         | _                   |       |
| READ access time             |          | tacca  | CL = 100 pF | _         | 70                  | ]     |
| READ Output disable time     | ]        | tонв   | CL = 100 pF | 5         | 50                  | ]     |

|                              |          | Table  | 25          |             |            |       |
|------------------------------|----------|--------|-------------|-------------|------------|-------|
|                              |          |        |             | (VDD = 2.7) | V, Ta = 25 | °C)   |
| ltem                         | Signal   | Symbol | Condition   | Rati        | Unite      |       |
| nem                          | Signal   | Symbol | condition   | Min.        | Max.       | Onits |
| Address hold time            |          | tans   |             | 0           |            |       |
| Address setup time           | A0       | taws   |             | 0           | _          |       |
| System cycle time            |          | tcyca  |             | 400         | _          |       |
| Enable L pulse width (WRITE) | WD       | tcclw  |             | 220         | _          |       |
| Enable H pulse width (WRITE) | WB       | tсснw  |             | 180         | _          |       |
| Enable L pulse width (READ)  | PD       | tcclr  |             | 220         | _          | ns    |
| Enable H pulse width (READ)  | KD       | tcchr  |             | 180         |            |       |
| WRITE Data setup time        |          | tds8   |             | 40          |            |       |
| WRITE Address hold time      | DO to DZ | tdнø   |             | 0           |            |       |
| READ access time             | D010D/   | tacce  | CL = 100 pF | —           | 140        |       |
| READ Output disable time     |          | tонв   | CL = 100 pF | 10          | 100        |       |

#### Table 26

|                              |          |        |             | (VDD = 1.8<br>Rati | (VDD = 1.8V , 1a = 25<br>Rating |       |
|------------------------------|----------|--------|-------------|--------------------|---------------------------------|-------|
| Item                         | Signal   | Symbol | Condition   | Min.               | Max.                            | Units |
| Address hold time            |          | tan8   |             | 0                  | —                               |       |
| Address setup time           | A0       | taws   |             | 0                  | _                               |       |
| System cycle time            | ]        | tcyca  |             | 640                | —                               |       |
| Enable L pulse width (WRITE) | WD       | tcclw  |             | 360                | _                               | ]     |
| Enable H pulse width (WRITE) | WK       | tсснw  |             | 280                | _                               | ]     |
| Enable L pulse width (READ)  | PD       | tcclr  |             | 360                | _                               | ns    |
| Enable H pulse width (READ)  |          | tcchr. |             | 280                |                                 | ]     |
| WRITE Data setup time        |          | tds8   |             | 80                 | —                               | ]     |
| WRITE Address hold time      | DO to DZ | tdнø   |             | 0                  | _                               | 1     |
| READ access time             |          | taccs  | CL = 100 pF | _                  | 240                             | ]     |
| READ Output disable time     | 1        | tонв   | CL = 100 pF | 10                 | 200                             | 1     |

\*1 The input signal rise time and fall time (tr, tf) is specified at 15 ns or less. When the system cycle time is extremely fast, (tr +tf) ≤ (tcYc8 - tccLW - tccHW) for (tr + tf) ≤ (tcYc8 - tccLR - tccHR) are specified.

\*2 All timing is specified using 20% and 80% of VDD as the reference.

\*3 toolw and toolR are specified as the overlap between /CS1 being "L" (CS2 = "H") and /WR and /RD being at the "L" level.



#### 2. RESET TIMING

| Reset Timing          |  |        |              |           |                |              |        |  |  |
|-----------------------|--|--------|--------------|-----------|----------------|--------------|--------|--|--|
| RES                   |  | trw    |              |           |                |              |        |  |  |
| Internal              | $\downarrow$                                 |        | During reset | $\exists$ | Re             | set complete |        |  |  |
| status                | / M  |        | Figure 41    |           |                |              |        |  |  |
|                       |  |        | Table 36     | (VDD      | = 3.3V . Ta    | =40 to 8     | 35°C ) |  |  |
| ltem                  | Signal                                       | Symbol | Condition    | Min.      | Rating<br>Typ. | Max.         | Units  |  |  |
| Reset time            |  | tR     |              | _         | _              | 1.0          | us     |  |  |
| Reset "L" pulse width | /RES   | trw    |              | 1.0       | _              | _            | us     |  |  |
|                       | Table 37<br>(Vpp = 2.7V , Ta = -40 to 85°C ) |        |              |           |                |              |        |  |  |
| Item                  | Signal                                       | Symbol | Condition    | Min.      | Rating<br>Typ. | Max.         | Units  |  |  |
| Reset time            |  | tR     |              |           | _              | 2.0          | us     |  |  |
| Reset "L" pulse width | /RES   | trw    |              | 2.0       | _              | _            | us     |  |  |
|                       |  |        | Table 38     | (VDD      | = 1.8V , Ta    | n =40 to     | 85°C)  |  |  |

| Item S                | Signal Sur | Symbol        | Condition |      | Unite |      |       |
|-----------------------|------------|---------------|-----------|------|-------|------|-------|
|                       | Signal     | Signal Symbol | condition | Min. | Тур.  | Max. | onits |
| Reset time            |            | tR            |           | _    |       | 3.0  | us    |
| Reset "L" pulse width | /RES       | trw           |           | 3.0  | _     |      | us    |

\*1 All timing is specified with 20% and 80% of VDD as the standard.

### 7.3 APPLICATION OF LCM

#### ■Reference circuit



\* The Vss2 voltage range must be set so that the Vout terminal voltage does not exceed the absolute maximum rated value.



C1=1.0--4.7uF,C2=0.1--4.7uF

### 7.4 TABLE OF COMMAND

|   |              | Table | 16: Ta | able c | of S   | T756     | 5.        | Com        | ma                  | nds            |                         | (Note) *: disabled data  |
|---|--------------|-------|--------|--------|--------|----------|-----------|------------|---------------------|----------------|-------------------------|--|
| Command   | Command Code |       |        |        |        |          |           |            | unction             |                |                         |  |
| oon in and  | A0           | /RD   | /WR    | D7     | D6     | D5       | D4        | D3         | D2                  | D1             | D.                      |  |
| (1) Display ON/OFF  | 0            | 1     | 0      | 1      | 0      | 1        | 0         | 1          | 1                   | 1              | 0<br>1                  | LCD display ON/OFF<br>0: OFF, 1: ON  |
| (2) Display start line set  | 0            | 1     | 0      | 0      | 1      | D        | spla      | ay sta     | art a               | ddre           | ∋ss                     | Sets the display RAM display start<br>line address                                 |
| (3) Page address set  | 0            | 1     | 0      | 1      | 0      | 1        | 1         | Pa         | ge a                | addr           | ess                     | Sets the display RAM page<br>address   |
| (4) Column address set  | 0            | 1     | 0      | 0      | 0      | 0        | 1         | Mos        | st si               | gnifi          | cant                    | Sets the most significant 4 bits of  |
| Column address set<br>lower bit   | 0            | 1     | 0      | 0      | 0      | 0        | 0         | Lea        | umn<br>ist s<br>umn | ignif<br>add   | iress<br>icant<br>tress | Sets the least significant 4 bits of the display RAM column address.               |
| (5) Status read   | 0            | 0     | 1      |        | St     | atus     |           | 0          | 0                   | 0              | 0                       | Reads the status data  |
| (6) Display data write  | 1            | 1     | 0      |        |        | 1        | Writ      | edat       | ta                  |                |                         | Writes to the display RAM  |
| (7) Display data read   | 1            | 0     | 1      |        |        |          | Rea       | ddar       | ta                  |                |                         | Reads from the display RAM   |
| (8) ADC select  | 0            | 1     | 0      | 1      | 0      | 1        | 0         | 0          | 0                   | 0              | 0<br>1                  | Sets the display RAM address<br>SEG output correspondence<br>0: normal, 1: reverse |
| (9) Display normal/<br>reverse  | 0            | 1     | 0      | 1      | 0      | 1        | 0         | 0          | 1                   | 1              | 0<br>1                  | Sets the LCD display normal/<br>reverse<br>0: normal, 1: reverse                   |
| (10) Display all points<br>ON/OFF                                       | 0            | 1     | 0      | 1      | 0      | 1        | 0         | 0          | 1                   | 0              | 0<br>1                  | Display all points<br>0: normal display<br>1: all points ON                        |
| (11) LCD bias set   | 0            | 1     | 0      | 1      | 0      | 1        | 0         | 0          | 0                   | 1              | 0<br>1                  | Sets the LCD drive voltage bias<br>ratio<br>0: 1/9 bias, 1: 1/7 bias (ST7565P)     |
| (12) Read/modify/write  | 0            | 1     | 0      | 1      | 1      | 1        | 0         | 0          | 0                   | 0              | 0                       | Column address increment<br>At write: +1<br>At read: 0                             |
| (13) End  | 0            | 1     | 0      | 1      | 1      | 1        | 0         | 1          | 1                   | 1              | 0                       | Clear read/modify/write  |
| (14) Reset  | 0            | 1     | 0      | 1      | 1      | 1        | 0         | 0          | 0                   | 1              | 0                       | Internal reset   |
| (15) Common output<br>mode select                                       | 0            | 1     | 0      | 1      | 1      | 0        | 0         | 0<br>1     | ٠                   | ٠              | ٠                       | Select COM output scan direction<br>0: normal direction<br>1: reverse direction    |
| (16) Power control set  | 0            | 1     | 0      | 0      | 0      | 1        | 0         | 1          | Op                  | oera<br>ode    | ting                    | Select internal power supply<br>operating mode                                     |
| (17) V0 voltage regulator<br>internal resistor ratio                    | 0            | 1     | 0      | 0      | 0      | 1        | 0         | 0          | Re                  | esist<br>atio  | or                      | Select internal resistor<br>ratio(Rb/Ra) mode                                      |
| (18) Electronic volume<br>mode set<br>Electronic volume<br>register set | 0            | 1     | 0      | 1<br>0 | 0<br>0 | 0<br>Ele | 0<br>ctro | 0<br>nic v | 0<br>olur           | 0<br>ne v      | 1<br>/alue              | Set the V0 output voltage<br>electronic volume register                            |
| (19) Static indicator<br>ON/OFF   | 0            | 1     | 0      | 1      | 0      | 1        | 0         | 1          | 1                   | 0              | 0                       | 0: OFF, 1: ON  |
| Static indicator<br>register set  |              |       | -      | 0      | 0      | 0        | 0         | 0          | 0                   | 0              | Mode                    | Set the flashing mode  |
| (20) Booster ratio set  | 0            | 1     | 0      | 1<br>0 | 1<br>0 | 1<br>0   | 1<br>0    | 1<br>0     | 0<br>0              | 0<br>ste<br>va | 0<br>p-up<br>alue       | select booster ratio<br>00: 2x,3x,4x<br>01: 5x<br>11: 6x                           |
| (21) Power saver  |              |       |        |        |        |          |           |            |                     |                |                         | Display OFF and display all<br>points ON compound command                          |
| (22) NOP  | 0            | 1     | 0      | 1      | 1      | 1        | 0         | 0          | 0                   | 1              | 1                       | Command for non-operation  |
| (23) Test   | 0            | 1     | 0      | 1      | 1      | 1        | 1         | *          | *                   | *              | *                       | Command for IC test. Do not<br>use this command                                    |

## 8. ELECTRO-OPTICAL CHARACTERISTICS

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| Item                   | Symbol          | Condition           | Temp                       | Min | Тур. | Max | Units | Note  |
|------------------------|-----------------|---------------------|----------------------------|-----|------|-----|-------|-------|
| LCD driving<br>voltage |                 | $\theta = \phi = 0$ | 0°C                        |     | 9.4  |     |       | NOTE1 |
|                        | VLCD            |                     | 25°C                       | 9.3 | 9.6  | 9.9 | v     |       |
|                        |                 |                     | 50°℃                       |     | 9.8  |     | ]     |       |
|                        | Rise Time (Tr)  | $\theta = \phi = 0$ | $b = 0 \qquad 0^{\circ} C$ |     |      |     | msec  | NOTE2 |
|                        | Decay Time (Tf) |                     |                            |     |      |     |       |       |
|                        | Rise Time (Tr)  |                     |                            |     | 225  | 340 |       |       |
| Response Time          | Decay Time (Tf) |                     |                            |     | 240  | 360 |       |       |
|                        | Rise Time (Tr)  |                     | <b>T</b> 0%G               |     |      |     |       |       |
|                        | Decay Time (Tf) |                     | 50°C                       |     |      |     |       |       |
| Contrast Ratio         | Cr              | $\theta = \phi = 0$ | <b>25°</b> ℃               | 5   | 10   |     |       | NOTE4 |

| Viewing Angle<br>Range | $\theta (\phi = 0^{\circ})$ (6") | $\phi = 90^{\circ}$ (3") | ψ=180°<br>(12") | φ=270°<br>(9") | 備註           |
|------------------------|----------------------------------|--------------------------|-----------------|----------------|--------------|
| $\theta$ (25°C)        | 40                               | 30                       | 20              | 30             | Deg<br>NOTE3 |

• For panel only

• Electro-Optical Characteristics Measuring Equipment(DMS501)



System

Illumination (D65)

### • Note 1. Definition of Driving Voltage(Vlcd) :





• Note 2. Definition of Optical Response Time :



• Note 3. Definition of Viewing Angle  $\theta$  and  $\phi$  :





#### • Note 4. Definition of Contrast ratio(CR) :







## 9. RELIABILITY

#### 9.1. MTBF

The LCD module shall be designed to meet a minimum MTBF value of 20000 hours with normal. (25°C in the room without sunlight,( $65 \pm 10$ )%RH)

#### 9.2. TESTS

| NO. | ITEM  | CONDITION   | CRITERION   |
|-----|---|---|---|
| 1   | High Temperature Operating                  | 70°C 120Hrs   | <ul> <li>No Defect Of</li> <li>Operational Function In</li> <li>Room Temperature Are</li> </ul> |
| 2   | Low Temperature Operating                   | -20°C 120Hrs  | Allowable.<br>• IDD of LCM in   |
| 3   | High Temperature/<br>Humidity Non-Operating | 60°C,90%RH,120 Hrs  | Pre-and post-test should follow specification   |
| 4   | High Temperature<br>Non-Operating           | 80°C 120Hrs   |   |
| 5   | Low Temperature<br>Non-Operating            | -30°C 120Hrs  |   |
| 6   | Temperature Cycling<br>Non-Operating        | $-20^{\circ}C(30Min) \leftrightarrow 60^{\circ}C(30Min)$<br>10 CYCLES |   |

Notes: Judgments should be mode after exposure in room temperature for two hours.



## **10. PRECAUTIONS FOR USING LCD MODULES**

#### 10.1. HANDLING PRECAUTIONS

- (1) The display panel is made of glass. Do not subject it to a mechanical shock or impact by dropping it.
- (2) If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.
- (3) Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- (4) The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- (5) If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten a cloth with one of the following solvents:
  - Isopropyl alcohol
  - Ethyl alcohol
- (6) Solvents other than those above mentioned may damage the polarizer.

Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents
- (7) Extra care to minimize corrosion of the electrode. Water droplets, moisture condensation or a current flow in a high-humidity environment accelerates corrosion of the electrode.
- (8) Install the LCD Module by using the mounting holes. When mounting the LCD Module, make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
- (9) Do not attempt to disassemble or process the LCD Module.
- (10) NC terminal should be open. Do not connect anything.
- (11) If the logic circuit power is off, do not apply the input signals.
- (12) To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
  - Be sure to ground the body when handling he LCD Module.
  - Tools required for assembling, such as soldering irons, must be properly grounded.
  - -To reduce the amount of static electricity generated, do not conduct assembling and other work under dry conditions.



-The LCD Module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.

#### 10.2. STORAGE CONDITIONS

When storing, avoid the LCD module to be exposed to direct sunlight of fluorescent lamps. For stability, to keep it away form high temperature and high humidity environment (The best condition is :  $23\pm5^{\circ}$ C,  $45\pm20\%$ RH). ESD protection is necessary for long-term storage also.

#### 10.3. OTHERS

Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.

If the LCD Module have been operating for a long time showing the same display patterns the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be recovered by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.

To minimize the performance degradation of the LCD Module resulting from destruction caused by static electricity etc. exercise care to avoid holding the following sections when handling the modules.

- Exposed area of the printed circuit board.
- Terminal electrode sections.



## **11. Using LCD modules**

### 11.1 LIQUID CRYSTAL DISPLAY MODULES

LCD is composed of glass and polarizer. Pay attention to the following items when handling.

- (1) Please keep the temperature within specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity.
- (2) Do not touch, push or rub the exposed polarizers with anything harder than a HB pencil lead (glass, tweezers, etc).
- (3) N-hexane is recommended for cleaning the adhesives used to attach front/rear polarizers and reflectors made of organic substances, which will be damaged by chemicals such as acetone, toluene, toluene, ethanol and isopropyl alcohol.
- (4) When the display surface becomes dusty, wipe gently with absorbent cotton or other soft material like chamois soaked in petroleum ether. Do not scrub hard to avoid damaging the display surface.
- (5) Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading.
- (6) Avoid contacting oil and fats.
- (7) Condensation on the surface and contact with terminals due to cold will damage, stain or polarizers. After products are tested at low temperature they must be warmed up in a container before coming is contacting with room temperature air.
- (8) Do not put or attach anything on the display area to avoid leaving marks on.
- (9) Do not touch the display with bare hands. This will stain the display area and degrade insulation between terminals (some cosmetics are determinate to the polarizers).
- (10)As glass is fragile, it tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring.

### 11.2 INSTALLING LCD MODULE

Attend to the following items when installing the LCM.

- (1) Cover the surface with a transparent protective plate to protect the polarizer and LC cell.
- (2) When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements. The measurement tolerance should be  $\pm 0.1$ mm.

### 11.3 ELECTRO-STATIC DISCHARGE CONTROL

Since this module uses a CMOS LSI, the same careful attention should be paid for electrostatic discharge as for an ordinary CMOS IC.



- (1) Make certain that you are grounded when handing LCM.
- (2) Before removing LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential.
- (3) When soldering the terminal of LCM, make certain the AC power source for the soldering iron does not leak.
- (4) When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.
- (5) As far as possible, make the electric potential of your work clothes and that of the workbenches to the ground potential.
- (6) To reduce the generation of electro-static discharge, be careful that the air in the work is not too dried. A relative humidity of 50%-60% is recommended.

#### 11.4 PRECAUTIONS FOR OPERATION

- (1) Viewing angle varies with the change of liquid crystal driving voltage (Vo). Adjust Vo to show the best contrast.
- (2) Driving the LCD in the voltage above the limit will shorten its lifetime.
- (3) Response time is greatly delayed at temperature below the operating temperature range. However, this does not mean the LCD will be out of the order. It will recover when it returns to the specified temperature range.
- (4) If the display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and then on.
- (5) Condensation on terminals can cause an electrochemical reaction disrupting the terminal circuit. Therefore, this product must be used and stored within the specified condition of  $23\pm5^{\circ}$ C,  $45\pm20\%$ RH.
- (6) When turning the power on, input each signal after the positive/negative voltage becomes stable.

#### 11.5 SAFETY

- (1) It is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.
- (2) If any liquid leaks out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and water.



## **12. REVISION HISTORY**

| Version | Revise record                                    | Date     |
|---------|--|----------|
| 1.0     | Original version                                 | 07-07-02 |
| 2.0     | Add LED parameter                                | 07-07-20 |
| 3.0     | Change the backlight From Y-G to White           | 10-03-23 |
| 4.0     | Change LCM model and VOP                         | 10-03-30 |
| 4.01    | Perfect the VER4.0spec, Commany internal modify. | 13-08-01 |
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## SAMPLE APPROVED REPORT

(样品确认单)

| SAMPLE MODEL NO. (样品型号)           | JHG12864D           |  |  |
|-----------------------------------|---------------------|--|--|
| SAMPLE SERIES NUMBER NO. (样品序号)   |                     |  |  |
| SAMPLE QUANTITY (样品数量)            |                     |  |  |
| COLOR/TYPE (底色/类型)                | FSTN(B/W)/POSITIVE  |  |  |
| VIEWING DIRECTION (视角)            | 12:00               |  |  |
| DRIVING METHOD (驱动参数)             | 1/65Duty, 1/9Bias   |  |  |
| LOGIC VOLTAGE (IC 工作电压)           | 3.2V                |  |  |
| LCD VOP (LCD 驱动电压)                | 9.6V                |  |  |
| OPERATING TEMP. (操作温度)            | -20~70℃             |  |  |
| STORAGE TEMP. (储存温度)              | -30~80°C            |  |  |
| POLARIZER MODE (偏光片类型)            | HIGHT-TRANSFLECTIVE |  |  |
| CONTROLLER/DRIVER IC(控制/驱动 IC)    | ST7565P             |  |  |
| BACKLIGHT COLOR/TYPE (背光源类型/颜色)   | LED/WHITE           |  |  |
| BACKLIGHT VOLTAGE (背光电压)          | 3.1V                |  |  |
| SPECIFICATION (规格书 份数)            | 1BATE               |  |  |
| REMARKS:                          |                     |  |  |
| (备注)                              |                     |  |  |
| WRIT BY: DATE: APROV BY: _        | DATE:               |  |  |
| CUSTOMER'S APPROVAL (客户确认):       |                     |  |  |
| 1) FUNCTION (功能): □ OK □          | ] N.G.              |  |  |
| 2) DRIVER CONDITION (驱动条件): □ OK  | □ N.G.              |  |  |
| 3) DISPLAY MODE (显示模式): □ OK      | $\square$ N.G.      |  |  |
| 4) VIEWING ANGLE (视角): □ OK       | $\Box$ N.G.         |  |  |
| 5) BACKLIGHT (背光源): □ OK          | $\square$ N.G.      |  |  |
| 6) DISPLAYING PATTERN (显示效果): □ 0 | $DK \square N.G.$   |  |  |
| CUSTOMER'S CONCLUSIONS (客户意见):    |                     |  |  |
|                                   |                     |  |  |
|                                   |                     |  |  |
| CUSTOMER'S SIGNATURE(客户签名):       | DATE (日期):          |  |  |