High Performance Schottky Rectifier, 1.0 A





PRODUCT SUMMARY			
Package	SMB		
I _{F(AV)}	1.0 A		
V _R	30 V		
V _F at I _F	0.30 V		
I _{RM} max.	15 mA at 125 °C		
T _J max.	150 °C		
Diode variation	Single die		
E _{AS}	3.0 mJ		

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FEATURES

- · Small foot print, surface mountable
- · Very low forward voltage drop
- High frequency operation
- · Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The VS-STPS1L30UPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Rectangular waveform	1.0	A	
V _{RRM}		30	V	
I _{FSM}	t _p = 5 ms sine	360	A	
V _F	1.0 A _{pk} , T _J = 125 °C	0.30	V	
TJ	Range	-55 to +150	°C	

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-STPS1L30UPbF	UNITS
Maximum DC reverse voltage	V _R	30	V
Maximum working peak reverse voltage	V _{RWM}		v

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T_L = 106 °C, rectangular waveform		1.0	
Maximum peak one cycle non-repetitive surge current	1	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	360	А
	10 ms sine or 6 ms rect. pulse	V_{RRM} applied	75		
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 6 mH		3.0	mJ
Repetitive avalanche current	I _{AR}	$\begin{tabular}{lllllllllllllllllllllllllllllllllll$		А	

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COMPLIANT



	SPECIFICATIONS
ELECINICAL	JECIFICATIONS

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop		1 A	T.I = 25 °C	0.420	V
	V _{FM} ⁽¹⁾	2 A	1j=23 0	0.470	
	VFM (**	1 A	T _J = 125 °C	0.300	
		2 A		0.375	
Maximum reverse leakage current		T _J = 25 °C	V _R = Rated V _R	0.2	
	I _{RM} ⁽¹⁾	$T_J = 100 \ ^\circ C$		5.0	mA
		T _J = 125 °C		15	
Maximum junction capacitance	CT	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		200	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		2.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 V/		V/µs	

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 $\,\%$

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		-55 to +150	°C
Maximum thermal resistance, junction to lead	R _{thJL} ⁽²⁾		25	°C/W
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	80	
Approximate weight		0.10	g	
			0.003	oz.
Marking device		Case style SMB (similar to DO-214AA) V13L		3L

Notes

 $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$ (1)

(2) Mounted 1" square PCB



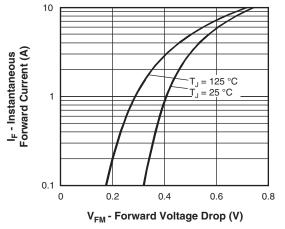


Fig. 1 - Maximum Forward Voltage Drop Characteristics

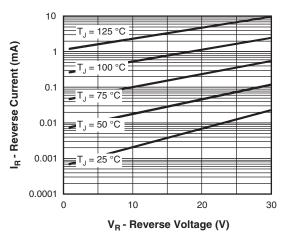


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

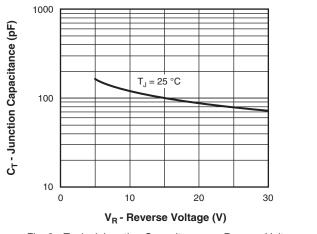


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

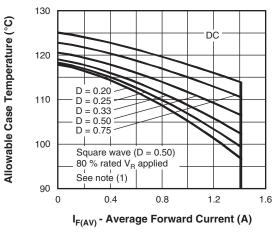


Fig. 4 - Maximum Average Forward Current vs. Allowable Lead Temperature

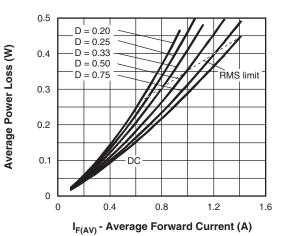


Fig. 5 - Maximum Average Forward Dissipation vs. Average Forward Current

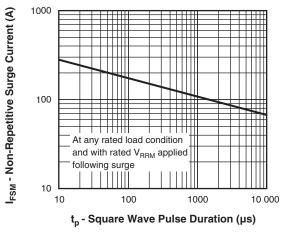


Fig. 6 - Maximum Peak Surge Forward Current vs. Pulse Duration

Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$;
- Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 D); I_R at V_{R1} = 80 % rated V_R

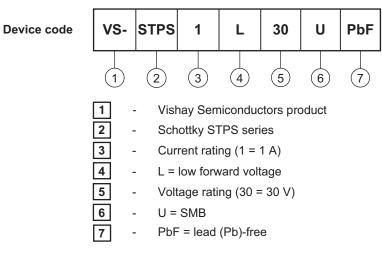
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ORDERING INFORMATION TABLE



Tape and reel only

ORDERING INFORMATION (Example)				
PREFERRED P/N	PREFERRED PACKAGE CODE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION	
VS-STPS1L30UPbF	5BT	3200	13" diameter plastic tape and reel	

LINKS TO RELATED DOCUMENTS		
Dimensions www.vishay.com/doc?95401		
Part marking information	www.vishay.com/doc?95403	
Packaging information	www.vishay.com/doc?95404	

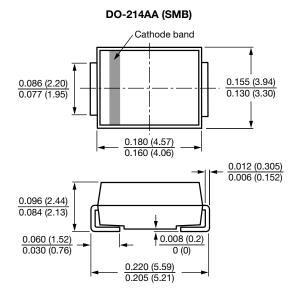


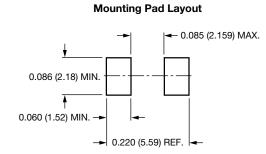
Outline Dimensions

Vishay Semiconductors

SMB

DIMENSIONS in inches (millimeters)







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