**3Q Hi-Com Triac** 4 October 2012

#### Product data sheet

#### **Product profile** 1.

### 1.1 General description

Planar passivated high commutation three quadrant triac in a SOT78 (TO-220AB) plastic package intended for use in circuits where high static and dynamic dV/dt and high dI/dt can occur. This "series C" triac will commutate the full rated RMS current at the maximum rated junction temperature without the aid of a snubber.

### 1.2 Features and benefits

- 3Q technology for improved noise immunity
- High commutation capability with maximum false trigger immunity
- High voltage capability •
- Less sensitive gate for high noise immunity •
- Planar passivated for voltage ruggedness and reliability •
- Triggering in three quadrants only •
- Very high immunity to false turn-on by dV/dt

### 1.3 Applications

- Electronic thermostats (heating and cooling) •
- High power motor controls e.g. washing machines and vacuum cleaners
- Rectifier-fed DC inductive loads e.g. DC motors and solenoids •

### 1.4 Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>DRM</sub>	repetitive peak off- state voltage		-	-	600	V
I <sub>TSM</sub>	non-repetitive peak on- state current	full sine wave; $T_{j(init)} = 25 \text{ °C};$ $t_p = 20 \text{ ms}; \text{ Fig. 4}; \text{ Fig. 5}$	-	-	100	A
I <sub>T(RMS)</sub>	RMS on-state current	full sine wave; T <sub>mb</sub> ≤ 100 °C; <u>Fig. 1;</u> <u>Fig. 2; Fig. 3</u>	-	-	12	A
Static chara	octeristics		· ·			
I <sub>GT</sub>	gate trigger current	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; \text{ T2+ G+};$ $T_j = 25 \text{ °C}; \text{ Fig. 7}$	2	-	35	mA
		V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2+ G-; T <sub>j</sub> = 25 °C; <u>Fig. 7</u>	2	-	35	mA





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Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
		V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2- G-; T <sub>i</sub> = 25 °C; <u>Fig. 7</u>	2	-	35	mA

# 2. Pinning information

		information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	T1	main terminal 1	mb	T2
2	T2	main terminal 2	204	G sym051
3	G	gate		
mb	Τ2	mounting base; main terminal 2		
			TO-220AB (SOT78)	

# 3. Ordering information

#### Table 3.Ordering information

Type number	Package		
	Name	Description	Version
BTA312-600C	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78

# 4. Limiting values

#### Table 4. Limiting values

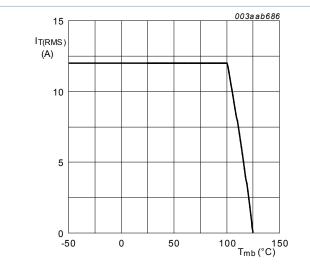
In accordance with the Absolute Maximum Rating System (IEC 60134).

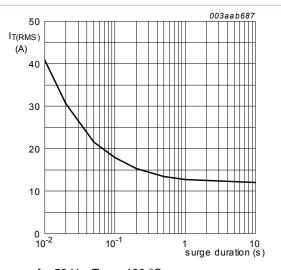
Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>DRM</sub>	repetitive peak off-state voltage		-	600	V
I <sub>T(RMS)</sub>	RMS on-state current	full sine wave; T <sub>mb</sub> ≤ 100 °C; <u>Fig. 1;</u> <u>Fig. 2; Fig. 3</u>	-	12	A
I <sub>TSM</sub>	non-repetitive peak on-state current	full sine wave; $T_{j(init)} = 25 \text{ °C};$ t <sub>p</sub> = 20 ms; <u>Fig. 4</u> ; <u>Fig. 5</u>	-	100	A
		full sine wave; $T_{j(init)} = 25 \text{ °C};$ $t_p = 16.7 \text{ ms}$	-	110	A
l <sup>2</sup> t	I <sup>2</sup> t for fusing	t <sub>p</sub> = 10 ms; SIN	-	50	A <sup>2</sup> s
dl <sub>T</sub> /dt	rate of rise of on-state current	$I_T$ = 20 A; $I_G$ = 0.2 A; $dI_G/dt$ = 0.2 A/µs	-	100	A/µs

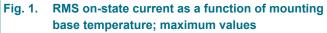
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Symbol	Parameter	Conditions	Min	Max	Unit
I <sub>GM</sub>	peak gate current		-	2	А
P <sub>GM</sub>	peak gate power		-	5	W
P <sub>G(AV)</sub>	average gate power	over any 20 ms period	-	0.5	W
T <sub>stg</sub>	storage temperature		-40	150	°C
Tj	junction temperature		-	125	°C

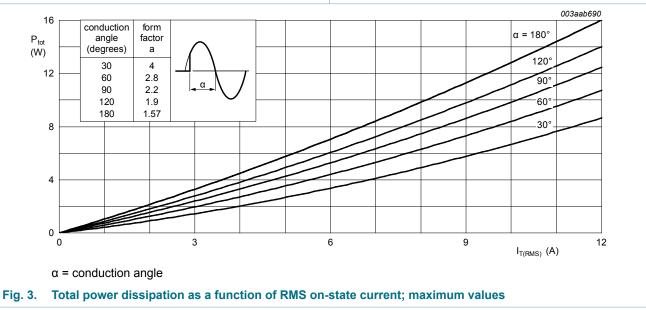






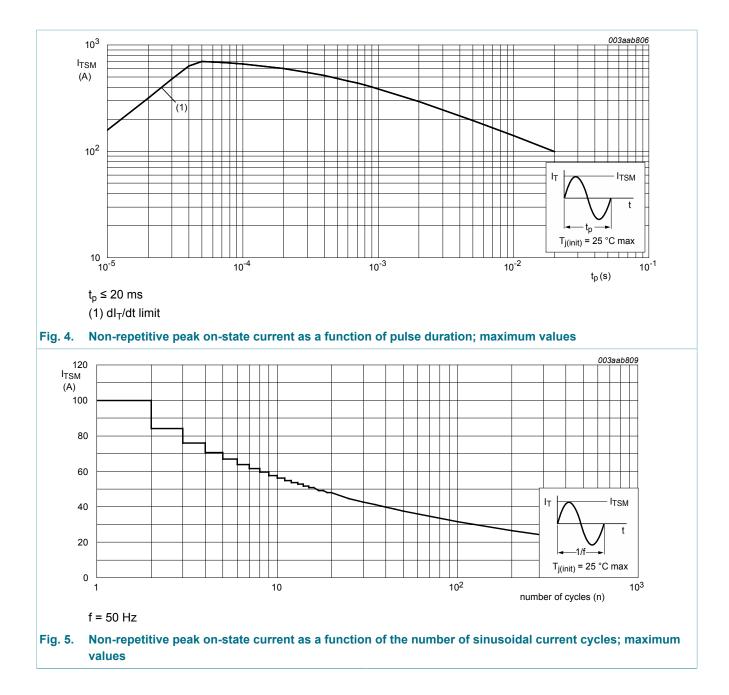
f = 50 Hz; T<sub>mb</sub> = 100 °C





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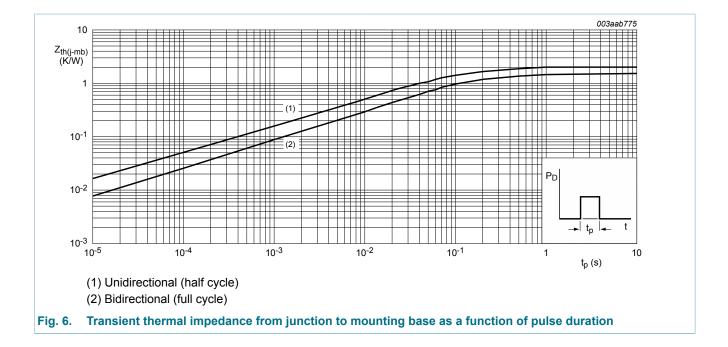


### 5. Thermal characteristics

Table 5. The	rmal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R <sub>th(j-mb)</sub>	thermal resistance	full cycle; <u>Fig. 6</u>	-	-	1.5	K/W
	from junction to mounting base	half cycle; <u>Fig. 6</u>	-	-	2	K/W
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	-	60	-	K/W

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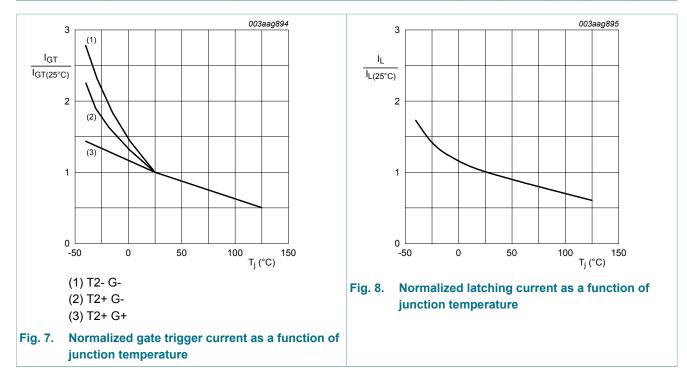
# 6. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static char	racteristics					
I <sub>GT</sub>	gate trigger current	V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2+ G+; T <sub>j</sub> = 25 °C; <u>Fig. 7</u>	2	-	35	mA
		$V_D$ = 12 V; I <sub>T</sub> = 0.1 A; T2+ G-; T <sub>j</sub> = 25 °C; <u>Fig. 7</u>	2	-	35	mA
		V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2- G-; T <sub>j</sub> = 25 °C; <u>Fig. 7</u>	2	-	35	mA
lL	latching current	$V_D = 12 \text{ V}; \text{ I}_G = 0.1 \text{ A}; \text{ T2+ G+};$ T <sub>j</sub> = 25 °C; <u>Fig. 8</u>	-	-	50	mA
		$V_D = 12 \text{ V}; \text{ I}_G = 0.1 \text{ A}; \text{ T2+ G-};$ T <sub>j</sub> = 25 °C; <u>Fig. 8</u>	-	-	60	mA
		$V_D$ = 12 V; I <sub>G</sub> = 0.1 A; T2- G-; T <sub>j</sub> = 25 °C; <u>Fig. 8</u>	-	-	50	mA
I <sub>H</sub>	holding current	V <sub>D</sub> = 12 V; T <sub>j</sub> = 25 °C; <u>Fig. 9</u>	-	-	35	mA
V <sub>T</sub>	on-state voltage	I <sub>T</sub> = 15 A; T <sub>j</sub> = 25 °C; <u>Fig. 10</u>	-	1.3	1.6	V
V <sub>GT</sub>	gate trigger voltage	V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T <sub>j</sub> = 25 °C; Fig. 11	-	0.8	1.5	V
		V <sub>D</sub> = 400 V; I <sub>T</sub> = 0.1 A; T <sub>j</sub> = 125 °C; <u>Fig. 11</u>	0.25	0.4	-	V
I <sub>D</sub>	off-state current	V <sub>D</sub> = 600 V; T <sub>i</sub> = 125 °C	-	0.1	0.5	mA

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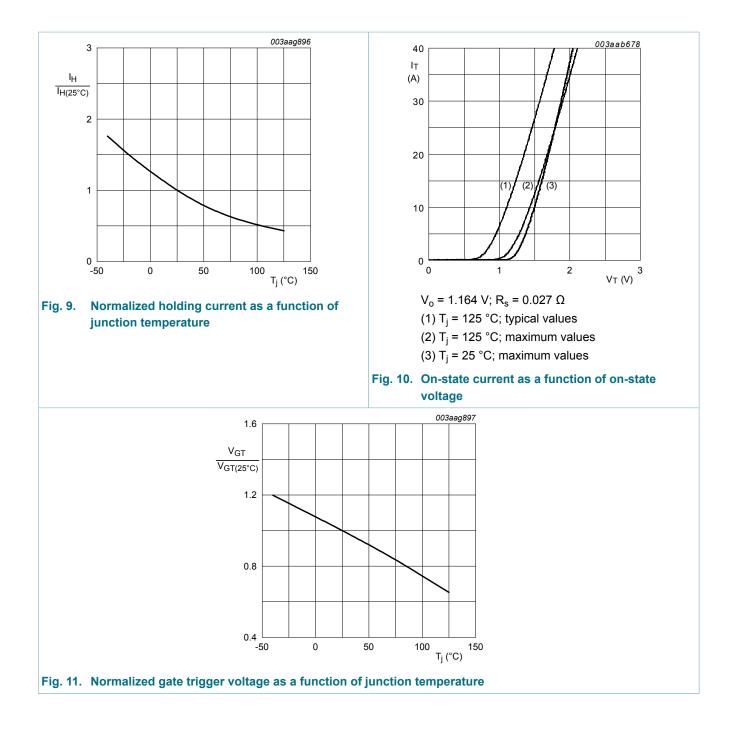
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Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
Dynamic ch	aracteristics					
dV <sub>D</sub> /dt	rate of rise of off-state voltage	$V_{DM}$ = 402 V; T <sub>j</sub> = 125 °C; (V <sub>DM</sub> = 67% of V <sub>DRM</sub> ); exponential waveform; gate open circuit	500	-	-	V/µs
dl <sub>com</sub> /dt	rate of change of commutating current	$\label{eq:VD} \begin{split} V_D &= 400 \text{ V};  \text{T}_{j} = 125 ^\circ\text{C};  \text{I}_{\text{T}(\text{RMS})} = 12 \text{ A}; \\ \text{d} V_{\text{com}}/\text{d} \text{t} &= 20  \text{V}/\mu\text{s};  (\text{snubberless} \\ \text{condition}); \text{ gate open circuit} \end{split}$	20	-	-	A/ms



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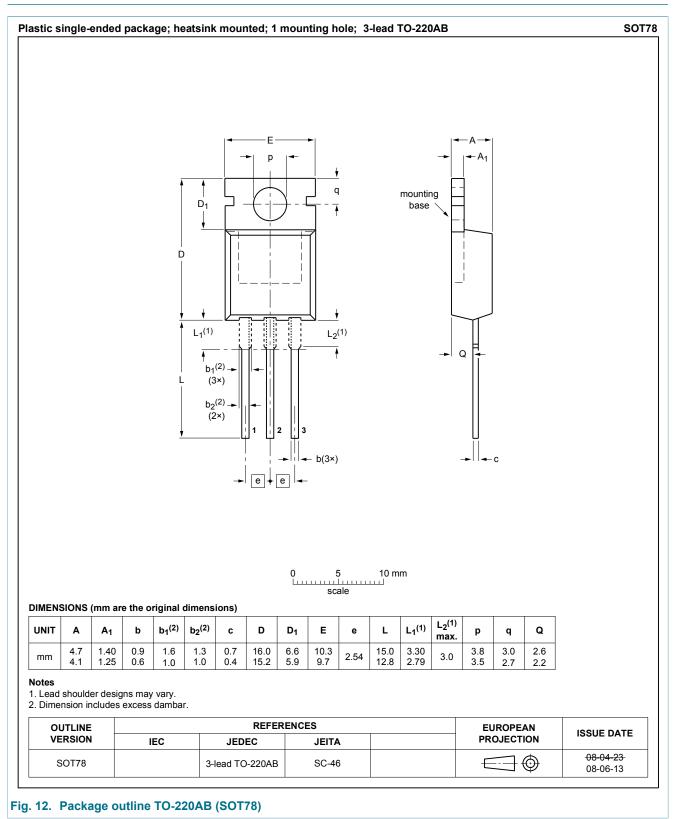
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### 7. Package outline



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Document status [1][2]	Product status [ <u>3]</u>	Definition
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