

## N-Channel Power MOSFET

100V, 15A, 90mΩ

### FEATURES

- 100% avalanche tested
- Low gate charge for fast switching
- Pb-free plating
- RoHS compliant
- Halogen-free mold compound

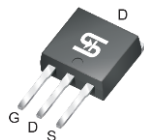
### APPLICATION

- Networking
- Load Switching
- LED Lighting Control
- AC-DC Secondary Rectification

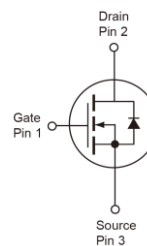
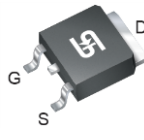
KEY PERFORMANCE PARAMETERS		
PARAMETER	VALUE	UNIT
$V_{DS}$	100	V
$R_{DS(on)}$ (max)	$V_{GS} = 10V$	90
	$V_{GS} = 4.5V$	100
$Q_g$	9.3	nC



**TO-251S (IPAK SL)**



**TO-252 (DPAK)**



**Notes:** Moisture sensitivity level: level 3. Per J-STD-020

ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)			
PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	$V_{DS}$	100	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current <sup>(Note 1)</sup>	$I_D$	$T_C = 25^\circ\text{C}$	15
		$T_C = 100^\circ\text{C}$	9.5
Pulsed Drain Current <sup>(Note 2)</sup>	$I_{DM}$	60	A
Total Power Dissipation @ $T_C = 25^\circ\text{C}$	$P_{DTOT}$	50	W
Single Pulsed Avalanche Energy <sup>(Note 3)</sup>	$E_{AS}$	18	mJ
Single Pulsed Avalanche Current <sup>(Note 3)</sup>	$I_{AS}$	6	A
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	- 55 to +150	$^\circ\text{C}$

THERMAL PERFORMANCE			
PARAMETER	SYMBOL	LIMIT	UNIT
Junction to Case Thermal Resistance	$R_{\theta JC}$	2.5	$^\circ\text{C/W}$
Junction to Ambient Thermal Resistance	$R_{\theta JA}$	62	$^\circ\text{C/W}$

**Notes:**  $R_{\theta JA}$  is the sum of the junction-to-case and case-to-ambient thermal resistances. The case thermal reference is defined at the solder mounting surface of the drain pins.  $R_{\theta JA}$  is guaranteed by design while  $R_{\theta CA}$  is determined by the user's board design.  $R_{\theta JA}$  shown below for single device operation on FR-4 PCB in still air.

<b>ELECTRICAL SPECIFICATIONS</b> ( $T_A = 25^\circ\text{C}$ unless otherwise noted)						
PARAMETER	CONDITIONS	SYMBOL	MIN	TYP	MAX	UNIT
<b>Static</b> (Note 4)						
Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu A$	$BV_{DSS}$	100	--	--	V
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu A$	$V_{GS(TH)}$	1.2	1.6	2.5	V
Gate Body Leakage	$V_{GS} = \pm 20V, V_{DS} = 0V$	$I_{GSS}$	--	--	$\pm 100$	nA
Zero Gate Voltage Drain Current	$V_{DS} = 100V, V_{GS} = 0V$	$I_{DSS}$	--	--	1	$\mu A$
Drain-Source On-State Resistance	$V_{GS} = 10V, I_D = 5A$	$R_{DS(on)}$	--	72	90	m $\Omega$
	$V_{GS} = 4.5V, I_D = 3A$		--	75	100	
<b>Dynamic</b> (Note 5)						
Total Gate Charge	$V_{DS} = 48V, I_D = 5A,$ $V_{GS} = 10V$	$Q_g$	--	9.3	--	nC
Gate-Source Charge		$Q_{gs}$	--	2.1	--	
Gate-Drain Charge		$Q_{gd}$	--	1.8	--	
Input Capacitance	$V_{DS} = 50V, V_{GS} = 0V,$ $f = 1.0\text{MHz}$	$C_{iss}$	--	1480	--	pF
Output Capacitance		$C_{oss}$	--	480	--	
Reverse Transfer Capacitance		$C_{rss}$	--	35	--	
Gate Resistance	$F = 1\text{MHz}, \text{open drain}$	$R_g$	--	1.3	--	$\Omega$
<b>Switching</b> (Note 6)						
Turn-On Delay Time	$V_{DD} = 30V,$ $R_{GEN} = 3.3\Omega,$ $I_D = 1A, V_{GS} = 10V,$	$t_{d(on)}$	--	2.9	--	ns
Turn-On Rise Time		$t_r$	--	9.5	--	
Turn-Off Delay Time		$t_{d(off)}$	--	18.4	--	
Turn-Off Fall Time		$t_f$	--	5.3	--	
<b>Source-Drain Diode</b> (Note 4)						
Forward On Voltage	$I_S = 3.3A, V_{GS} = 0V$	$V_{SD}$	--	--	1	V
Continuous Drain-Source Diode	$V_G = V_D = 0V, \text{Force Current}$	$I_S$	--	--	15	A
Pulse Drain-Source Diode		$I_{SM}$	--	--	60	A

**Notes:**

1. Current limited by package
2. Pulse width limited by the maximum junction temperature
3.  $L = 0.1\text{mH}, I_{AS} = 6A, V_{DD} = 50V, R_G = 25\Omega, \text{Starting } T_J = 25^\circ\text{C}$
4. Pulse test:  $PW \leq 300\mu s, \text{duty cycle} \leq 2\%$
5. For DESIGN AID ONLY, not subject to production testing.
6. Switching time is essentially independent of operating temperature.

**ORDERING INFORMATION (EXAMPLE)**

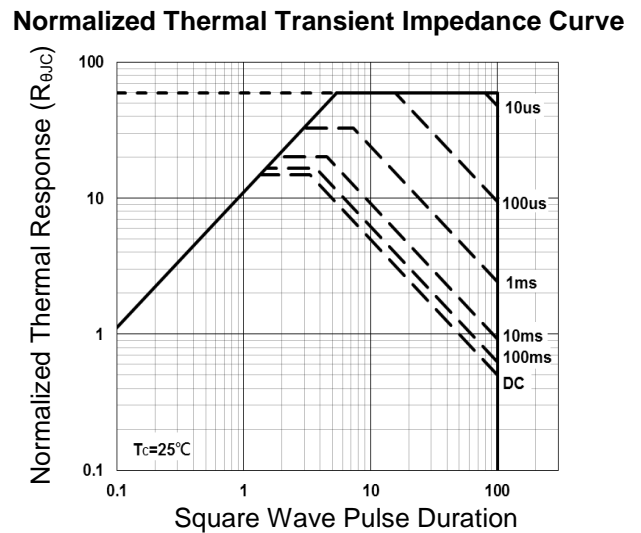
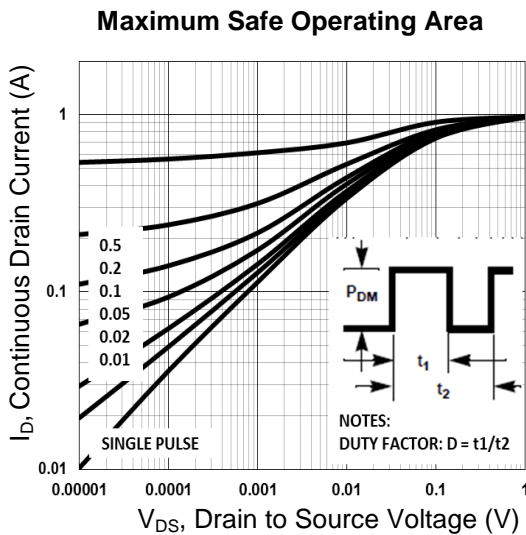
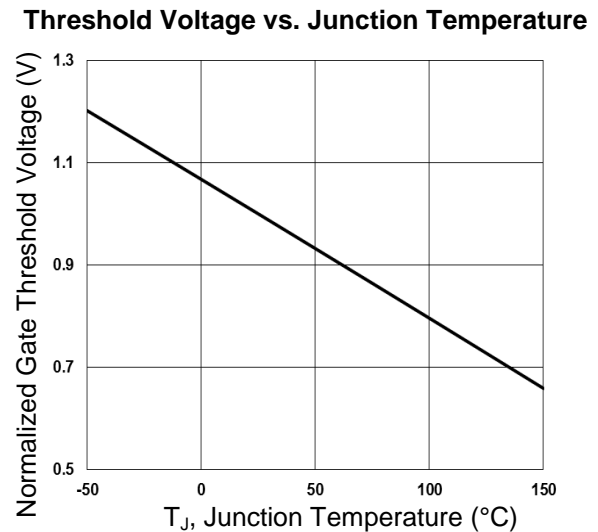
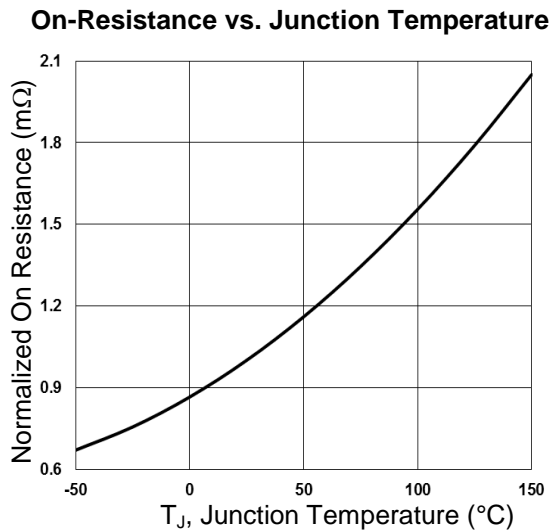
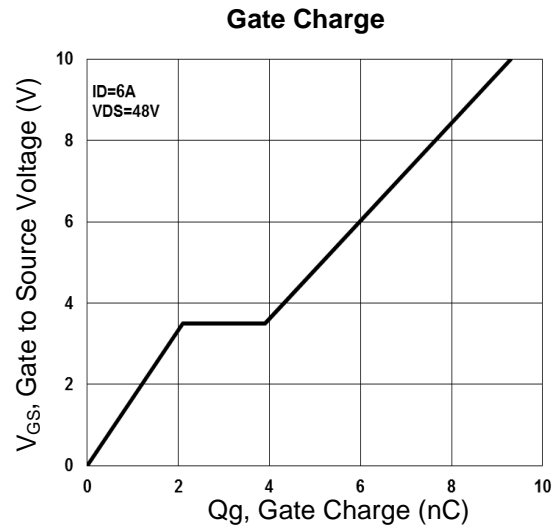
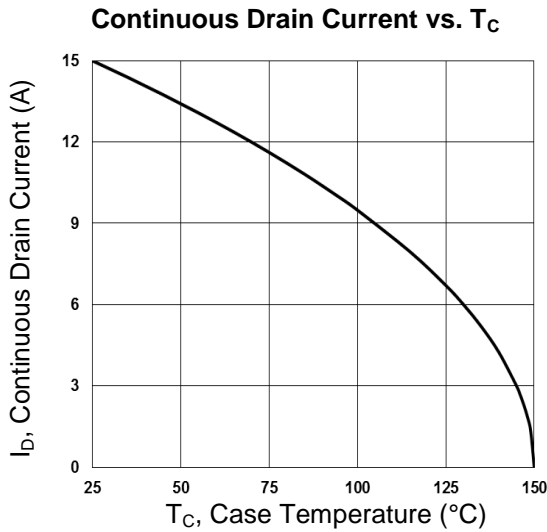
<b>PART NO.</b>	<b>PACKAGE</b>	<b>PACKING</b>
TSM900N10CH X0G	TO-251S (IPAK SL)	75pcs / Tube
TSM900N10CP ROG	TO-252 (DPAK)	2,500pcs / 13" Reel

**Note:**

1. Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
2. Halogen-free according to IEC 61249-2-21 definition

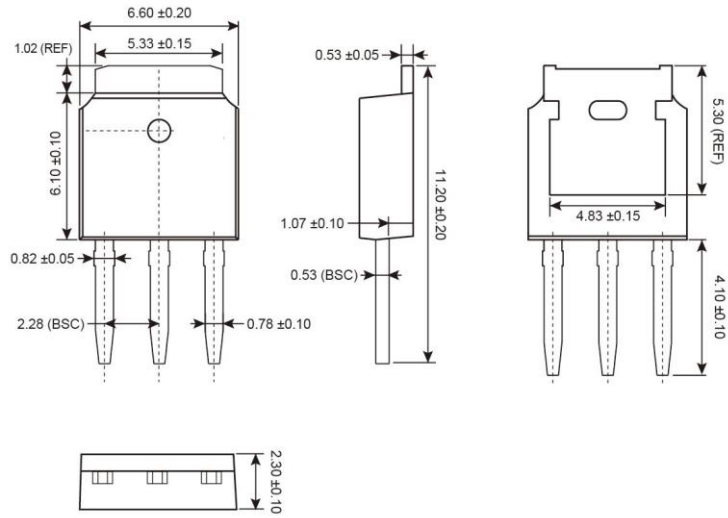
**CHARACTERISTICS CURVES**

( $T_C = 25^\circ\text{C}$  unless otherwise noted)

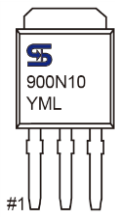


**PACKAGE OUTLINE DIMENSIONS** (Unit: Millimeters)

**TO-251S (IPAK SL)**



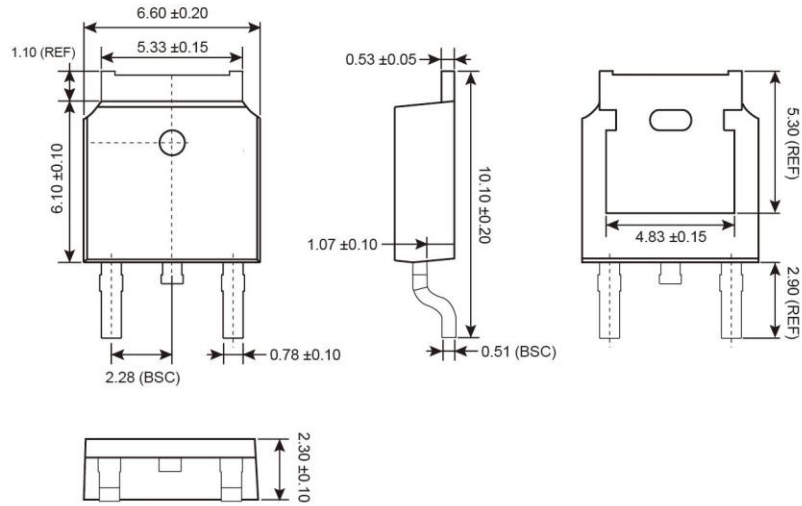
**MARKING DIAGRAM**



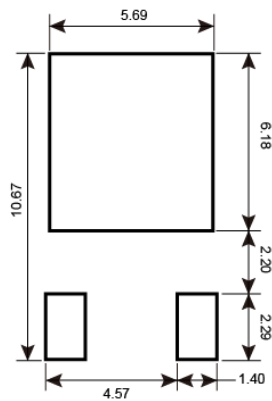
- Y** = Year Code
- M** = Month Code for Halogen Free Product
  - O** =Jan    **P** =Feb    **Q** =Mar    **R** =Apr
  - S** =May    **T** =Jun    **U** =Jul    **V** =Aug
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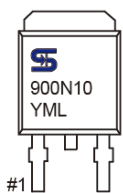
**TO-252 (DPAK)**



**SUGGESTED PAD LAYOUT**



**MARKING DIAGRAM**



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