

# NSR02F30MXT5G

## 200 mA, 30 V Schottky Barrier Diode

These Schottky barrier diodes are optimized for low forward voltage drop and low leakage current that offers the most optimal power dissipation in applications. They are housed in a spacing saving x3DFN 0201 package ideal for space constraint applications.

### Features

- Low Forward Voltage Drop – 500 mV (Typ.) @  $I_F = 200$  mA
- Low Reverse Current – 20  $\mu$ A (Typ.) @  $V_R = 30$  V
- 200 mA of Continuous Forward Current
- ESD Rating – Human Body Model: Class 2  
– Machine Model: Class M3  
– CDM: Class IV
- High Switching Speed
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

### Typical Applications

- LCD and Keypad Backlighting
- Camera Photo Flash
- Buck and Boost dc-dc Converters
- Reverse Voltage and Current Protection
- Clamping and Protection

### MAXIMUM RATINGS

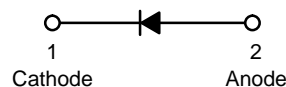
Rating	Symbol	Value	Unit
Reverse Voltage	$V_R$	30	V
Forward Current (DC)	$I_F$	200	mA
Forward Surge Current (60 Hz @ 1 cycle)	$I_{FSM}$	2	A
Repetitive Peak Forward Current (Pulse Wave = 1 sec, Duty Cycle = 66%)	$I_{FRM}$	1	A
ESD Rating: Human Body Model Machine Model	ESD	2 – 4 >400	kV V

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



ON Semiconductor®

[www.onsemi.com](http://www.onsemi.com)



### MARKING DIAGRAM



X3DFN2  
CASE 152AF



J = Specific Device Code  
(Rotated 180°)  
M = Month Code

### ORDERING INFORMATION

Device	Package	Shipping†
NSR02F30MXT5G	X3DFN (Pb-Free)	15000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# NSR02F30MXT5G

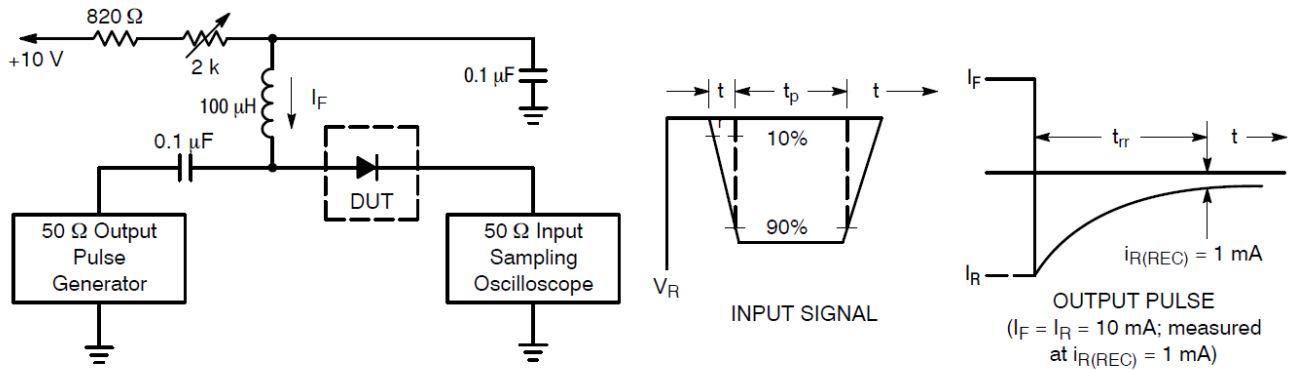
**Table 1. THERMAL CHARACTERISTICS**

Rating	Symbol	Max	Unit
Thermal Resistance Junction-to-Ambient (Note 1) Total Power Dissipation @ $T_A = 25^\circ\text{C}$	$R_{\theta JA}$ $P_D$	695 180	$^\circ\text{C/W}$ mW
Storage Temperature Range	$T_{stg}$	-55 to +125	$^\circ\text{C}$
Junction Temperature	$T_J$	+125	$^\circ\text{C}$

1. Mounted onto a 4 in square FR-4 board 100 mm sq. 2 oz. Cu 0.06" thick single sided. Operating to steady state.

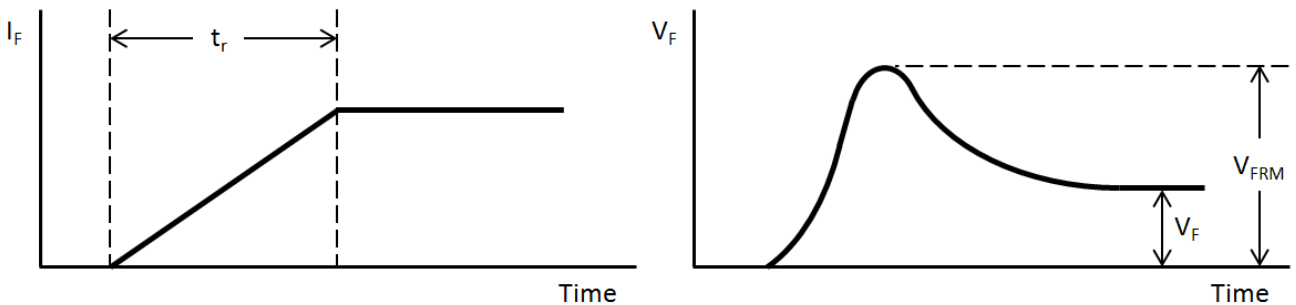
**Table 2. ELECTRICAL CHARACTERISTICS** ( $T_A = 25^\circ\text{C}$  unless otherwise noted)

Parameter	Test Conditions	Symbol	Min	Typ	Max	Unit
Reverse Leakage	$V_R = 10\text{ V}$	$I_R$	-	-	15	$\mu\text{A}$
Reverse Leakage	$V_R = 30\text{ V}$	$I_R$	-	20	50	$\mu\text{A}$
Forward Voltage	$I_F = 1\text{ mA}$	$V_F$	-	155		mV
Forward Voltage	$I_F = 10\text{ mA}$	$V_F$	-	250	290	mV
Forward Voltage	$I_F = 100\text{ mA}$	$V_F$	-	375	490	mV
Forward Voltage	$I_F = 200\text{ mA}$	$V_F$	-	500	600	mV
Total Capacitance	$V_R = 1.0\text{ V}$ , $f = 1.0\text{ MHz}$	$C_T$	-	6	8	pF
Reverse Recovery Time	$I_F = I_R = 10\text{ mA}$ , $I_{R(REC)} = 1.0\text{ mA}$ , Figure 2	$t_{rr}$	-	2.4	3	ns



Notes: 1. A 2.0 k $\Omega$  variable resistor adjusted for a Forward Current ( $I_F$ ) of 10 mA.  
 2. Input pulse is adjusted so  $I_{R(\text{peak})}$  is equal to 10 mA.  
 3.  $t_p \gg t_{rr}$

**Figure 1. Recovery Time Equivalent Test Circuit**



**Figure 2. Peak Forward Recover Voltage Definition**

# NSR02F30MXT5G

## TYPICAL CHARACTERISTICS

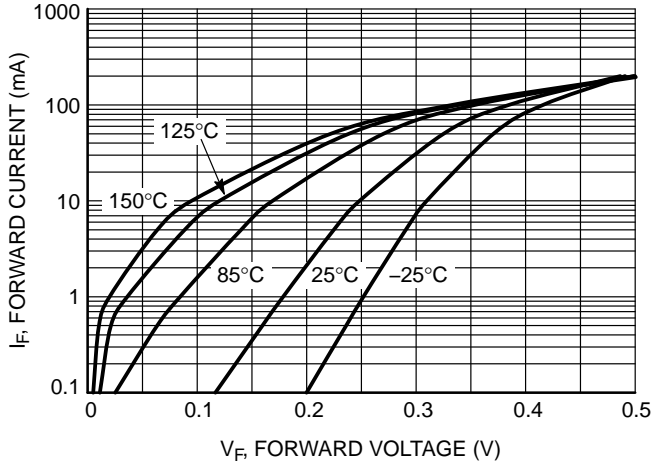


Figure 3. Forward Voltage

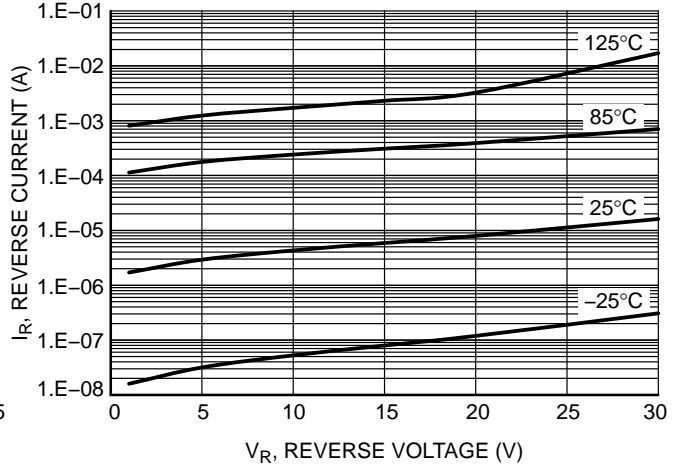


Figure 4. Leakage Current

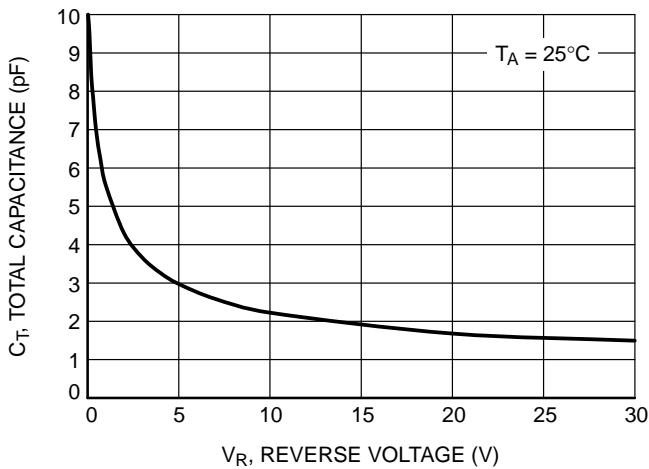


Figure 5. Total Capacitance

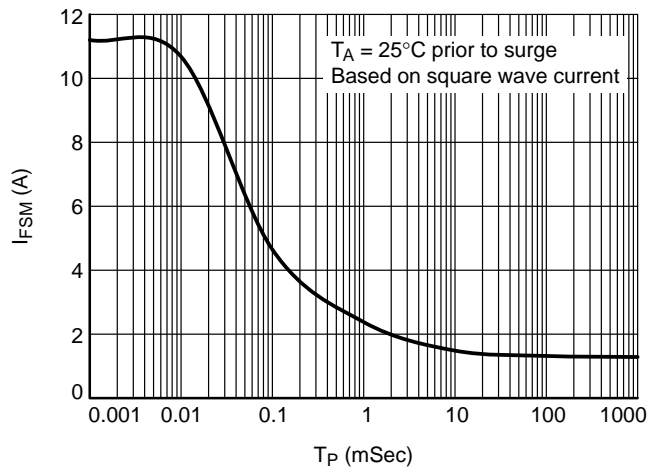


Figure 6. Forward Surge Current

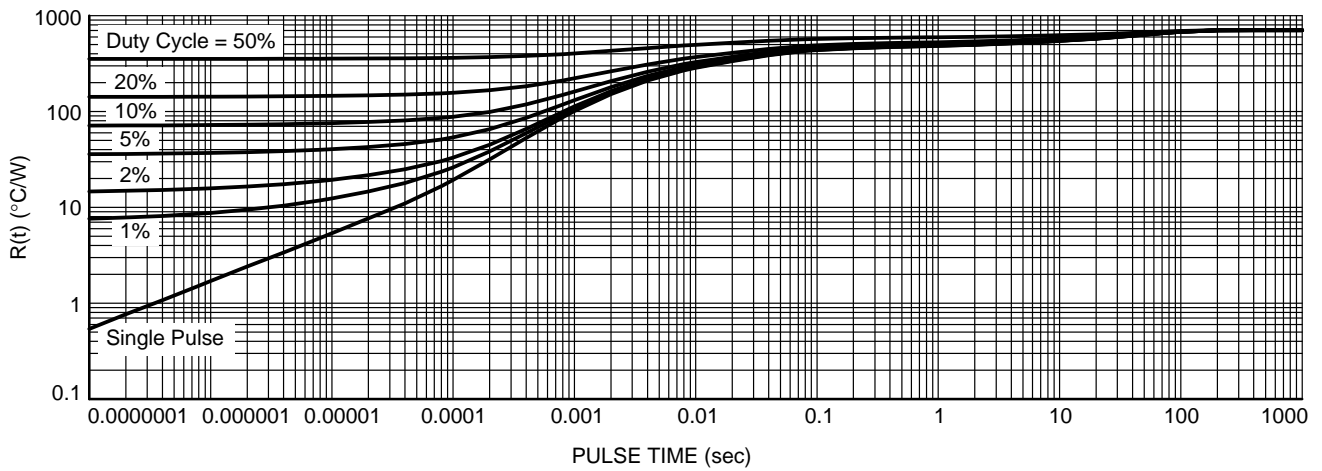
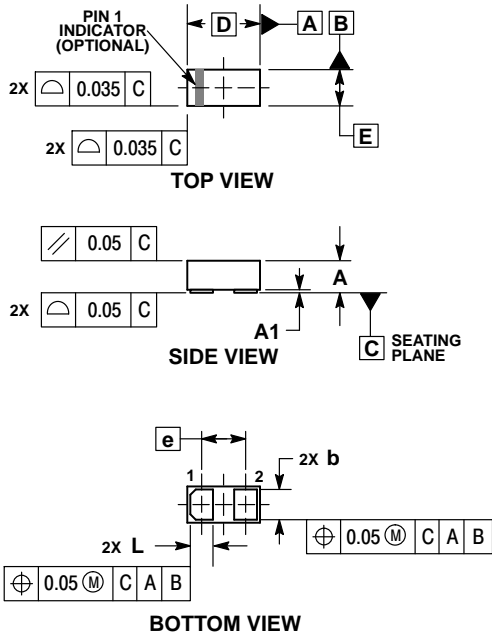


Figure 7. Thermal Response

# NSR02F30MXT5G

## PACKAGE DIMENSIONS

**X3DFN2, 0.62x0.32, 0.355P, (0201)**  
**CASE 152AF**  
**ISSUE O**

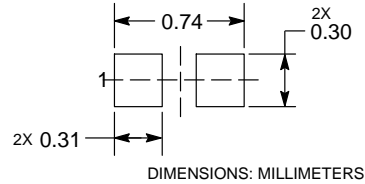


**NOTES:**

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.

MILLIMETERS		
DIM	MIN	MAX
A	0.25	0.33
A1	---	0.05
b	0.22	0.28
D	0.62 BSC	
E	0.32 BSC	
e	0.355 BSC	
L	0.17	0.23

**RECOMMENDED MOUNTING FOOTPRINT\***



See Application Note AND8398/D for more mounting details  
 \*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and the are registered trademarks of Semiconductor Components Industries, LLC (SCILLC) or its subsidiaries in the United States and/or other countries. SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at [www.onsemi.com/site/pdf/Patent-Marketing.pdf](http://www.onsemi.com/site/pdf/Patent-Marketing.pdf). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

**PUBLICATION ORDERING INFORMATION**

**LITERATURE FULFILLMENT:**  
 Literature Distribution Center for ON Semiconductor  
 P.O. Box 5163, Denver, Colorado 80217 USA  
**Phone:** 303-675-2175 or 800-344-3860 Toll Free USA/Canada  
**Fax:** 303-675-2176 or 800-344-3867 Toll Free USA/Canada  
**Email:** [orderlit@onsemi.com](mailto:orderlit@onsemi.com)

**N. American Technical Support:** 800-282-9855 Toll Free  
 USA/Canada  
**Europe, Middle East and Africa Technical Support:**  
 Phone: 421 33 790 2910  
**Japan Customer Focus Center**  
 Phone: 81-3-5817-1050

**ON Semiconductor Website:** [www.onsemi.com](http://www.onsemi.com)  
**Order Literature:** <http://www.onsemi.com/orderlit>  
 For additional information, please contact your local Sales Representative