MINED BY :		FILE NO . CAS-0006811
Vincent Uh	EMERGING DISPLAY	ISSUE : FEB.11, 2010
OVED BY:	TECHNOLOGIES CORPORATION	TOTAL PAGE: 28
David Chang		VERSION : 5
CUSTOMER	ACCEPTANCE SPEC	CIFICATIONS
	DEL NO.: <u>ETQ570G2DH6</u> (RoHS) MESSRS:	
DATE :		
BY :		

EMERG	ING D	ISPLAY	MODEL NO.	VERSION	PAGE
	OGIES CORI		E T Q 5 7 0 G 2 D H 6	5	0-1
			DOC . FIRST ISSUE		
RECORD		EVISION		J	AN.23, 2009
D A T E	REVISED PAGE NO.		S U M M A R Y		
MAR.04, 2009	1		SPECIFICATIONS - 16.7M \rightarrow 262K		
	3		CHARACTERISTICS		
		PARAMETER POWER SUPPLY CURREN FOR VCOM DRIVER PARAMETER	SYMBOL CONDITION MIN. TYP. MAX. UNI IT ICC -3.3V - 630 720 m/ LED B/L=ON - 530 720 m/ SYMBOL CONDITION MIN. TYP. MAX. UNI		
		POWER SUPPLY CURREN	NCC VEC		
	8	8. BLOCK DIAGR ADD FRAME G			
	12	11.1 POWER SUP			
		VDD	VDD		
		TFT LCD		3.3V	
		vss			
		vcc —	3.3V VCOM VSS CIRCUIT LEDCTRL	3.3V 0~2.5V	
		VCOM VSS CIRCUIT LEDCTRL	0-4.0V PWCTRL	ON OFF	
					ТОГ
		LEDCTRL	NESS CONTROLLED BY BACKLIC	HI CUKKEN	I OF
		45 40 (YU) huaano hu hitin 15 5 0 0 0.5 1		CLIGHT CURRENT VS. LEDCTRL	25
APR.15, 2009	7	7. OUTLINE DIM MARK	ENSIONS DIFY CN1 TYPE		
AUG.21, 2009	3	4. ELECTRICAL O	CHARACTERISTICS Y CURRENT FOR VCOM DRIVER:		
		$TYP.=(630) \rightarrow 4$	$50, MAX.=(720) \rightarrow 580$		
FEB.10, 2010	2		NTAL ABSOLUTE MAXIMUM RAT		v
		I T E M AMBIENT TEMPER	MIN. MAX. MIN. MA		
		ITEM	↓ OPERATING STORAGE MIN. MAX. MIN. MA	x REMAR	K
		AMBIENT TEMPER			2)
		NOTE (1) : Ta A	$T - 20^{\circ}C \rightarrow -30^{\circ}C ; 70^{\circ}C \rightarrow 80^{\circ}C$		

EMERG	ING DI	SPLAY	MODEL NO.	VERSION	PAGE
	OGIES CORPO		E T Q 5 7 0 G 2 D H 6	5	0-2
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D A T E	REVISED PAGE NO.		S U M M A R Y		
FEB.11, 2010		13.4.1 STANDAR	D SPECIFICATIONS FOR RELIABILI	TY OF LCD N	MODULE
		NO ITEM HIGH	DESCRIPTION		
		1 TEMPERATU OPERATION		FOR 240	
		2 LOW 2 TEMPERATU OPERATION		FOR 240	
		3 TEMPERATU STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +70°C	FOR 240	
		4 LOW 4 TEMPERATUI STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -20 240 HRS	°C FOR	
		NO ITEM	↓ DESCRIPTION		
		1 TEMPERATU OPERATION		FOR 240	
		2 LOW 2 TEMPERATUR OPERATION	RE THE SAMPLE SHOULD BE ALLOWED TO STAND AT -20°C	FOR 240	
		3 TEMPERATU STORAGE	RE THE SAMPLE SHOULD BE ALLOWED TO STAND AT +80°C HRS	FOR 240	
		4 LOW 4 TEMPERATUI STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -30 240 HRS	°C FOR	

TECHNO	CHNOLOGIES CORPORATION E T Q 5 7 0 G 2 D H 6							
	TABLE OF CONT	<u>`ENTS</u>						
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	ING DISPLAY	MODEL NO.		VERSION	PAGE	
TECHNOI	LOGIES CORPORATION	E T Q 5 7 0 G 2 D I	H 6	5	1	
1. GEI	NERAL SPECIFICATIONS					
	APPLICATION NOTES FOR PLEASE REFER TO :	CONTROLLER/DRIVER				
		X H X 8 2 1 8 X H X 8 6 1 5				
]	MATERIAL SAFETY DESCRI ASSEMBLIES SHALL COMPI INCLUDING PROHIBITED M MERCURY, CADMIUM, HEX BIPHENYLS (PBB) AND POL DIPHENYL ETHERS (PBDE)	LY WITH EUROPEAN RO ATERIALS/COMPONEN AVALENT CHROMIUM, YBROMINATED	LS CON	ITAINING	LEAD,	
2. ME	CHANICAL SPECIFICATION	JS				
(1)	DIAGONALS	5.7 inch				
(2)	NUMBER OF DOTS	320W * (RGE	320W * (RGB) * 240H DOTS			
(3)	MODULE SIZE	142.1W * 100	.4H * 12	.5D(MAX.)	mm	
		(WITHOUT I	FPC)			
(4)	EFFECTIVE AREA	117.2W * 88.4	4H mm (T/P)		
(5)	ACTIVE AREA	115.2W * 86.4	4H mm (LCD)		
		116.2W * 87.4	4H mm (T/P)		
(6)	DOT SIZE	0.12W * 0.36	H mm			
(7)	PIXEL SIZE	0.36W * 0.36	H mm			
(8)	LCD TYPE	TFT , TRANS	SMISSIV	E		
(9)	COLOR	262K				
	VIEWING DIRECTION	6 O'CLOCK				
(10)	BACK LIGHT	LED, COLO	R : WHIT	Έ		
	DACK LIGHT			Ľ		

E M E R G I N GD I S P L A Y
TECHNOLOGIES CORPORATIONMODEL NO.VERSIONE T Q 5 7 0 G 2 D H 65

3. ABSOLUTE MAXIMUM RATINGS

3.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS.

PARAMETER	SYMBOL	MIN.	MAX.	UNIT	REMARK
POWER SUPPLY VOLTAGE	VDD-VSS	-0.3	7.0	V	
FOWER SUFFLY VOLTAGE	VCC-VSS	-0.3	7.0	V	
INPUT SIGNAL VOLTAGE	VL-VSS	-0.3	VCC+0.3	V	
STATIC ELECTRICITY				V	NOTE (1)
LED BACKLIGHT POWER DISSIPATION	PD		1.28	W	
LED BACKLIGHT FORWARD CURRENT	IF		0.06	А	
LED BACKLIGHT REVERSE VOLTAGE	VR		45	V	

NOTE ($1\)\ :\ LCM$ SHOULD BE GROUNDED DURING HANDING LCM.

3.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS.

ITEM	OPERA	ATING	STOP	RAGE	REMARK	
	MIN.	MIN. MAX. MIN		MAX.	KEWIAKK	
AMBIENT TEMPERATURE	-20°C	70°C	-30°C	80°C	NOTE (1), (2)	
HUMIDITY	NOTE (3)		NOTE(3)		WITHOUT CONDENSATION	
VIBRATION		2.45 m/s ² (0.25 G)		11.76 m/s ² (1.2 G)	5~20Hz, 1HR 20~500Hz(20Hz), 1HR 20~500Hz(500Hz), 1HR X, Y, Z, TOTAL 3HRS	
SHOCK		29.4 m/s ² (3 G)		490 m/s ² (50G)	10 m SECONDS XYZ DIRECTIONS 1 TIME EACH	
CORROSIVE GAS	NOT ACC	EPTABLE	NOT ACC	EPTABLE		

NOTE (1) : Ta AT -30°C : 48HRS MAX .

80°C: 168HRS MAX.

NOTE (2) : BACKGROUND COLOR CHANGES SLIGHTLY DEPENDING ON AMBIENT TEMPERATURE THIS PHENOMENON IS REVERSIBLE .

NOTE (3) $\ : \ Ta \leq 60^{\circ}C$: 90%RH $\ MAX$ (96HRS MAX).

Ta > 60°C : ABSOLUTE HUMIDITY MUST BE LOWER THAN THE HUMIDITY OF 90%RH AT 60°C(96HRS MAX).

E M E R G I N GD I S P L A YMODEL NO.VERSIONPAGETECHNOLOGIES CORPORATIONE T Q 5 7 0 G 2 D H 653

4. ELECTRICAL CHARACTERISTICS

							$Ta = 25 \circ C$	
PARAMETER	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK	
POWER SUPPLY VOLTAGE FOR DIGITAL	VDD-VSS	—	3	3.3	3.6	V		
POWER SUPPLY VOLTAGE FOR VCOM DRIVER	VCC-VSS	_	3	3.3	3.6	V		
POWER SUPPLY CURRENT FOR DIGITAL	IDD	VDD-VSS =3.3V		8	11	mA	NOTE (1)	
POWER SUPPLY CURRENT FOR VCOM DRIVER	ICC	VCC-VSS = 3.3V LED B/L=ON		450	580	mA		
LOW LEVEL INPUT VOLTAGE	VIL	_	0		0.3*VDD	V	NOTE (2)	
HIGH LEVEL INPUT VOLTAGE	VIH	_	0.7*VDD		VDD	V	NOTE (2)	
LOW LEVEL OUTPUT VOLTAGE	VOL	$IOL = 400 \mu A$	0		0.2*VDD	V	NOTE (2)	
HIGH LEVEL OUTPUT VOLTAGE	VOH	$IOH = -400 \mu A$	0.8*VDD		VDD	V	NOTE (3)	
FRAME FREQUENCY	fFRAME			83	92	Hz		
DOT DATA CLOCK	DCLK			6.4	7.1	MHz		
POWER SUPPLY FOR LED BACKLIGHT	$V_{\rm F}$	I _F =40mA	28	30	32	V	NOTE (4)	
LED LIFE TIME			30000	40000		HRS		

NOTE (1): THE DISPLAY PATTERN IS ALL "WHITE".

NOTE (2): APPLIED TO TERMINALS /RESET, HSYNC, VSYNC, ENB, DCLK, B5~B0, G5~G0, R5~R0.

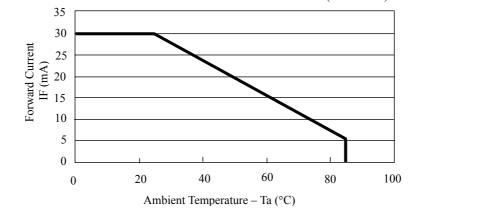
NOTE (3): APPLIED TO TERMINALS B5~B0, G5~G0, R5~R0.

NOTE (4): INTERNAL CIRCUIT DIAGRAM OF BACKLIGHT

(VF=VBL+(A)-VBL1-(K1)=VBL+(A)-VBL2-(K2))



NOTE (5): AMBIENT TEMP. VS. ALLOWABLE FORWARD CURRENT.(PER LED)



 $T_0 = 25 \circ C$

MODEL NO. VERSION PAGE EMERGING DISPLAY TECHNOLOGIES CORPORATION ETQ570G2DH6 5 4 TIMING CHARACTERISTICS 5. 5.1 DIGITAL PARALLEL RGB INTERFACE SIGNAL ITEM SYMBOL MIN. TYP. MAX. UNIT FREQUENCY TCLK 6.4 7.1 MHz ____ DCLK TCH HIGH TIME 78 ____ ____ ns LOW TIME TCL 78 ____ ns SETUP TIME TDS 12 ____ ns ____ DATA HOLD TIME TDH 12 ns TH 408 DCLK PERIOD PULSE WIDTH THP 30 DCLK HSYNC BACK-PORCH THB 38 DCLK ____ ____ DISPLAY PERIOD THD 320 DCLK ____ ____ FRONT-PORCH THF 20 ____ DCLK _____ NTSC 262.5 PERIOD ΤV TH PAL 312.5 PULSE WIDTH TVP 1 3 5 TH BACK-PORCH NTSC 15 VSYNC TVB TH ____ BACK-PORCH PAL 23

TVD

TVF

NTSC

PAL

DISPLAY PERIOD

FRONT-PORCH

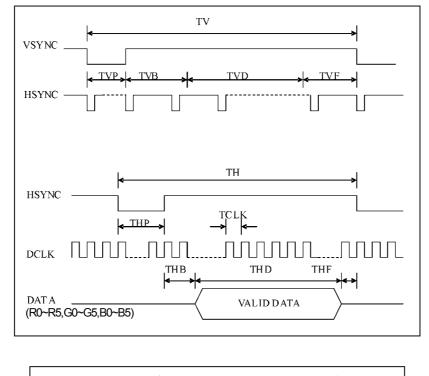
240

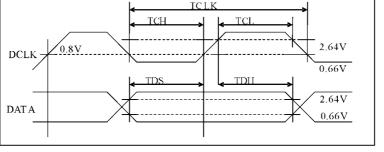
4.5

46.5

TH

TH





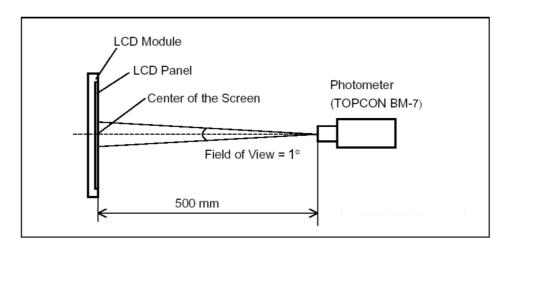
MODEL NO . E T Q 5 7 0 G 2 D H 6

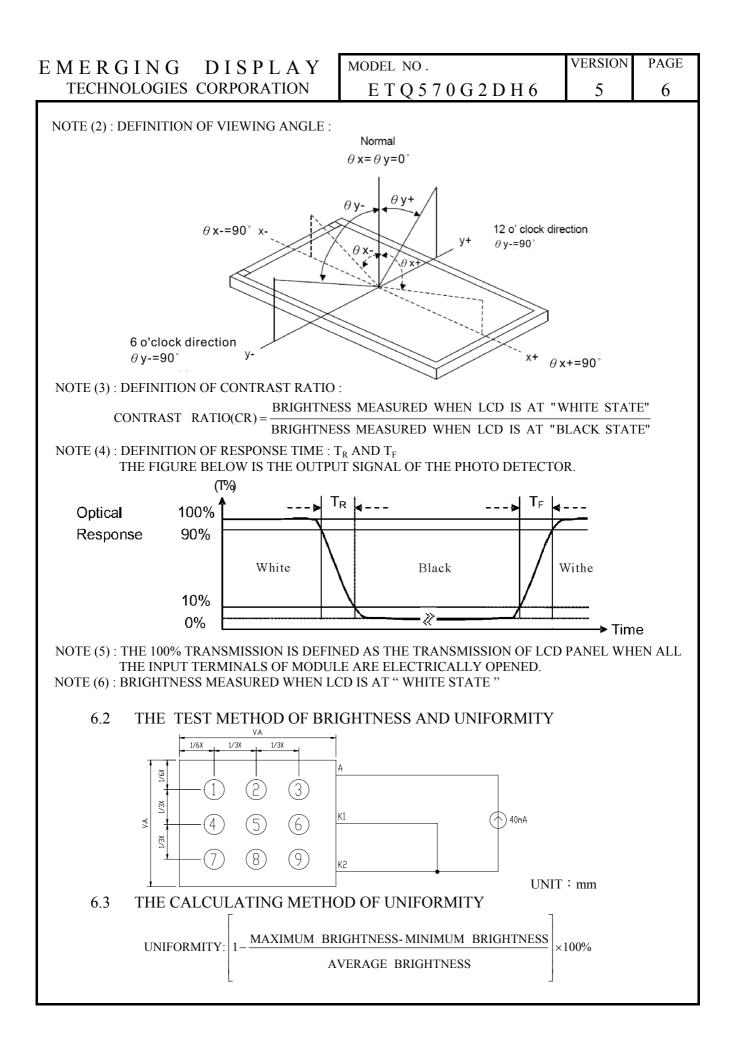
6. OPTICAL CHARACTERISTICS (NOTE 1) 6.1 OPTICAL CHARACTERISTICS

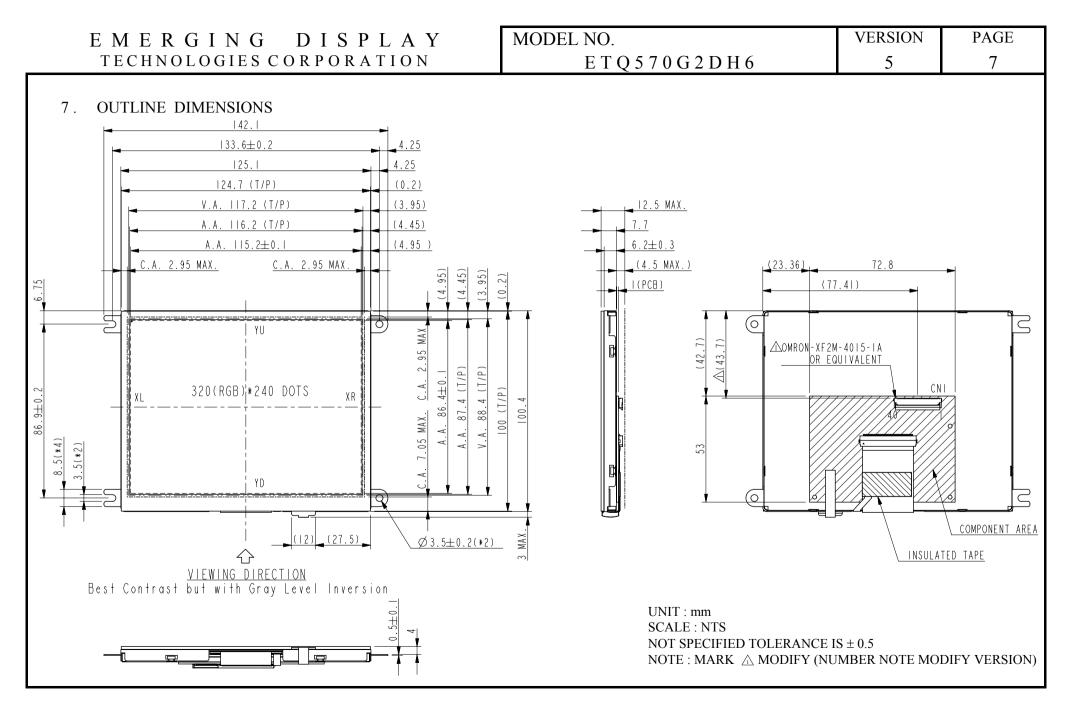
								Та	$= 25 \pm 2 \circ C$	
ΙΤΕΜ		SYMBOL	COND	ITION	MIN.	TYP.	MAX.	UNIT	REMARK	
		$\theta_{\mathbf{y}^+}$		θ _x =0°	55	60				
VIEWING ANGL	Б	θ_{y}	$CR \ge 10$	$\theta_x = 0$	70	75		daa	NOTE (2)	
VIE WING ANGL	E.	θ_{x^+}	$CK \ge 10$	$CK \ge 10$	0 -0%	70	75		deg.	NOTE (3)
		θ_{x}		$\theta_y=0^{\circ}$	70	75				
CONTRAST RAT	ΠO	CR	θx=0°,	θy=0°	300	400			NOTE (3)	
RESPONSE TIME	7	T_{R} (rise)	009	009		15	30	m 600	NOTE (4)	
KESPONSE HIVI	2	$T_F(fall)$	$\theta x=0^{\circ}, \ \theta y=0^{\circ}$			35	50	msec	NOTE (4)	
	WHITE	Wx			0.27	0.32	0.37			
		Wy			0.30	0.35	0.40			
	RED	Rx				0.58	0.63	0.68		
COLOR OF		Ry	$\theta x=0^{\circ}$,		0.31	0.36	0.41		NOTE (7)	
CIE COORDINATE	GREEN	Gx	$I_F = 4$ NTSC	0mA 2 [.] 60%	0.28	0.33	0.38		NOTE (5)	
COORDINATE	GREEN	Gy			0.55	0.60	0.65			
	DLUE	Bx			0.09	0.14	0.19			
	BLUE	Ву			0.06	0.11	0.16			
THE BRIGHTNESS		В			360	400		cd/m ²		
OF MODULE		D		θy=0°	300	400		Cu/III	NOTE (6)	
THE UNIFORMITY OF MODULE			I _F =4	0mA	75	80		%	NOTE (0)	

NOTE (1) : TEST EQUIPMENT SETUP :

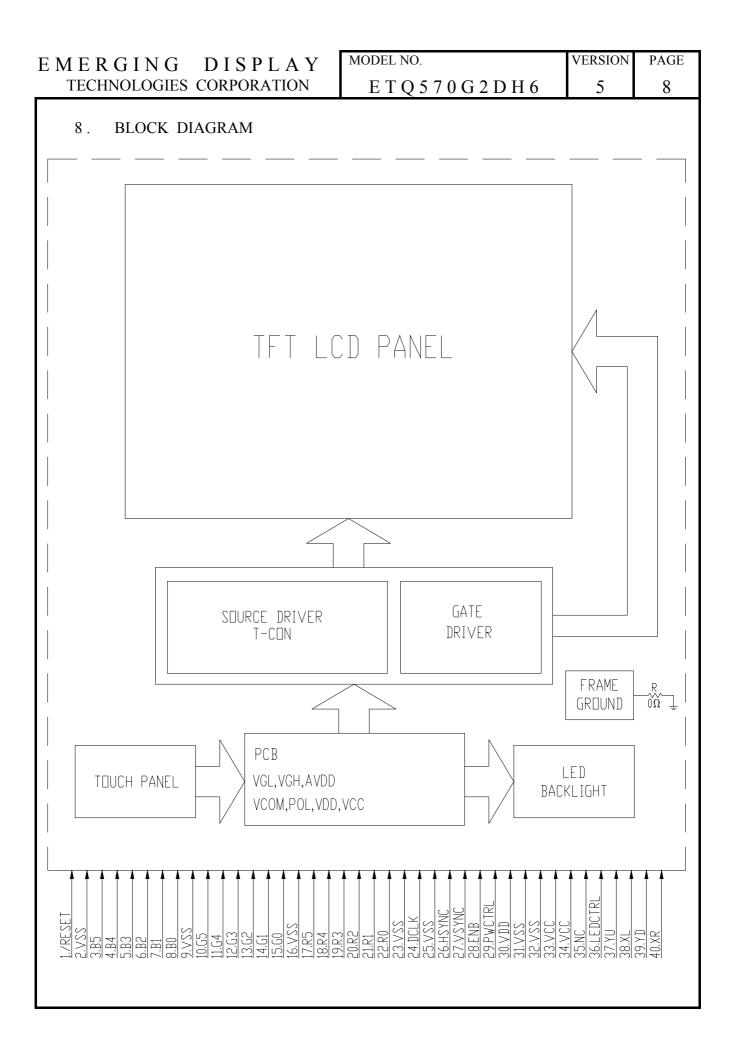
AFTER STABILIZING AND LEAVING THE PANEL ALONE AT A GIVEN TEMPERATURE FOR 30 MINUTES, THE MEASUREMENT SHOULD BE EXECUTED. MEASUREMENT SHOULD BE EXECUTED IN A STABLE, WINDLESS, AND DARK ROOM. OPTICAL SPECIFICATIONS ARE MEASURED BY TOPCON BM-7 (FAST) WITH A VIEWING ANGLE OF 1° AT A DISTANCE OF 50cm AND NORMAL DIRECTION.

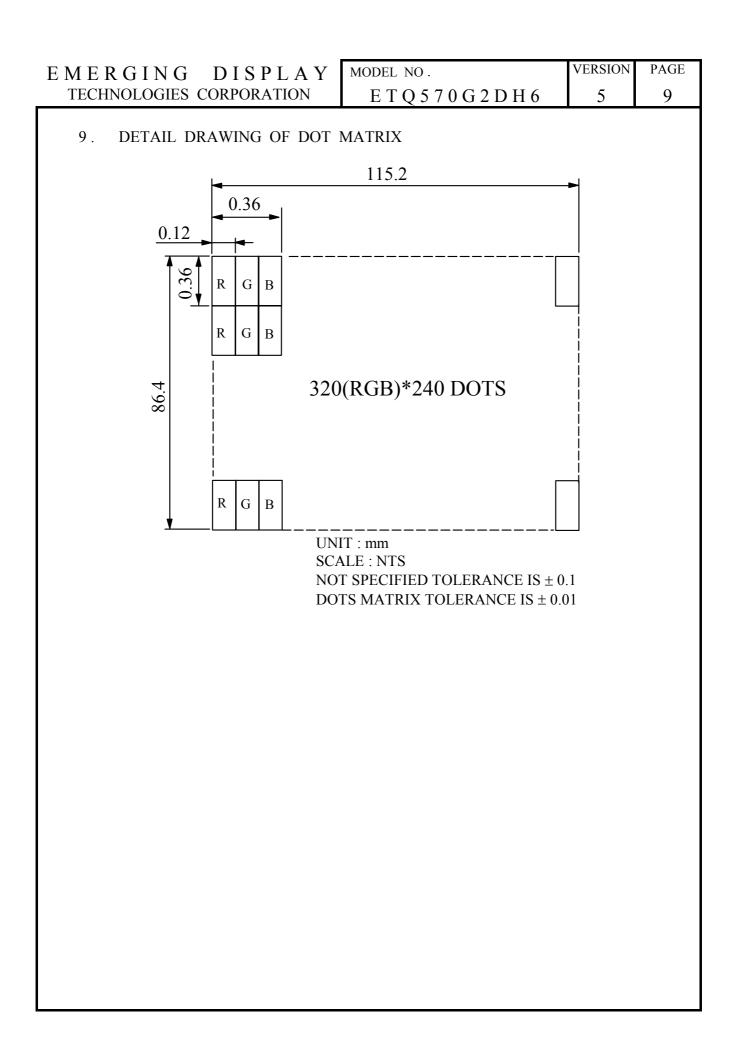






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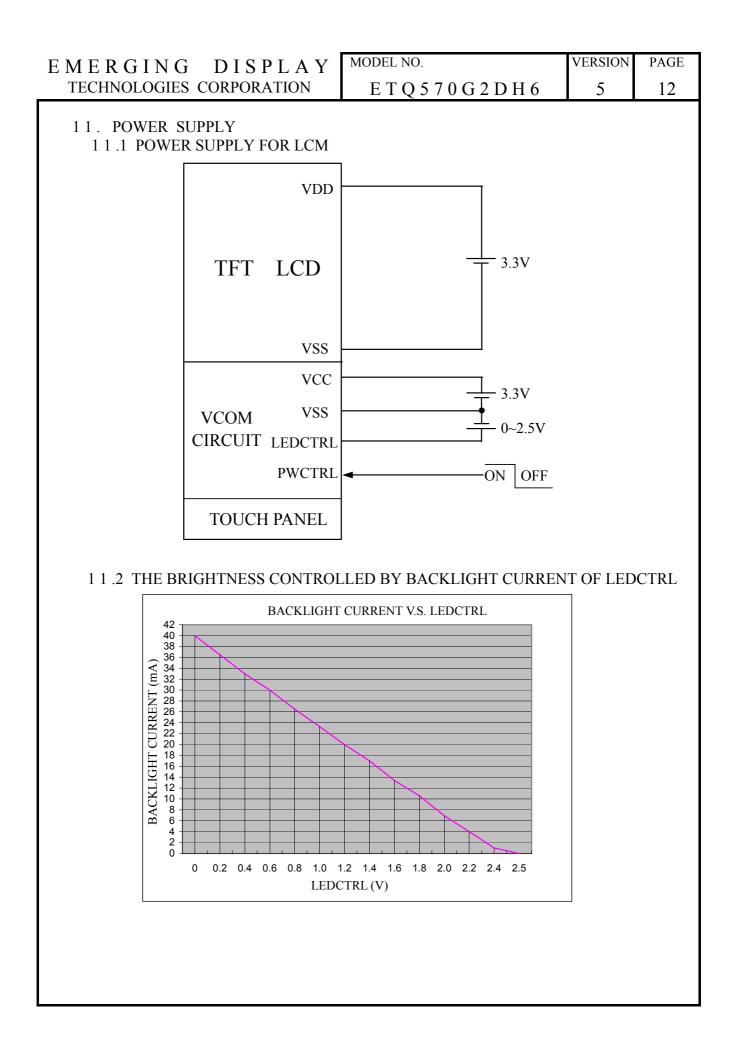
 E T Q 5 7 0 G 2 D H 6
 5

10. INTERFACE SIGNALS

PIN NO	SYMBOL	I/O	FUNCTION
1	/RESET	Ι	HARDWARE RESET
2	VSS	Р	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)
3	B5	Ι	BLUE DATA BIT 5
4	B4	Ι	BLUE DATA BIT 4
5	В3	Ι	BLUE DATA BIT 3
6	B2	Ι	BLUE DATA BIT 2
7	B1	Ι	BLUE DATA BIT 1
8	B0	Ι	BLUE DATA BIT 0
9	VSS	Р	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)
10	G5	Ι	GREEN DATA BIT 5
11	G4	Ι	GREEN DATA BIT 4
12	G3	Ι	GREEN DATA BIT 3
13	G2	Ι	GREEN DATA BIT 2
14	G1	Ι	GREEN DATA BIT 1
15	G0	Ι	GREEN DATA BIT 0
16	VSS	Р	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)
17	R5	Ι	RED DATA BIT 5
18	R4	Ι	RED DATA BIT 4
19	R3	Ι	RED DATA BIT 3
20	R2	Ι	RED DATA BIT 2
21	R1	Ι	RED DATA BIT 1
22	R0	Ι	RED DATA BIT 0
23	VSS	Р	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)
24	DCLK	Ι	DOT DATA CLOCK
25	VSS	Р	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)
26	HSYNC	Ι	HORIZONTAL SYNC INPUT
27	VSYNC	Ι	VERTICAL SYNC INPUT
28	ENB	Ι	DATA ENABLE INPUT

MODEL NO . E T Q 5 7 0 G 2 D H 6

PIN NO	SYMBOL	I/O		FUNC	TION				
				PWCT	RL	REMARK			
			LOGIC LEVEL	Н		POWER ON			
29	PWCTRL	Ι	H=3.3V L=0V	L		SHUTDOWN			
			WHEN INTERNAL LE	D DRIVER : J	P15 1-2(DEF.	AULT)			
			WHEN EXTERNAL LE	D DRIVER : .	JP15 2-3				
30	VDD	Р	POWER SUPPLY FOR	DIGITAL CIF	RCUIT				
31	VSS	Р	GROUND (VSS IS CON CONDUCTIVE TAPE)	INECTED TO	METAL HO	USING WITH			
32	VSS	Р	GROUND (VSS IS CON CONDUCTIVE TAPE)	INECTED TO	METAL HO	USING WITH			
33	VCC	Р	POWER SUPPLY FOR	POWER SUPPLY FOR VCOM DRIVER CIRCUIT					
34	VCC	Р	POWER SUPPLY FOR	POWER SUPPLY FOR VCOM DRIVER CIRCUIT					
35	NC		(USING EXTERNAL L WHEN INTERNAL LE	NON CONNECTION (USING INTERNAL LED DRIVER) OR ANODE (USING EXTERNAL LED DRIVER) WHEN INTERNAL LED DRIVER : JP5 1-2 (DEFAULT) WHEN EXTERNAL LED DRIVER : JP5 2-3					
36	LEDCTRL	Ι	LEDCTRL (USING INT EXTERNAL LED DRIV WHEN INTERNAL LE	WHEN EXTERNAL LED DRIVER : JPS 2-3 BRIGHTNESS CONTROL FOR LED BACKLIGHT ; LEDCTRL (USING INTERNAL LED DRIVER) OR CATHODE (USING EXTERNAL LED DRIVER) WHEN INTERNAL LED DRIVER : JP6 1-2 (DEFAULT) JP14 1-2 (DEFAULT) WHEN EXTERNAL LED DRIVER : JP6 2-3					
37	YU		TOP PANEL			ONNECTION			
38	XL		LEFT PANEL						
39	YD		BOTTOM PANEL		TOUCH PAN	NEL			
40	XR		RIGHT PANEL						



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12. TOUCH PANEL SPECIFICATION 12.1 ELECTRICAL CHARACTERISTICS

 $Ta = 25^{\circ}C$

			1a 25 C
ITEM	CONDITION	SPEC.	UNIT
LINEARITY		≤ 1.5	%
TRANSMISSION	ASTM D1003	80 OR MORE	%
ON LOAD	POLYACETAL PEN INPUT	15~80	g
TERMINAL RESISTANCE	X AXIS	$400 \sim 1000$	Ω
I ERMINAL RESISTANCE	Y AXIS	$200 \sim 700$	52
INSULATION RESISTANCE	DC25V	≥ 10	MΩ
INPUT VOLTAGE		5	V

12.2 PRECAUTIONS IN USE OF TOUCH PANEL

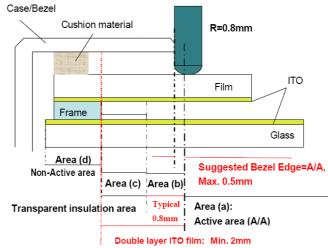
12.2.1 PURPOSE :

IN ORDER TO PREVENT ACCIDENTAL USE AND PERFORMANCE DETERIORATION, PLEASE KEEP THE FOLLOWING PRECAUTIONS AND INHIBITED POINTS.

12.2.2 ITEM AND ILLUSTRATION :

(1) STRUCTURE, AREA DEFINITION

THE STRUCTURE AND THE PERFORMANCE GUARANTEED AREA OF THIS TOUCH PANEL ARE DEFINED BELOW :



Single layer ITO film: Min. 3mm

THE ABOVE FIGURE IS OUR DESIGN RULE OF TOUCH PANEL. IF IT CANNOT MEET YOUR REQUIREMENT, PLEASE CONTACT WITH OUR ENGINEERS FOR FURTHER DISCUSSION.

ABOVE FIGURE ILLUSTRATES THE RECOMMENDED BEZEL AND CUSHION DESIGN. IN ORDER TO PREVENT UNUSUAL PERFORMANCE DEGRADATION AND MALFUNCTION OF A TOUCH PANEL, PLEASE CARRY OUT THE SET CASE DESIGNING AND A TOUCH PANEL ASSEMBLING METHOD AFTER SURELY CONSIDERING THE DEFINITION OF EACH AREA ILLUSTRATED IN ABOVE FIGURE.

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PRECISION, OPERATION FO STRONGLY RECOMMENDE MENU KEYS WITHIN THE A	RANTEED THE POSITION DATA RCE AND OTHER OPERATIONS D TO PLACE THE OPERATION F CTIVE AREA. DUE TO STRUCT ABLE AT THE EDGE OR CLOSE	5. IT IS BUTTON (URE, THE	DR

AREA(b) : OPERATION NON-GUARANTEED AREA THIS AREA DOES NOT GUARANTEE A TOUCH PANEL OPERATION AND ITS FUNCTION. WHEN THIS AREA IS PRESSED, TOUCH PANEL SHOWS DEGRADATION OF ITS PERFORMANCE AND DURABILITY SUCH AS A PEN SLIDING DURABILITY BECOMES ABOUT ONE-TENTH COMPARED WITH THE ACTIVE AREA (AREA-(A) AS GUARANTEED AREA) AND ITS OPERATION FORCE REQUIRES ABOUT DOUBLE. ABOUT 0.5 MM OUTSIDE FROM A BOUNDARY OF THE ACTIVE AREA CORRESPONDS TO THIS AREA.

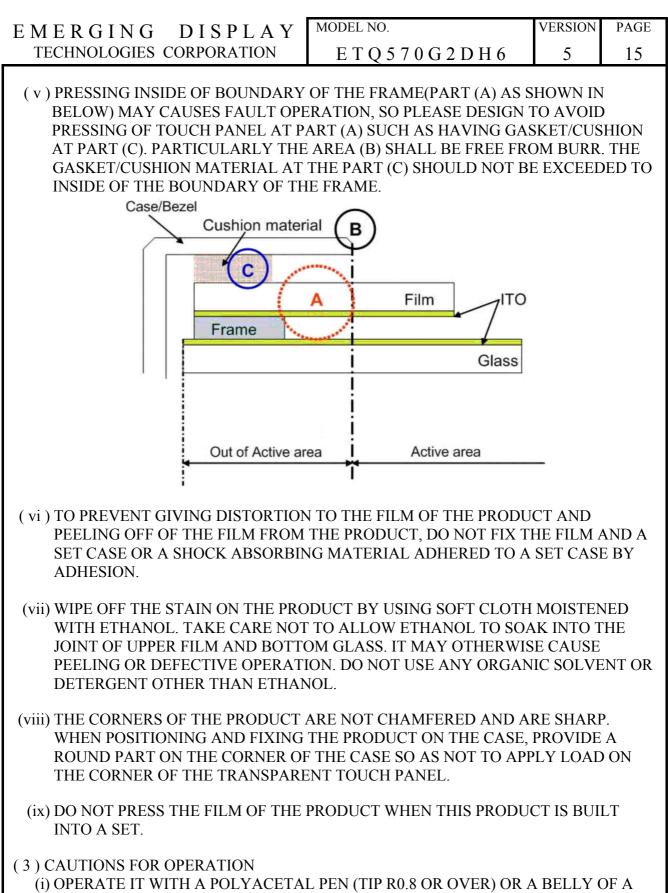
AREA(c) : PRESSING PROHIBITION AREA THE AREA WHICH FORBIDS PRESSING, BECAUSE AN EXCESSIVE LOAD IS APPLIED TO A TRANSPARENT ELECTRODE (ITO) AND A SERIOUS DAMAGE IS GIVEN TO A TOUCH PANEL FUNCTION BY PRESSING.

- AREA(d) : NON-ACTIVE AREA THE AREA DOES NOT ACTIVATE EVEN IF PRESSED.
- (2) CAUTIONS FOR INSTALLING AND ASSEMBLING(i) DO NOT GIVE EXCESSIVE STRAIN TO THE PRODUCT.

(ii) FLEXIBLE PATTERN CABLE IS CONNECTED TO THE BODY BY THERMAL PRESSURE METHOD. SO, DO NOT APPLY EXCESSIVE FORCES TO THE FLEXIBLE PATTERN. DO NOT ADD AN EXCESSIVE FORCE TO A FPC(FLEX TAIL) THAT MAKES PEELING OFF OF THE FPC FROM THE PRODUCT. DO NOT FIX, ADHERE OR MOUNT ANY ADDITIONAL GOODS ON THE FPC SUCH AS ADDITIONAL FILM/PLATE ON THE FPC, BECAUSE SUCH ADDITIONAL GOODS WILL APPLY A STRESS AT THE FPC BONDING AREA. IT MAY AFFECT THE CONDUCTIVITY OF FPC WITH TOUCH PANEL.

(iii) IN ORDER NOT TO APPLY LOAD ON THE DISPLAY, PROVIDE A CLEARANCE OF AT LEAST 0.3MM BETWEEN THE PRODUCT AND DISPLAY.

(iv) WE RECOMMEND THE DESIGN OF A CASE OR BEZEL SHOULD COVERS THE BOUNDARY OF THE ACTIVE AREA INSIDE IN ORDER TO PREVENT AN OPERATION AT OUTSIDE OF THE ACTIVE AREA WHICH CAN NOT GUARANTEE THE FUNCTION OR DURABILITY (REFER TO ITEM 5.1.2. STRUCTURE, AREA DEFINITION).
BEZEL'S EDGE PART MAY GUIDE THE PEN SLIDING ON THE SAME POSITION REPEATEDLY. IF THE BEZEL IS PLACED OUTSIDE OF THE ACTIVE AREA, IT MAY CAUSE THE DAMAGE OF THE ITO FILM.



1) OPERATE IT WITH A POLYACETAL PEN (TIP R0.8 OR OVER) OR A BELLY OF A FINGER WITHOUT APPLYING EXCESSIVE LOAD. NEVER USE ANY MECHANICAL PENCILS, BALL POINT PENS AND HARD FINGERTIPS WHOSE TIP IS HARD FOR INPUT, OTHERWISE MALFUNCTIONS MAY RESULT.

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(ii) THE INPUT POSITION MAY BE FLU USE. IT IS DESIRABLE TO PROVID A CIRCUIT AND SOFTWARE.			
(iii) OPERATION AT THE OUT OF ACTI CAUSES A SERIOUS DAMAGE OF A OPERATE AT THE OUT OF ACTIVE	A TRANSPARENT ELECTRODE		IT
(iv) IN CASE OF CLEANING THE PART SET, USE A SOFT CLOTH WITH A F CLEAN WITH A THI NG OTHER TH EDGES LIKE A FINGER NAIL ETC. TRANSPARENT CONDUCTIVE FILL TO YOUR LAST CUSTOMERS.	FINGER BERRY OR A COTTON IAN THE FINGER SUCH AS HAI ON THE CLOTH, BECAUSE IT (BUD. DO 1 RD OR SH. CAUSE	NOT ARP
12.3 DURABILITY			
12.3.1 STYLUS HITTING : ONE MILLION TIMES OR OVER NO DAMAGE ON FILM SURFAC PEN : R8 mm SILICON RUBBER LOAD : 250g FREQUENCY : 240 times/min MEASUREMENT POSITION: 1 POINT OF TOUCH PANEL AC REPEATED : OVER 1,000,000 T	CE X CTIVE AREA		
12.3.2 PEN TOUCH SLIDING DURAB 100,000 TIMES OR OVER WRITING WITH R0.8mm PLAST IN ACTIVE AREA. SPEED IS 60mm/sec.	ILITY : FIC STYLUS PEN; WRITING FO	RCE 150g	

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ON TOUCH PANEL SURFAC	LL FROM THE HEIGHT OF 30ci CE, MUST PASS BELOW CONDI ARANCE WITHOUT ANY CHAN	TIONS:	
HIGHT: 30cm HIGHT: 30cm 5cm 5cm 5cm 5cm 5cm 5cm 5cm 5cm 5cm 5	CE STANDARD AND MAINTAI	- N PRODUC	CT
(PANEL MUST B (C) VISUAL ANGEL : > ((D) LIGHT SOURCE : FL 12.5.2 JUDGE CRITERION : JUDGEMENT UNDER AE TESTED UNDER LIGHT TESTING GOODS DEFEC WHICH WILL BE JUDGE SAMPLING STANDARD	UMINANCE : 500 LUX ° N HUMAN EYES AND PANEL : E TESTED UNDER LIGHT TRAN 60° ° LUORESCENT LIGHT SOURCE ° BOVE MENTIONED CRITERION TRANSPARENT), CT CAN BE VISIBLE WITHIN 10 D AS MAJOR DEFECTS °	ISPARENT (PANEL N SECONDS	/UST BE

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INSPECTION ITEMS		SEPC.	JUDGE CRITERION	OPERATION GUIDELINE	
SCRATCH	$W \le 0.05 \text{mm} \& L \le 10 \text{mm}$		ACCEPTABLE	REFL	
SCRICH	W > 0.05mm or L > 10mm		NOT ACCEPTABLE	BACK GROUND TESTING GOODS	
LINEAR FOREIGN	$W \le 0.05 \text{mm} \& L \le 5 \text{mm}$		ACCEPTABLE	FLUORESCENT LIGHT SOURCE	
OBJECT	W > 0.05	5mm or L >5mm	NOT ACCEPTABLE	300mm =	
	D ≤ 0.25mm		ACCEPTABLE	60° ENVIRONMENTAL IUMINANCE : 500Lux	
GRANULAR FOREIGN OBJECT	0.25mm < D ≤0.30mm		MAX. 2 EA	REFL FLUORESCENT LIGHT SOURCE	
	D	>0.30mm	NOT ACCEPTABLE	60° ENVIRONMENTAL IUMINANCE : 500Lux	
PET BUBBLES	D ≤0.5mm D >0.5mm		ACCEPTABLE		
			NOT ACCEPTABLE		
CHIP ON GLASS	CORNER	$X \le 3mm \land$ Y \le 3mm \cdot Z < t (t = /thickness)	ACCEPTABLE	Chip of glass	
	EDGE	$W \le 3mm \checkmark$ $Y \le 3mm \checkmark Z < t$		Y X X	

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13. INSPECTION CRITERION 13.1 APPLICATION				
THIS INSPECTION STANDARD IS DELIVERED FROM EMERGING I CUSTOMERS		-		ТО
13.2 INSPECTION CONDITIONS				
13.2.1 (1)OBSERVATION DIST (2)VIEW ANGLE :	ANCE : 35cm±5cm			
NON-OPERATION CO)NDITION : $+5^{\circ}$			
	O LCD PANEL SURFA	ACE)		
OPERATION CONDIT		,		
(PERPENDICULAR T	O LCD PANEL SURFA	ACE)		
	Operation Condi	tion		
Non-operation Condition				
	5° / 30cm	n~40cm		
	90°	L L		
13.2.2 ENVIRONMENT CONDI				
AMBIENT TEMPE			<u>C~25°C</u>	
AMBIENT HUN			20%RH	
	ETIC INSPECTION ONAL INSPECTION		HAN 600L 500 Lux	ux
ILLOWINGATION FUNCT	ONAL INSI LETION	500~	500 Lux	
13.2.3 INSPECTION LOT				
QUANTITY PER DELIV	ERY LOT FOR EACH	MODEL		
13.2.4 INSPECTION METHOD				UE.
A SAMPLING INSPECT FOLLOWING PROVISIO				ΠE
(a)APPLICABLE STAN			011111	
MIL-STD-105E				
NORMAL INSPECTIO	ON, SINGLE SAMPLIN	G		
LEVEL II				
(b)AQL : MAJOR DEFEC	-			
MINOR DEFEC	CT : AQL 1.0			

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13.3 INSPECTION STANDARDS

13.3.1 VISUAL DEFECTS CLASSIFICATION

TYPE OF DEFECT	INSPECTION ITEM	DEFECT FEATURE	AQL
MAJOR DEFECT	1.DISPLAY ON	• DEFECT TO MISS SPECIFIED DISPLAY FUNCTION, FOR ALL AND SPECIFIED DOTS EX: DISCONNECTION, SHORT CIRCUIT ETC	
	2.BACKLIGHT	 NO LIGHT FLICKERING AND OTHER ABNORMAL ILLUMINATION 	0.65
	3.DIMENSIONS	• SUBJECT TO INDIVIDUAL ACCEPTANCE SPECIFICATIONS	
	1.DISPLAY ZONE	 BLACK/WHITE SPOT BUBBLES ON POLARIZER NEWTON RING BLACK/WHITE LINE SCRATCH CONTAMINATION LEVER COLOR SPREED 	
MINOR DEFECT	2.BEZEL ZONE	• STAINS • SCRATCHES • FOREIGN MATTER	1.0
	3.SOLDERING	 INSUFFICIENT SOLDER SOLDERED IN INCORRECT POSITION CONVEX SOLDERING SPOT SOLDER BALLS SOLDER SCRAPS 	
	4.DISPLAY ON (ALL ON)	• LIGHT LINE	

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13.3.2 MODULE DEFECTS CALSSIFICATION

I O.	ITEM		_	ΓERIA	
1.	DISPLAY ON INSPECTION	 (1)INCORRECT PATTERN (2)MISSING SEGMENT (3)DIM SEGMENT (4)OPERATING VOLTAGE BEYOND SPEC 			
2.	OVERALL DIMENSIONS	(1)OVERALL DIN	(1)OVERALL DIMENSION BEYOND SPEC		
3.	DOT DEFECT	AND BLUE SCREENS. (2) ITEMS BRIGHT DOT DARK DOT TOAL BRIGHT AND DARK DOTS NOTE : 1. THE DEFINITION OF DOT : THE SIZE OF A DEFECTIVE DOT O REGARDED AS ONE DEFECTIVE D 2. BRIGHT DOT : DOTS APPEAR BRIGHT AND UNCH PANEL IS DISPLAYING UNDER BL 3. DARK DOT : DOTS APPEAR DARK AND UNCH PANEL IS DISPLAYING UNDER PU		DOT. CHANGED IN SIZE IN SLACK PATTERN. IANGED IN SIZE IN V	E DOT IS WHICH LCD
4.	FOREIGN BLACK/WHITE/ BRIGHT LINE/ SCRATCH OF VIEWING AREA	$\begin{tabular}{ c c c c c c c c c c c c c c c c c c c$	WIDTH : W $W \le 0.05$ $0.05 < W \le 0.1$ $0.1 < W$	PERMISSIBLE NO. IGNORE 4 NONE	
	FOREIGN MATTER \ BLACK SPOTS \	AVERAGE DIAMETER (mm): DNUMBER OF PIECES PERMITT $D \le 0.15$ IGNORE $0.15 < D \le 0.5$ 4 $0.5 < D$ NONENOTE : DIAMETER D=(a+b)/2b a b			E

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NO.	ITEM		CRITERIA		
			AVERAGE DIAMETER (mm) : D	NUMBER OF PIECES PERMITTED	
			D ≤ 0.25	IGNORE	
		BUBBLE ON THE POLARIZER	$0.25 < D \le 0.5$	$N \le 5$	
		I OLAKIZEK	0.5 < D	NOTE	
		SURFACE STATUS	D < 0.1 mm	IGNORE	
		SURFACE STATUS	$0.1 < D \leq 0.3 mm$	$N \leq 3$	
		CF FAIL / SPOT	D < 0.1 mm	IGNORE	
			$0.1 < D \leq 0.3 mm$	$N \leq 3$	
6.	BUBBLES OF POLARIZER /DIRT/CF FAIL /SURFACE STAINS	ON ACTIV BUBBLE APPEARS (2)THE EXT OBSERV (3)THE DEF AS FOLL	ER BUBBLE IS DEFINED VE DISPLAY AREA. THE I SHALL BE IGNORED IF T ON THE OUTSIDE OF AC RANEOUS SUBSTANCE I ED WHEN THE MODULE INITION OF AVERAGE D OWING. E DIAMETER (D)=(a+b)/2	DEFECT OF POLARIZER THE POLARIZER BUBBLE CTIVE DISPLAY AREA. IS DEFINED AS IT CAN BI IS POWER ON. IAMETER, D IS DEFINED	
7.	LINE DEFECT ON DISPLAY	OBVIOUS VERTICAL OR HORIZONTAL LINE DEFECT IS NOT ALLOW			
8.	MURA ON DISPLAY	IT'S OK IF MURA IS	SLIGHT VISIBLE THROU	NG 6% ND FILTER	
9.	UNEVEN COLOR SPREAD, COLORATION	(1)TO BE DETERMINED BASED UPON THE STANDARD SAMPLE.			
10.	BEZEL APPEARANCE	 (1)BEZEL MAY NOT HAVE RUST, BE DEFORMED OR HAVE FINGER PRINTS STAINS OF OTHER CONTAMINATION. (2)BEZEL MUST COMPLY WITH JOB SPECIFICATIONS. 			
11	РСВ	(1)THERE MAY NOT THE SEAL AREA (THAN THREE PLA (2)NO OXIDATION C (3)PARTS ON PCB M CHARACTERISTIC THERE SHOULD B PARTS. (4)THE JUMPER ON T CHARACTERISTIC (5)IF SOLDER GETS	BE MORE THAN 2mm OF ON THE PCB, AND THERI ACES. OR CONTAMINATION PCE UST BE THE SAME AS ON C CHART. BE NO WRONG PARTS, M THE PCB SHOULD CONFO	E SEALANT OUTSIDE E SHOULD BE NO MORE B TERMINALS. N THE PRODUCTION ISSING PARTS OR EXCES ORM TO THE PRODUCT ED PAD, ZEBRA PAD OR	

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NO.	ITEM	CRITERIA
12.		(b)CHIP COMPONENT COMPONENT IS OFF CENTER, AND MORE THAN 50% OF THE LEADS IS OFF THE PAD OUTLINE
	SOLDERING	
		 (4)NO UNMELTED SOLDER PASTE MAY BE PRESENT ON THE PCB. (5)NO COLD SOLDER JOINTS, MISSING SOLDER CONNECTIONS, OXIDATION OR ICICLE. (6)NO RESIDUE OR SOLDER BALLS ON PCB. (7)NO SHORT CIRCUITS IN COMPONENTS ON PCB.
13.	BACKLIGHT	 (1)NO LIGHT (2)FLICKERING AND OTHER ABNORMAL ILLUMINATION (3)SPOTS OR SCRATCHES THAT APPEAR WHEN LIT MUST BE JUDGED USING LCD SPOT, LINES AND CONTAMINATION STANDARDS. (4)BACKLIGHT DOESN'T LIGHT OR COLOR IS WRONG.
14.	GENERAL APPEARANCE	 (1)NO OXIDATION, CONTAMINATION, CURVES OR, BENDS ON INTERFACE PIN (OLB) OF TCP. (2)NO CRACKS ON INTERFACE PIN (OLB) OF TCP. (3)NO CONTAMINATION, SOLDER RESIDUE OR SOLDER BALLS ON PRODUCT. (4)THE IC ON THE TCP MAY NOT BE DAMAGED, CIRCUITS. (5)THE UPPERMOST EDGE OF THE PROTECTIVE STRIP ON THE INTERFACE PIN MUST BE PRESENT OR LOOK AS IF IT CAUSE THE INTERFACE PIN TO SEVER. (6)THE RESIDUAL ROSIN OR TIN OIL OF SOLDERING (COMPONENT OR CHIP COMPONENT) IS NOT BURNED INTO BROWN OR BLACK COLOR. (7)SEALANT ON TOP OF THE ITO CIRCUIT HAS NOT HARDENED. (8)PIN TYPE MUST MATCH TYPE IN SPECIFICATION SHEET. (9)LCD PIN LOOSE OR MISSING PINS. (10)PRODUCT PACKAGING MUST THE SAME AS SPECIFIED ON PACKAGING SPECIFICATION SHEET. (11)PRODUCT DIMENSION AND STRUCTURE MUST CONFORM TO PRODUCT SPECIFICATION SHEET. (12)THE APPEARANCE OF HEAT SEAL SHOULD NOT ADMIT ANY DIRT AND BREAK.

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NO. IT	EM	CRITERIA		
NO. IT		IVE CRACK IS NOT ACCEPTABLE		
15. CRACKED C	GLASS	$\begin{array}{c c c c c c c c c c c c c c c c c c c $		
	c c c	a b c ≤t ≤1/8X ≤L *X=LCD SIDE WIDTH t = GLASS THICKNESS L=ELECTRODE PAD LENGTH ①IF GLASS CHIPPING THE ITO TERMINAL, OVER 2/3 OF THE ITO MUST REMAIN AND BE, INSPECTED ACCORDING TO ELECTRODE TERMINAL SPECIFICATIONS ②IF THE PRODUCT WILL BE HEAT SEALED BY THE CUSTOMER, THE ALIGNMENT MARK MUST NOT BE DEMAGED		

13.4 RELIABILITY TEST

13.4.1 STANDARD SPECIFICATIONS FOR RELIABILITY OF LCD MODULE

NO	ITEM	DESCRIPTION
1	HIGH TEMPERATURE OPERATION	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +70°C FOR 240 HRS
2	LOW TEMPERATURE OPERATION	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -20°C FOR 240 HRS
3	HIGH TEMPERATURE STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +80°C FOR 240 HRS
4	LOW TEMPERATURE STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -30°C FOR 240 HRS
5	HIGH TEMP / HUMIDITY TEST STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT 60°C , 90% RH 240 HRS
6	THERMAL SHOCK (NOT OPERATED)	THE SAMPLE SHOULD BE ALLOWED TO STAND THE FOLLOWING 10 CYCLES OF OPERATION: -40°C FOR 30 MINUTES ~ +80°C FOR 30 MINUTES
7		AIR DISCHARGE ± 12KV CONTACT DISCHARGE ± 8KV

NOTE (1) : THE TEST SAMPLES HAVE RECOVERY TIME FOR 2 HOURS AT ROOM TEMPERATURE BEFORE THE FUNCTION CHECK. IN THE STANDARD CONDITIONS, THERE IS NO DISPLAY FUNCTION NG ISSUE OCCURRED.

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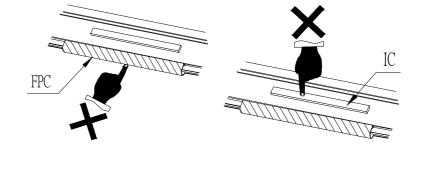
13.5 TESTING CONDITIONS AND INSPECTION CRITERIA

FOR THE FINAL TEST THE TESTING SAMPLE MUST BE STORED AT ROOM TEMPERATURE FOR 24 HOURS, STANDARD SPECIFICATIONS FOR RELIABILITY HAVE BEEN EXECUTED IN ORDER TO ENSURE STABILITY.

NO	ITEM	TEST MODEL	INSPECTION CRITERIA
1	CURRENT CONSUMPTION		THE CURRENT CONSUMPTION SHOULD CONFORM TO THE PRODUCT SPECIFICATION.
2	CONTRAST	REFER TO SPECIFICATION	AFTER THE TESTS HAVE BEEN EXECUTED, THE CONTRAST MUST BE LARGER THAN HALF OF ITS INITIAL VALUE PRIOR TO THE TESTS.
3	APPEARANCE	VISUAL INSPECTION	DEFECT FREE

13.6 OPERATION

- 13.6.1 DO NOT CONNECT OR DISCONNECT MODULES TO OR FROM THE MAIN SYSTEM WHILE POWER IS BEING SUPPLIED .
- 13.6.2 USE THE MODULE WITHIN SPECIFIED TEMPERATURE ; LOWER TEMPERATURE CAUSES THE RETARDATION OF BLINKING SPEED OF THE DISPLAY ; HIGHER TEMPERATURE MAKES OVERALL DISPLAY DISCOLOR . WHEN THE TEMPERATURE RETURNS TO NORMALITY, THE DISPLAY WILL OPERATE NORMALLY .
- 13.6.3 ADJUST THE LC DRIVING VOLTAGE TO OBTAIN THE OPTIMUM CONTRAST .
- 13.6.4 POWER ON SEQUENCE INPUT SIGNALS SHOULD NOT BE SUPPLIED TO LCD MODULE BEFORE POWER SUPPLY VOLTAGE IS APPLIED AND REACHES THE SPECIFIED VALUE . IF ABOVE SEQUENCE IS NOT FOLLOWED, CMOS LSIS OF LCD MODULES MAY BE DAMAGED DUE TO LATCH - UP PROBLEM .
- 13.6.5 NOT ALLOWED TO INFLICT ANY EXTERNAL STRESS AND TO CAUSE ANY MECHANICAL INTERFERENCE ON THE BENDING AREA OF FPC DURING THE TAIL BENDING BACKWARDS! DO NOT STRESS FPC AND IC ON THE MODULE!



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13.7 NOTICE

13.7.1	USE A GROUNDED SOLDERING IRON WHEN SOLDERING
	CONNECTOR I/O TERMINALS . FOR SOLDERING OR REPAIRING,
	TAKE PRECAUTION AGAINST THE TEMPERATURE OF THE
	SOLDERING IRON AND THE SOLDERING TIME TO PREVENT PEELING
	OFF THE THROUGH-HOLE-PAD .

- 13.7.2 DO NOT DISASSEMBLE . EDT SHALL NOT BE HELD RESPONSIBLE IF THE MODULE IS DISASSEMBLED AND UPON THE REASSEMBLY THE MODULE FAILED .
- 13.7.3 DO NOT CHARGE STATIC ELECTRICITY , AS THE CIRCUIT OF THIS MODULE CONTAINS CMOS LSIS. A WORKMAN'S BODY SHOULD ALWAYS BE STATIC-PROTECTED BY USE OF AN ESD STRAP. WORKING CLOTHES FOR SUCH PERSONNEL SHOULD BE OF STATIC-PROTECTED MATERIAL .
- 13.7.4 ALWAYS GROUND THE ELECTRICALLY-POWERED DRIVER BEFORE USING IT TO INSTALL THE LCD MODULE. WHILE CLEANING THE WORK STATION BY VACUUM CLEANER, DO NOT BRING THE SUCKING MOUTH NEAR THE MODULE ; STATIC ELECTRICITY OF THE ELECTRICALLY-POWERED DRIVER OR THE VACUUM CLEANER MAY DESTROY THE MODULE .
- 13.7.5 DON'T GIVE EXTERNAL SHOCK.
- 13.7.6 DON'T APPLY EXCESSIVE FORCE ON THE SURFACE.
- 13.7.7 LIQUID IN LCD IS HAZARDOUS SUBSTANCE. MUST NOT LICK AND SWALLOW.WHEN THE LIQUID IS ATTACH TO YOUR, SKIN, CLOTH ETC. WASH IT OUT THOROUGHLY AND IMMEDIATELY.
- 13.7.8 DON'T OPERATE IT ABOVE THE ABSOLUTE MAXIMUM RATING.
- 13.7.9 STORAGE IN A CLEAN ENVIRONMENT, FREE FROM DUST, ACTIVE GAS, AND SOLVENT.
- 13.7.10 STORE WITHOUT ANY PHYSICAL LOAD.
- 13.7.11 REWIRING: NO MORE THAN 3 TIMES.