



DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(on) max}	Ι_D Τ _A = +25°C		
20V	0.55Ω @ $V_{GS} = 4.5V$	540mA		

Description

This MOSFET has been designed to minimize the on-state resistance $(R_{DS(on)})$ and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

Load Switch

Features

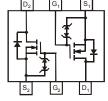
- Dual N-Channel MOSFET
- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SOT363
- Case Material: Molded Plastic, "Green" Molding
 Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin annealed over Alloy 42
 leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.006 grams (approximate)







Top View

Top View Internal Schematic

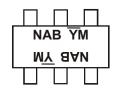
Ordering Information (Note 4)

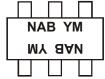
Part Number	Case	Packaging		
DMN2004DWK-7	SOT363	3000/Tape & Reel		

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html

Marking Information





NAB = Product Type Marking Code $\underline{Y}M$ = Date Code Marking for SAT (Shanghai Assembly/ Test site) $\underline{Y}M$ = Date Code Marking for CAT (Chengdu Assembly/ Test site) \underline{Y} or \underline{Y} = Year (ex: A = 2013) \underline{M} = Month (ex: 9 = September)

Date Code Key

	-)											
Year	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015	2016	2017
Code	Т	U	V	W	Χ	Υ	Z	Α	В	С	D	Е
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Char	acteristic		Symbol	Value	Units
Drain-Source Voltage			V _{DSS}	20	V
Gate-Source Voltage			V _{GSS}	±8	V
Drain Current (Note 5)	Steady State	T _A = +25°C T _A = +85°C	I _D	540 390	mA
Pulsed Drain Current (Note 6)			I _{DM}	1.5	Α

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 5)	P_{D}	200	mW
Thermal Resistance, Junction to Ambient	$R_{ hetaJA}$	625	°C/W
Operating and Storage Temperature Range	$T_{J_i}T_{STG}$	-65 to +150	°C

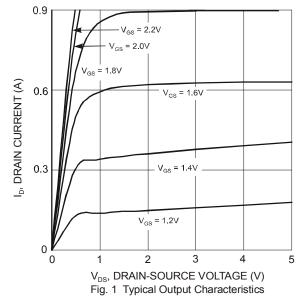
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

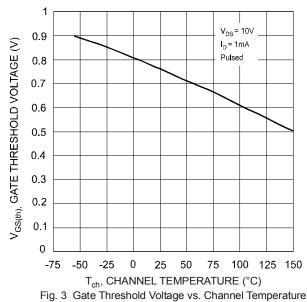
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	20	_	_	V	$V_{GS} = 0V, I_{D} = 10\mu A$
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1	μA	V _{DS} = 16V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	_	_	±1	μΑ	$V_{GS} = \pm 4.5V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(th)}	0.5	_	1.0	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
	R _{DS (ON)}	_	0.4	0.55 0.70 0.9	Ω	V_{GS} = 4.5V, I_{D} = 540mA
Static Drain-Source On-Resistance			0.5			V_{GS} = 2.5V, I_{D} = 500mA
			0.7			V_{GS} = 1.8V, I_{D} = 350mA
Forward Transfer Admittance	Y _{fs}	200	_	_	ms	V _{DS} =10V, I _D = 0.2A
Diode Forward Voltage (Note 7)	V _{SD}	0.5	_	1.4	V	V _{GS} = 0V, I _S = 115mA
DYNAMIC CHARACTERISTICS						
Input Capacitance	C _{iss}	_	_	150	pF	
Output Capacitance	Coss	_	_	25	pF	V _{DS} = 16V, V _{GS} = 0V f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	_	_	20	pF	1.000112

Notes:

- 5. Device mounted on FR-4 PCB.
- 6. Pulse width ≤10μS, Duty Cycle ≤1%.7. Short duration pulse test used to minimize self-heating effect.







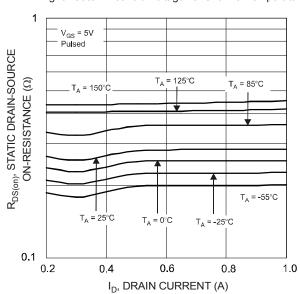


Fig. 5 Static Drain-Source On-Resistance vs. Drain Current

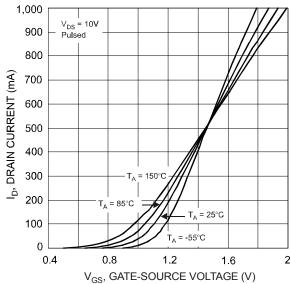


Fig. 2 Reverse Drain Current vs. Source-Drain Voltage

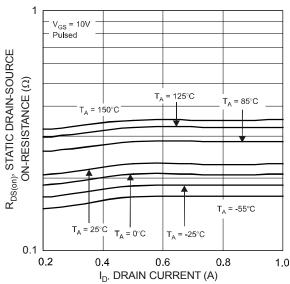


Fig. 4 Static Drain-Source On-Resistance Vs. Drain Current

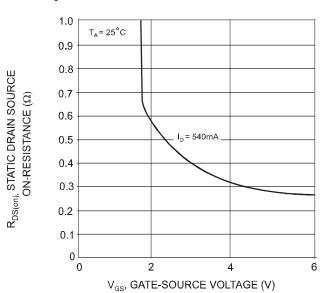


Fig. 6 Static Drain-Source, On-Resistance vs. Gate-Source Voltage



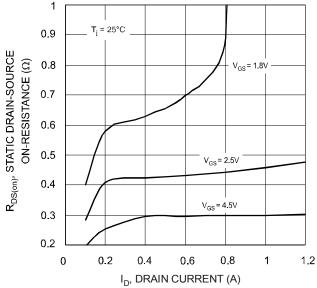
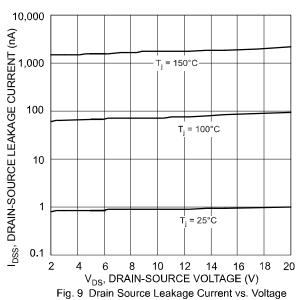


Fig. 7 On-Resistance vs. Drain Current and Gate Voltage



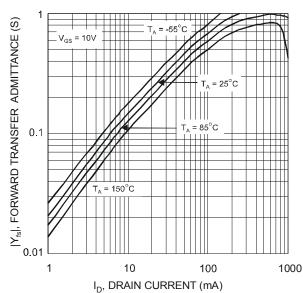


Fig. 11 Forward Transfer Admittance vs. Drain Current

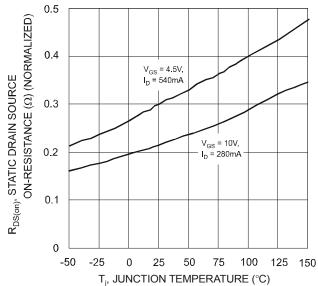


Fig. 8 Static Drain-Source, On-Resistance vs. Temperature

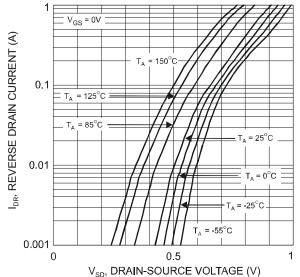


Fig. 10 Reverse Drain Current vs. Source-Drain Voltage

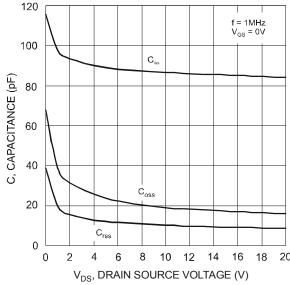
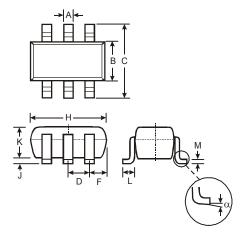


Fig. 12 Capacitance Variation



Package Outline Dimensions

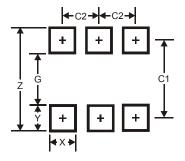
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SOT363					
Dim	Min	Max			
Α	0.10	0.30			
В	1.15	1.35			
С	2.00	2.20			
D	0.65 Typ				
F	0.40	0.45			
Н	1.80	2.20			
J	0	0.10			
K	0.90	1.00			
L	0.25	0.40			
M	0.10	0.22			
α	0°	8°			
All Dimensions in mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for latest version.



Dimensions	Value (in mm)
Z	2.5
G	1.3
Х	0.42
Y	0.6
C1	1.9
C2	0.65



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