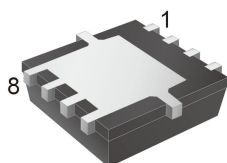


TSM085N03PQ33

30V N-Channel Power MOSFET

PDFN33



Pin Definition:

- | | |
|-----------|----------|
| 1. Source | 8. Drain |
| 2. Source | 7. Drain |
| 3. Source | 6. Drain |
| 4. Gate | 5. Drain |

Key Parameter Performance

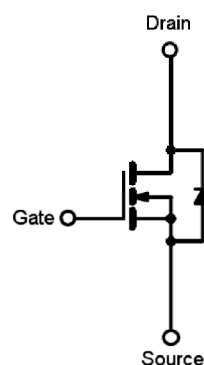
Parameter	Value	Unit
V_{DS}	30	V
$R_{DS(on)}$ (max)	$V_{GS} = 10V$	8.5
	$V_{GS} = 4.5V$	13
Q_g	7.5	nC

Ordering Information

Part No.	Package	Packing
TSM085N03PQ33 RGG	PDFN33	5kpcs / 13+Reel

Note: %G+denotes for Halogen- and Antimony-free as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds

Block Diagram



N-Channel MOSFET

Absolute Maximum Ratings ($T_C = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	I_D	$T_C = 25^\circ C$	48
		$T_C = 100^\circ C$	30
Pulsed Drain Current ^(Note 1)	I_{DM}	192	A
Single Pulse Avalanche Energy ^(Note 2)	E_{AS}	45	mJ
Power Dissipation @ $T_C = 25^\circ C$	P_D	35	W
Operating Junction Temperature	T_J	150	$^\circ C$
Storage Temperature Range	T_{STG}	-55 to +150	$^\circ C$

Thermal Performance

Parameter	Symbol	Limit	Unit
Thermal Resistance - Junction to Ambient	R_{JA}	62	$^\circ C/W$
Thermal Resistance - Junction to Case	R_{JC}	3.6	$^\circ C/W$

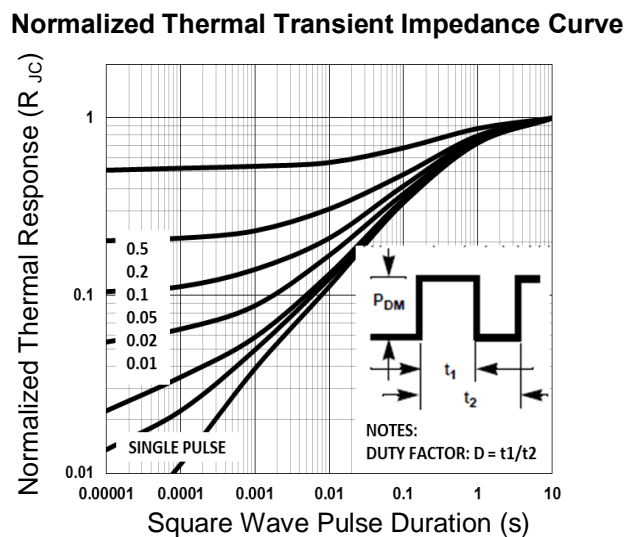
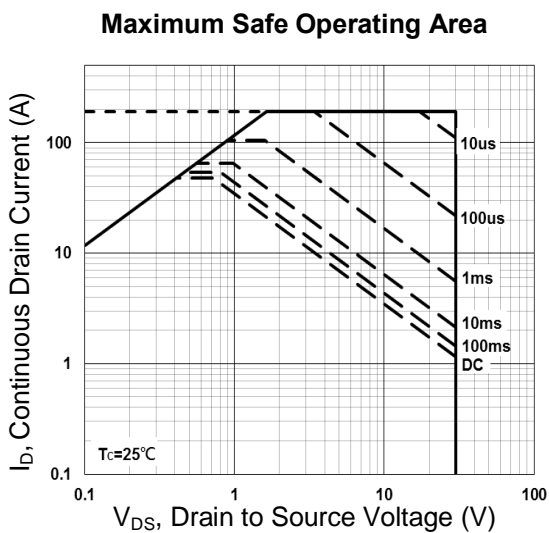
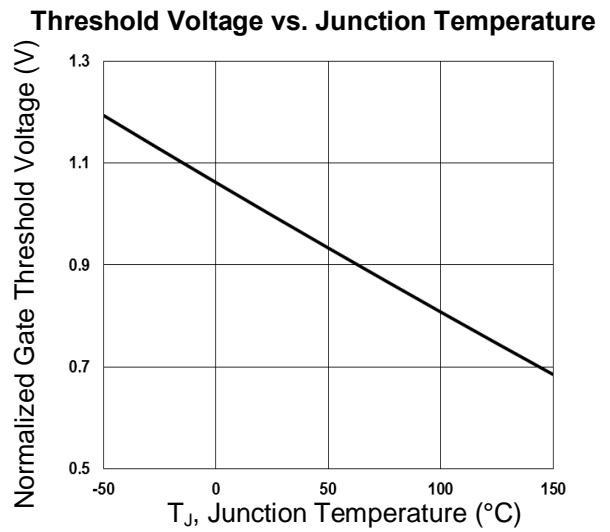
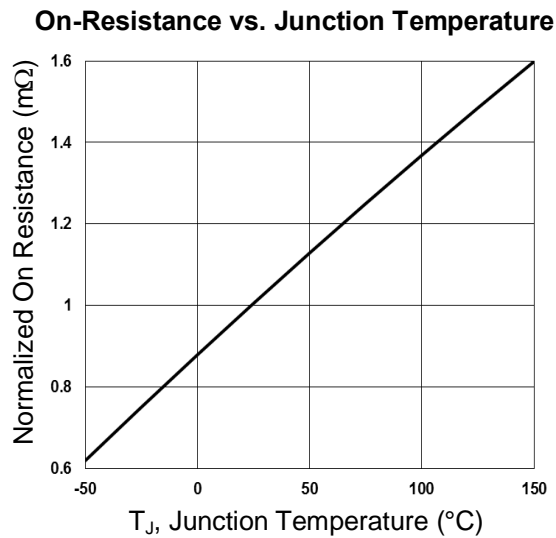
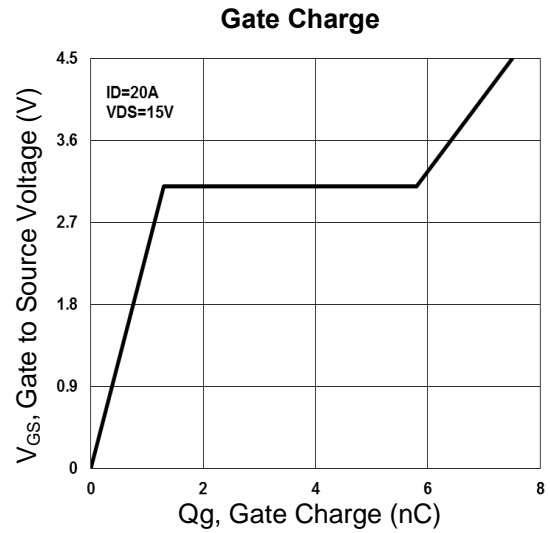
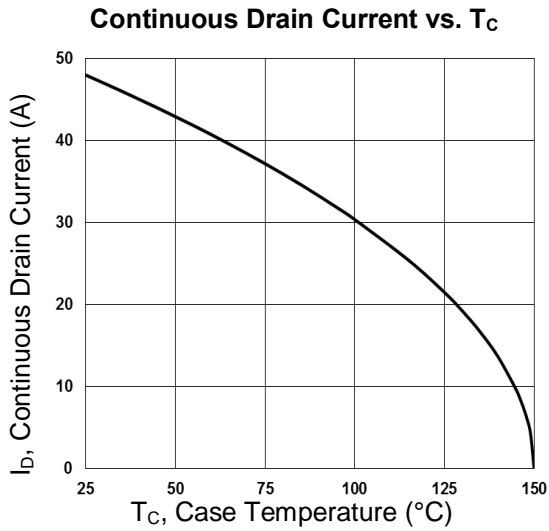
Electrical Specifications ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Conditions	Symbol	Min	Typ	Max	Unit
Static						
Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu\text{A}$	BV_{DSS}	30	--	--	V
Drain-Source On-State Resistance	$V_{GS} = 10V, I_D = 16A$	$R_{DS(ON)}$	--	6.2	8.5	m
	$V_{GS} = 4.5V, I_D = 8A$		--	9	13	
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	$V_{GS(TH)}$	1.2	1.6	2.5	V
Zero Gate Voltage Drain Current	$V_{DS} = 30V, V_{GS} = 0V$	I_{DSS}	--	--	1	μA
	$V_{DS} = 24V, T_J = 125^\circ\text{C}$		--	--	10	
Gate Body Leakage	$V_{GS} = \pm 20V, V_{DS} = 0V$	I_{GSS}	--	--	± 100	nA
Forward Transconductance ^(Note 3)	$V_{DS} = 10V, I_D = 8A$	g_{fs}	--	9.5	--	S
Dynamic						
Total Gate Charge ^(Note 3,4)	$V_{DS} = 15V, I_D = 20A,$ $V_{GS} = 4.5V$	Q_g	--	7.5	--	nC
Gate-Source Charge ^(Note 3,4)		Q_{gs}	--	1.3	--	
Gate-Drain Charge ^(Note 3,4)		Q_{gd}	--	4.5	--	
Input Capacitance	$V_{DS} = 25V, V_{GS} = 0V,$ $f = 1.0\text{MHz}$	C_{iss}	--	680	--	pF
Output Capacitance		C_{oss}	--	150	--	
Reverse Transfer Capacitance		C_{rss}	--	70	--	
Switching						
Turn-On Delay Time ^(Note 3,4)	$V_{DD} = 15V, I_D = 15A,$ $V_{GS} = 10V, R_{GEN} = 3.3$	$t_{d(on)}$	--	4.8	--	ns
Turn-On Rise Time ^(Note 3,4)		t_r	--	12.5	--	
Turn-Off Delay Time ^(Note 3,4)		$t_{d(off)}$	--	27.6	--	
Turn-Off Fall Time ^(Note 3,4)		t_f	--	8.2	--	
Source-Drain Diode Ratings and Characteristic						
Maximum Continuous Drain-Source Diode Forward Current	Integral reverse diode in the MOSFET	I_S	--	--	48	A
Maximum Pulse Drain-Source Diode Forward Current		I_{SM}	--	--	192	A
Diode-Source Forward Voltage	$V_{GS} = 0V, I_S = 1A$	V_{SD}	--	--	1	V

Note:

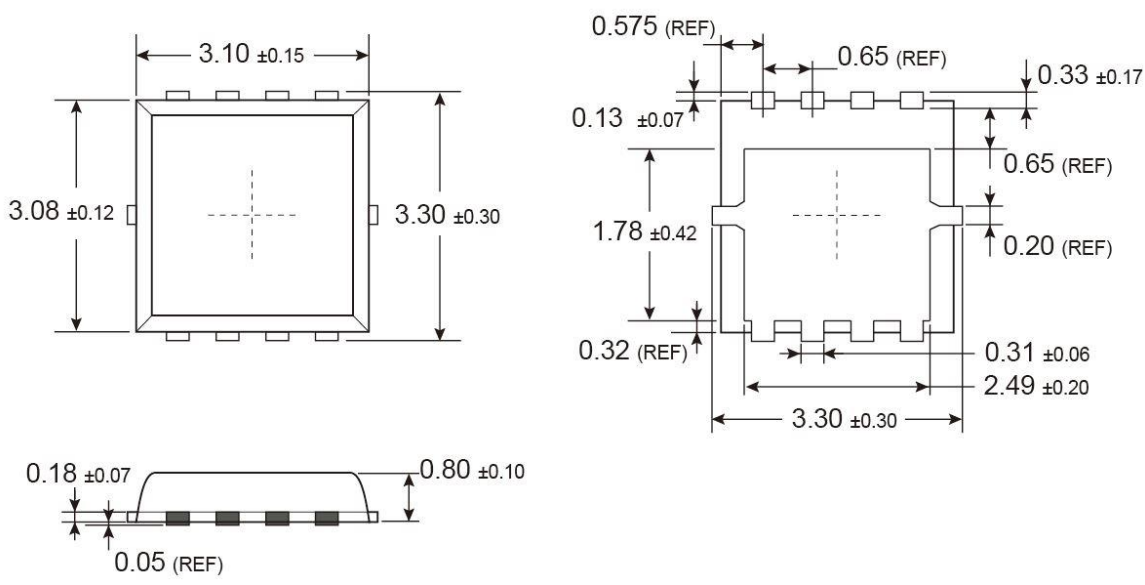
1. Pulse width limited by safe operating area
2. $L = 0.1\text{mH}, I_{AS} = 30A, V_{DD} = 25V, R_G = 25\ \Omega$, Starting $T_J = 25^\circ\text{C}$
3. Pulse test: pulse width $m300\mu\text{s}$, duty cycle $m2\%$
4. Switching time is essentially independent of operating temperature.

Electrical Characteristics Curve



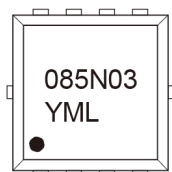


PDFN33 Mechanical Drawing



Unit: Millimeters

Marking Diagram



- Y** = Year Code
- M** = Month Code for Halogen Free Product
(**O**=Jan, **P**=Feb, **Q**=Mar, **R**=Apr, **S**=May, **T**=Jun, **U**=Jul, **V**=Aug, **W**=Sep, **X**=Oct, **Y**=Nov, **Z**=Dec)
- L** = Lot Code

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