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April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<a href="http://www.renesas.com">http://www.renesas.com</a>)

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# RQK0302GGDQS

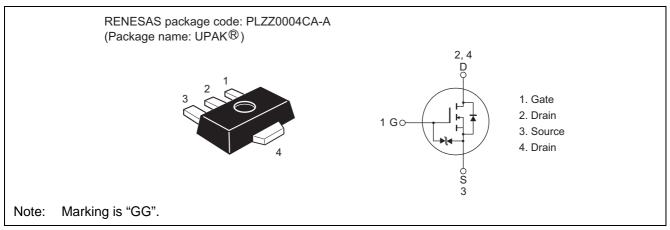
# Silicon N Channel MOS FET Power Switching

REJ03G1270-0300 Rev.3.00 Jun 22, 2006

### **Features**

- Low on-resistance  $R_{DS(on)} = 81 \text{ m}\Omega \text{ typ } (V_{GS} = 10 \text{ V}, I_D = 1.9 \text{ A})$
- Low drive current
- High speed switching
- 4.5 V gate drive

### **Outline**



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### **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

			(1u 25 C)
Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	30	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	I <sub>D</sub>	3.8	Α
Drain peak current	I <sub>D (pulse)</sub> Note1	5.6	Α
Body - drain diode reverse drain current	I <sub>DR</sub>	3.8	А
Channel dissipation	Pch Note2	1.5	W
Channel dissipation	Pch (pulse) Note1	5	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1.  $PW \le 1$  s, duty cycle  $\le 1\%$ 

2. When using the glass epoxy board (FR-4: 40 x 40 x 1 mm)

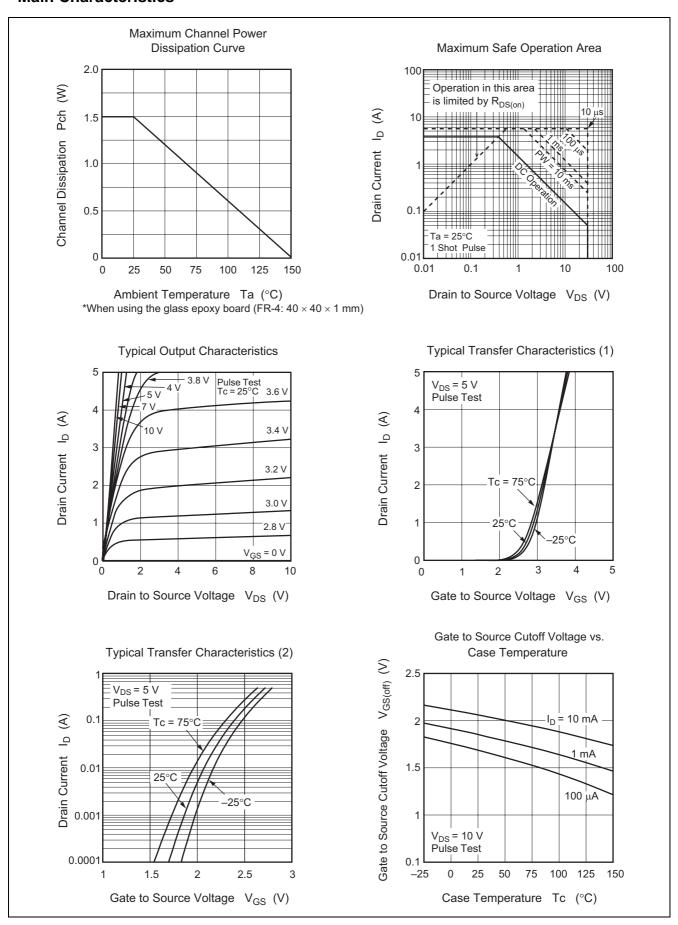
### **Electrical Characteristics**

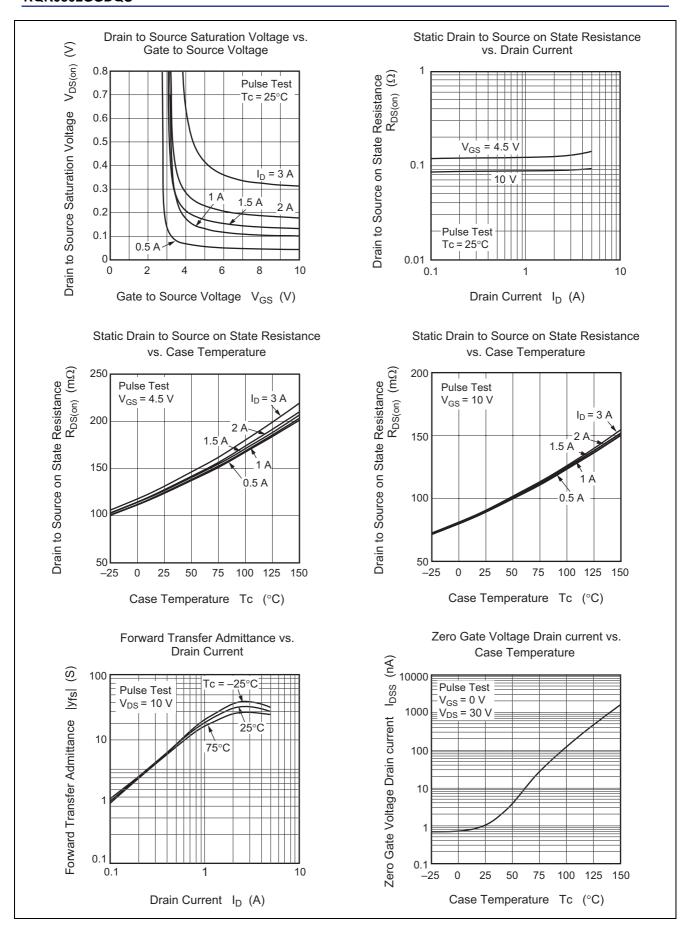
 $(Ta = 25^{\circ}C)$ 

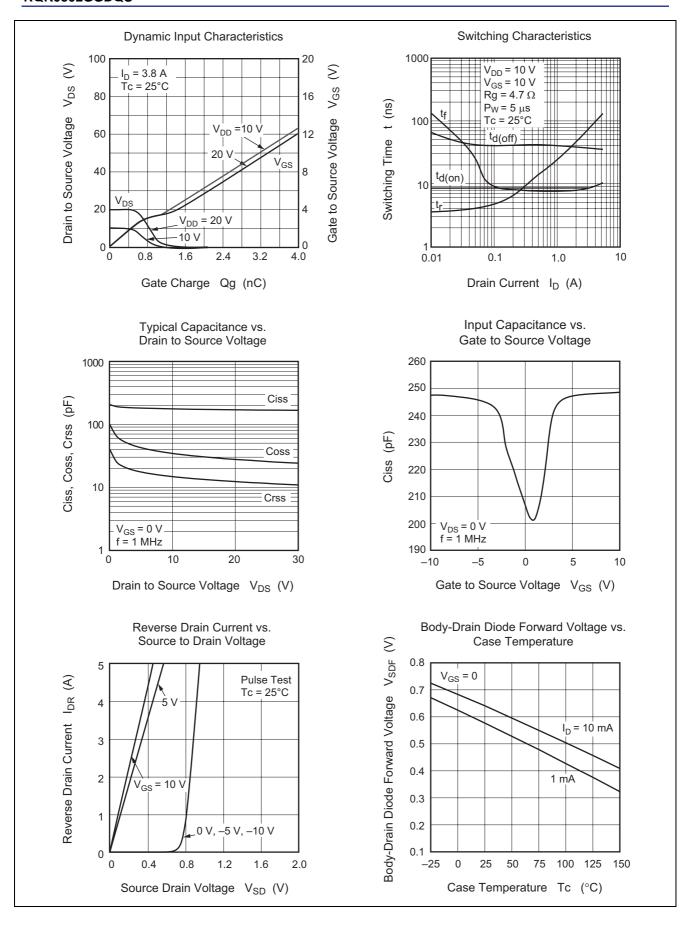
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	30	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	_	_	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	±10	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Drain to source leak current	I <sub>DSS</sub>	_	_	1	μΑ	$V_{DS} = 30 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.0	_	2.0	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Drain to source on state resistance	R <sub>DS(on)</sub>	_	81	102	mΩ	$I_D = 1.9 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note3}}$
	R <sub>DS(on)</sub>	_	107	150	mΩ	$I_D = 1.9 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note3}}$
Forward transfer admittance	y <sub>fs</sub>	2.6	4.3	_	S	$I_D = 1.9 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note3}}$
Input capacitance	Ciss	_	170	_	pF	$V_{DS} = 10 \text{ V}, V_{GS} = 0,$
Output capacitance	Coss	_	35	_	pF	f = 1 MHz
Reverse transfer capacitance	Crss	_	16	_	pF	
Turn - on delay time	t <sub>d(on)</sub>	_	8.7	_	ns	$I_D = 0.5 \text{ A}, V_{GS} = 10 \text{ V},$
Rise time	t <sub>r</sub>	_	24	_	ns	$R_L = 20 \Omega$ , $Rg = 4.7 \Omega$
Turn - off delay time	t <sub>d(off)</sub>	_	40	_	ns	
Fall time	t <sub>f</sub>	_	7.9	_	ns	
Total gate charge	Qg	_	3.2	_	nC	$V_{DD} = 10 \text{ V}, V_{GS} = 10 \text{ V},$
Gate to source charge	Qgs	_	0.6	_	nC	$I_D = 3.8 \text{ A}$
Gate to drain charge	Qgd	_	1.2	_	nC	
Body - drain diode forward voltage	$V_{DF}$	_	0.85		V	$I_F = 1.5 \text{ A}, V_{GS} = 0^{\text{Note3}}$

Notes: 3. Pulse test

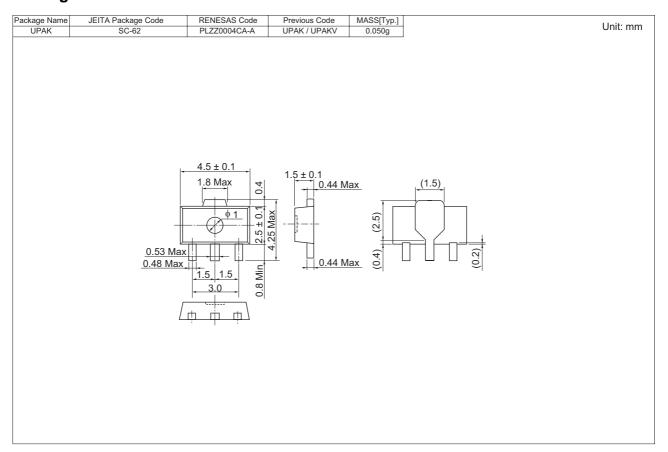
### **Main Characteristics**







### **Package Dimensions**



## **Ordering Information**

Part Name	Quantity	Shipping Container
RQK0302GGDQSTL-E	1000 pcs.	φ178 reel, 12 mm Emboss taping

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