

CTLTVS12
**SURFACE MOUNT SILICON
TRANSIENT VOLTAGE SUPPRESSOR**



www.centrasemi.com



DESCRIPTION:

The CENTRAL SEMICONDUCTOR CTLTVS12 is a low leakage, fast response TVS packaged in an ultra small, ultra low profile surface mount package. This device is designed to protect sensitive equipment against ESD damage.

MARKING CODE: O

APPLICATIONS:

- PDAs
- Memory Card Ports
- Cellular Phones
- Instrumentation

FEATURES:

- Ultra Small, Ultra Low Profile 0.3mm x 0.6mm x 0.3mm Leadless Surface Mount package
- Low Capacitance
- Low Leakage Current

MAXIMUM RATINGS: (T_A=25°C)

Peak Power Dissipation (8x20µs)
ESD Voltage (IEC 61000-4-2, Air)
ESD Voltage (IEC 61000-4-2, Contact)
Operating and Storage Junction Temperature

SYMBOL

P_{PK} 35
V_{ESD} 30
V_{ESD} 30
T_J, T_{stg} -55 to +150

UNITS

W
kV
kV
°C

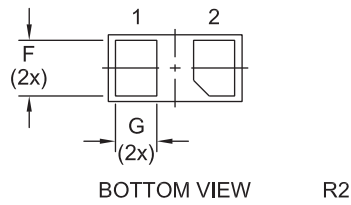
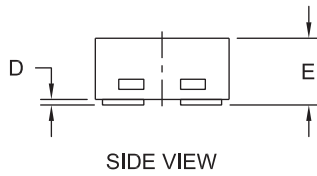
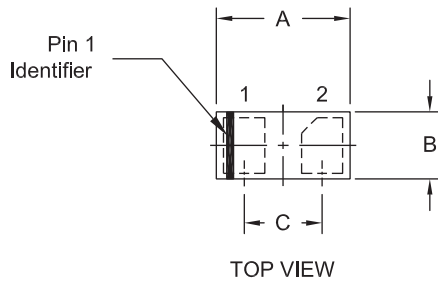
ELECTRICAL CHARACTERISTICS: (T_A=25°C) V_F=1.0V MAX @ I_F=10mA

Maximum Reverse Stand-off Voltage V _{RWM}	Breakdown Voltage V _{BR} @ I _T			Test Current I _T	Maximum Reverse Leakage Current I _R @ V _{RWM}	Maximum Clamping Voltage V _C @ I _{PP}	Peak Pulse Current I _{PP}	Typical Capacitance @ 0V Bias pF	Typical Capacitance @ 9V Bias pF
	MIN V	NOM V	MAX V						
V	MIN V	NOM V	MAX V	mA	µA	V	A	pF	pF
9.0	10	12	14	5.0	1.0	18	1.8	14	5.0

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TLM2D3D6 CASE - MECHANICAL OUTLINE



SYMBOL	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.022	0.026	0.55	0.65
B	0.010	0.014	0.25	0.35
C	0.014		0.35	
D	0.000	0.002	0.00	0.05
E	0.011	0.013	0.28	0.32
F	0.008	0.012	0.20	0.30
G	0.005	0.009	0.13	0.24

TLM2D3D6 (REV: R2)

LEAD CODE:
 1) Cathode
 2) Anode

MARKING CODE: O

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SERVICES

- Bonded Inventory
- Custom Electrical Screening
- Custom Electrical Characteristic Curves
- SPICE Models
- Custom Packaging
- Package Base Options
- Custom Device Development/ Multi Discrete Modules (MDM™)
- Bare Die Available for Hybrid Applications

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R2 (2-April 2015)