

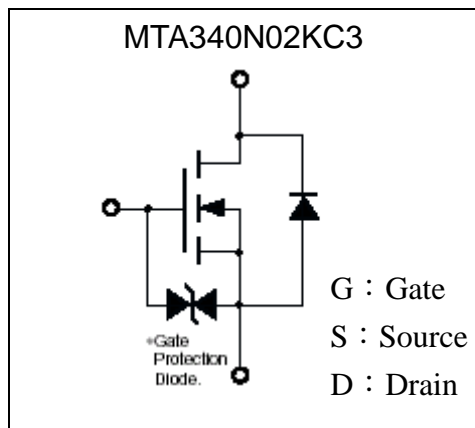
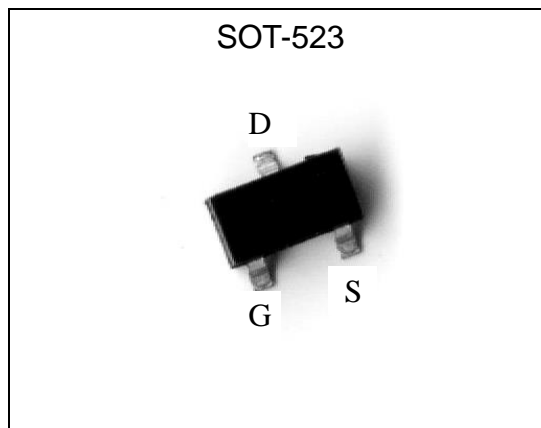
20V N-Channel Enhancement Mode MOSFET

MTA340N02KC3

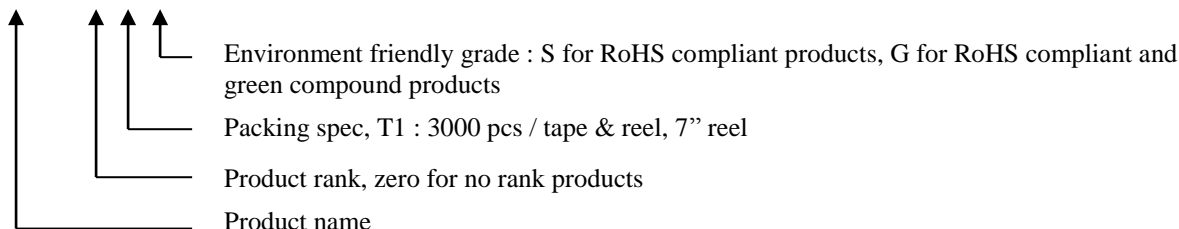
BV_{DSS}	20V
$I_D @ V_{GS}=4.5V$	700mA
$R_{DS(on)} @ V_{GS}=4.5V, I_D=650mA$	299m Ω (typ)
$R_{DS(on)} @ V_{GS}=2.5V, I_D=500mA$	550m Ω (typ)
$R_{DS(on)} @ V_{GS}=1.8V, I_D=200mA$	1.05 Ω (typ)

Features

- Simple drive requirement
- Small package outline
- Pb-free lead plating and halogen-free package

Symbol

Outline

Ordering Information

Device	Package	Shipping
MTA340N02KC3-0-T1-G	SOT-523 (Pb-free lead plating package)	3000 pcs / tape & reel





Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	V _{DS}	20	V
Gate-Source Voltage	V _{GS}	±12	
Continuous Drain Current @ TA=25°C, V _{GS} =4.5V	I _D	700 (Note 3)	mA
Continuous Drain Current @ TA=70°C, V _{GS} =4.5V		560 (Note 3)	
Pulsed Drain Current (Notes 1, 2)	I _{DM}	2.8	A
Power Dissipation	P _D	280 (Note 3)	mW
ESD susceptibility	V _{ESD}	1400 (Note 4)	V
Operating Junction and Storage Temperature	T _j , T _{stg}	-55~+150	°C

Thermal Performance

Parameter	Symbol	Limit	Unit
Thermal Resistance, Junction-to-Ambient, max (Note 3)	R _{th,ja}	450	°C/W

- Note : 1. Pulse width limited by maximum junction temperature.
 2. Pulse width ≤ 300μs, duty cycle ≤ 2%.
 3. Surface mounted on 1 in² copper pad of FR-4 board.
 4. Human body model, 1.5kΩ in series with 100pF.

Electrical Characteristics (Tj=25°C, unless otherwise noted)

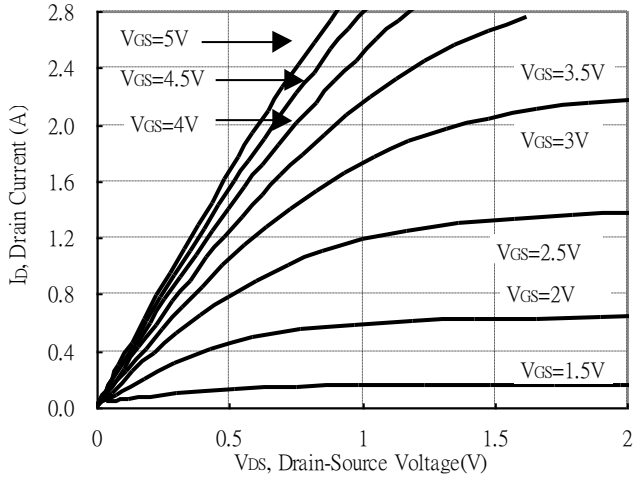
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	20	-	-	V	V _{GS} =0V, I _D =250μA
V _{GS(th)}	0.45	0.66	1.0	V	V _{DS} =V _{GS} , I _D =250μA
I _{GSS}	-	-	±10	μA	V _{GS} =±12V, V _{DS} =0V
I _{DSS}	-	-	1		V _{DS} =16V, V _{GS} =0V
	-	-	10		V _{DS} =16V, V _{GS} =0V (T _j =70°C)
*R _{DS(ON)}	-	299	390	mΩ	V _{GS} =4.5V, I _D =650mA
	-	550	705		V _{GS} =2.5V, I _D =500mA
	-	1.05	2.1	Ω	V _{GS} =1.8V, I _D =200mA
*G _{FS}	-	870	-	mS	V _{DS} =10V, I _D =400mA
Dynamic					
C _{iss}	-	35	-	pF	V _{DS} =15V, V _{GS} =0, f=1MHz
C _{oss}	-	11	-		
C _{rss}	-	9	-		
t _{d(ON)}	-	7	-	ns	V _{DS} =15V, I _D =500mA, V _{GS} =4.5V, R _G =6Ω
t _r	-	21	-		
t _{d(OFF)}	-	25	-		
t _f	-	47	-		
Q _g	-	1	-	nC	V _{DS} =15V, I _D =500mA, V _{GS} =4.5V
Q _{gs}	-	0.05	-		
Q _{gd}	-	0.4	-		
Source-Drain Diode					
*V _{SD}	-	0.78	1.0	V	V _{GS} =0V, I _S =150mA

*Pulse Test : Pulse Width ≤ 300μs, Duty Cycle ≤ 2%

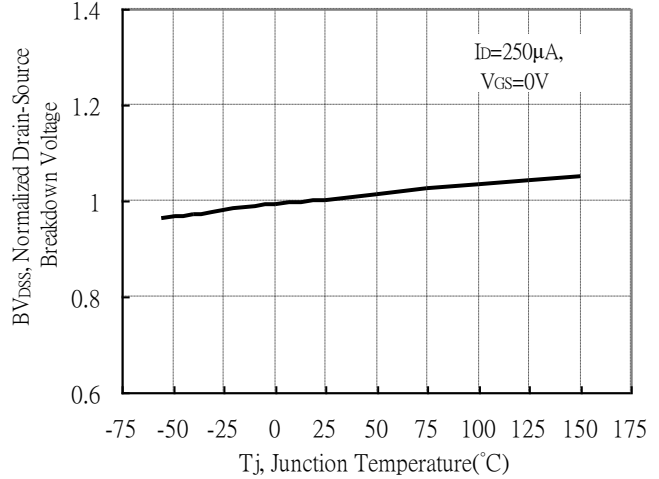


Typical Characteristics

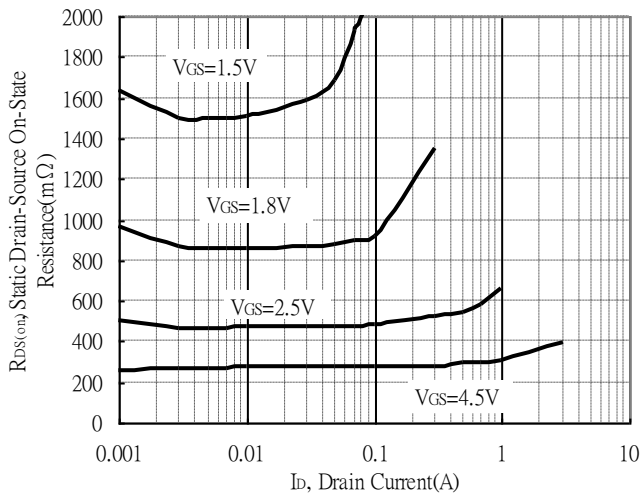
Typical Output Characteristics



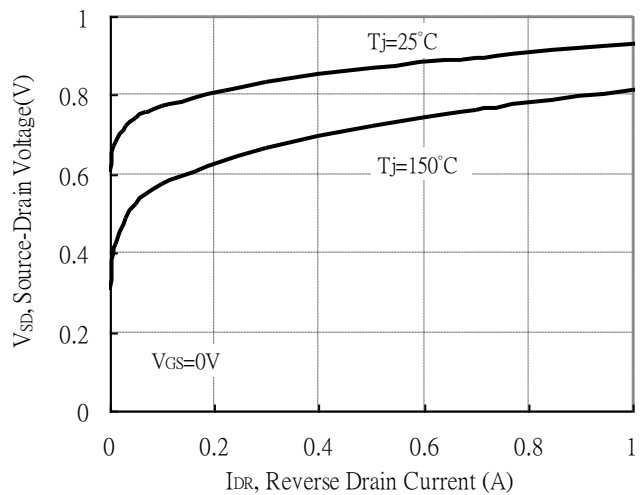
Brekdown Voltage vs Ambient Temperature



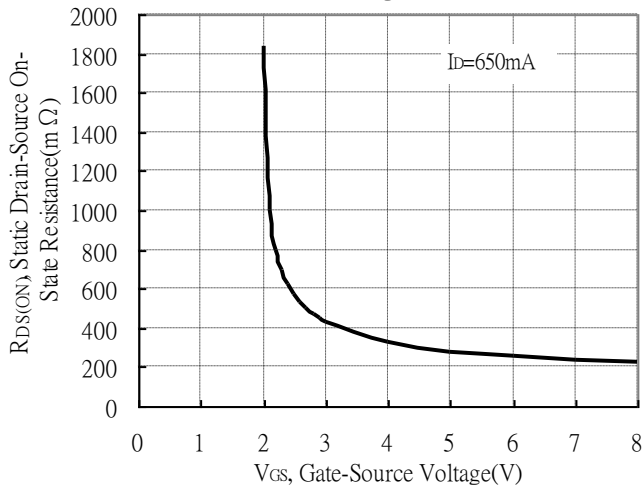
Static Drain-Source On-State resistance vs Drain Current



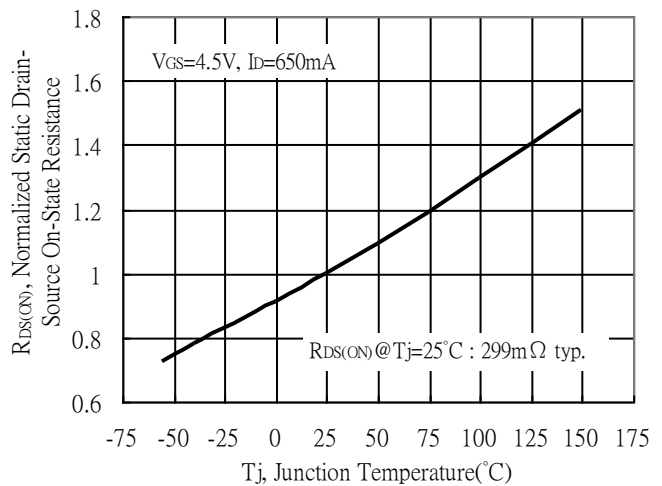
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

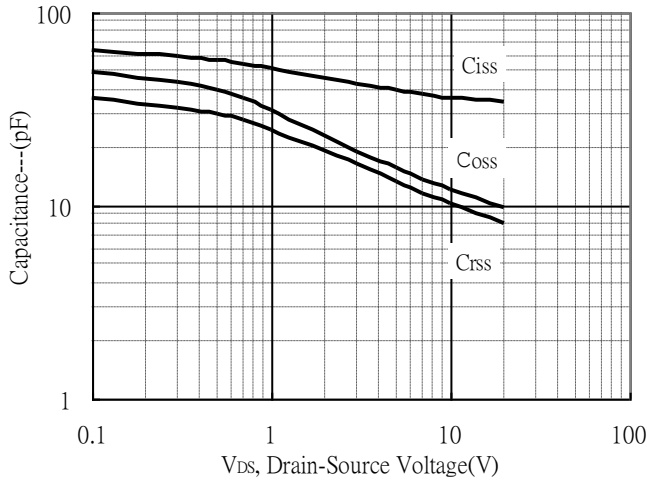


Drain-Source On-State Resistance vs Junction Temperature

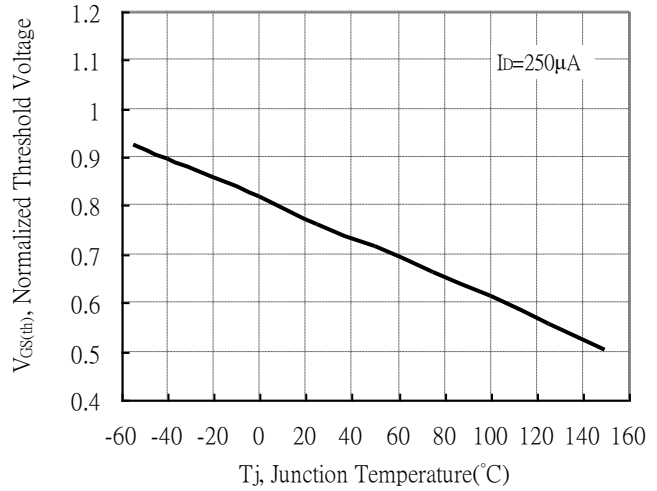


Typical Characteristics(Cont.)

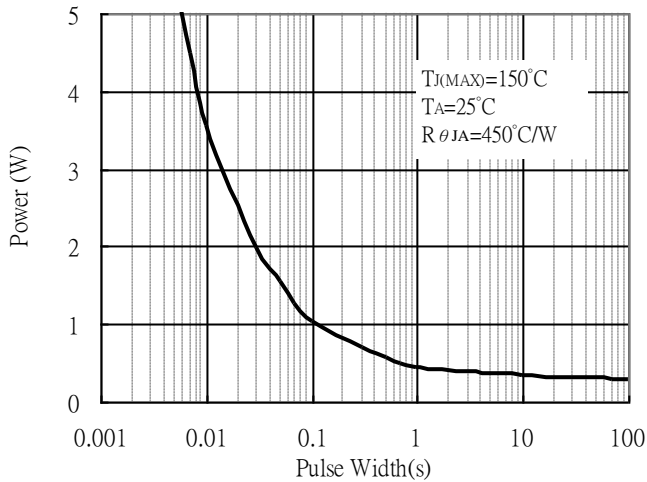
Capacitance vs Drain-to-Source Voltage



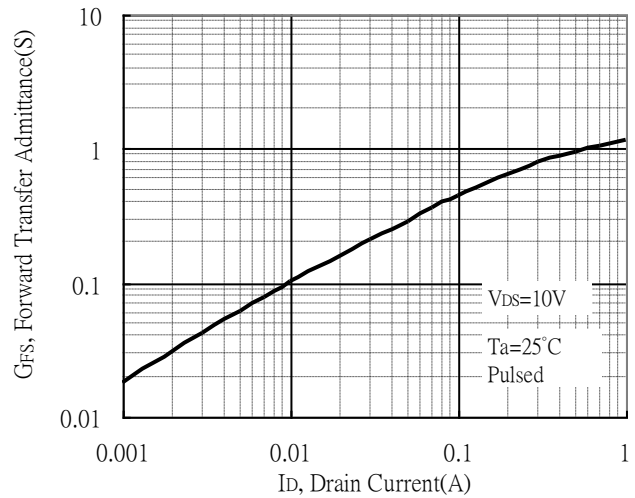
Threshold Voltage vs Junction Temperature



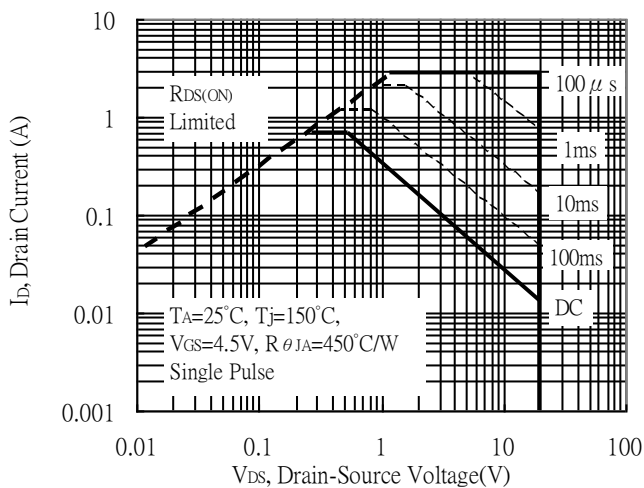
Single Pulse Power Rating, Junction to Ambient
 (Note on page 2)



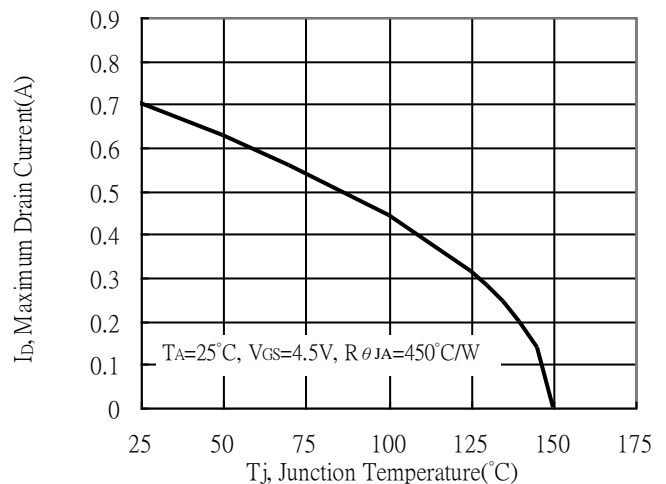
Forward Transfer Admittance vs Drain Current



Maximum Safe Operating Area

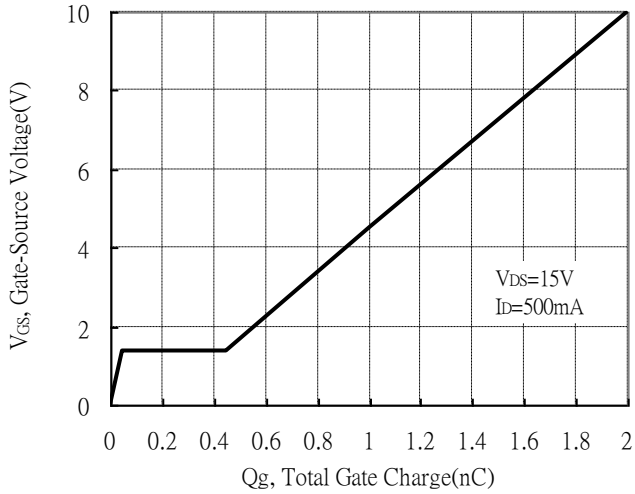


Maximum Drain Current vs Junction Temperature

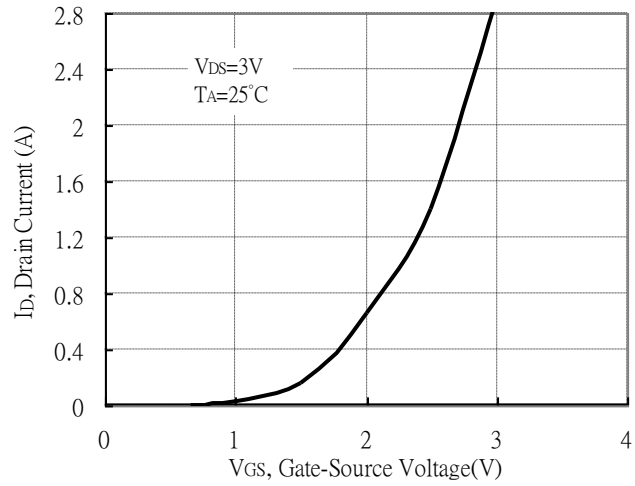


Typical Characteristics(Cont.)

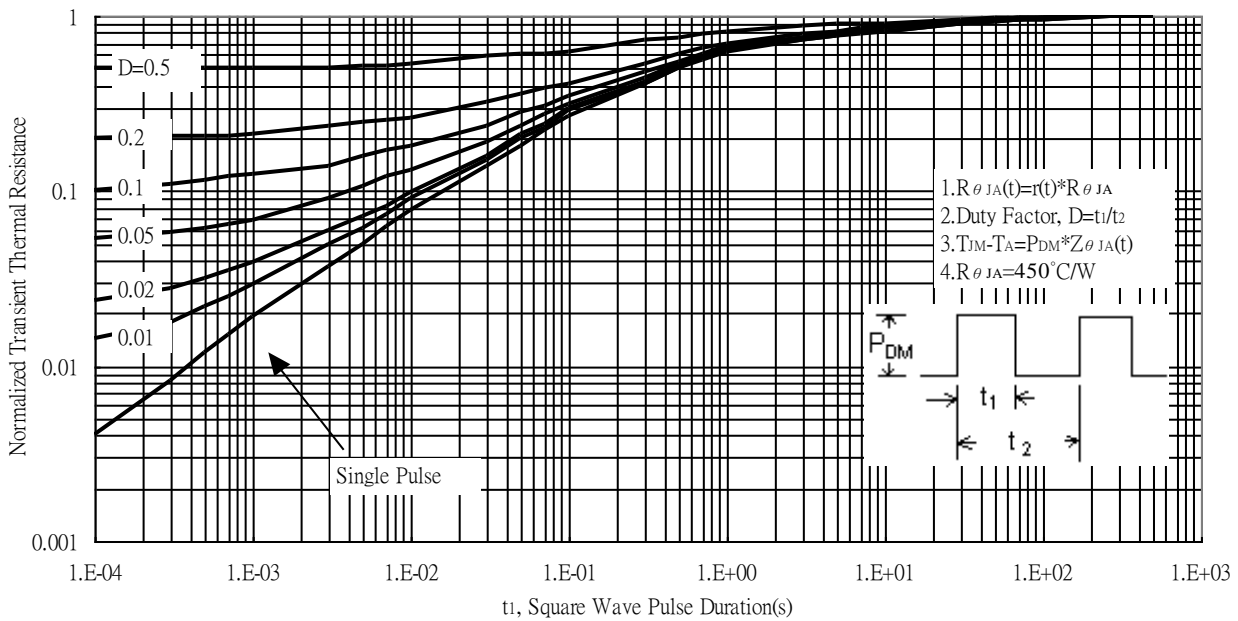
Gate Charge Characteristics



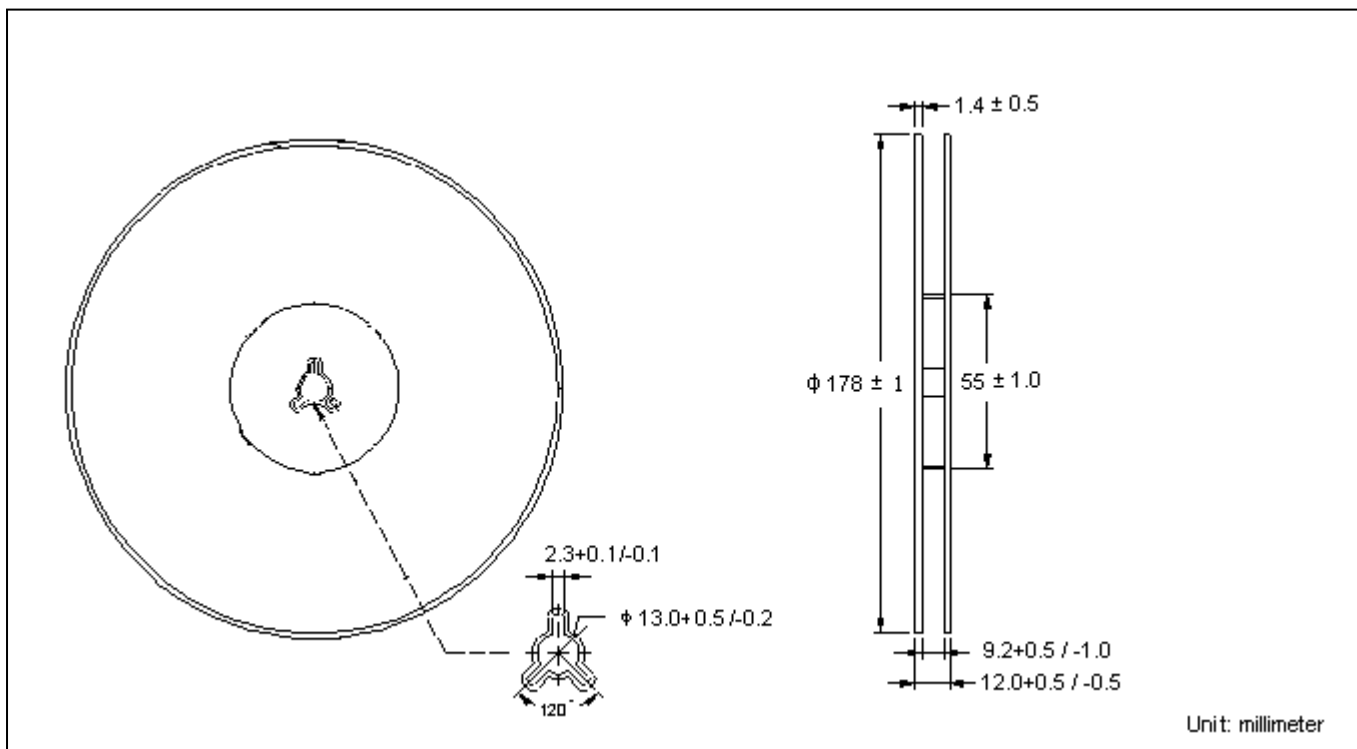
Typical Transfer Characteristics



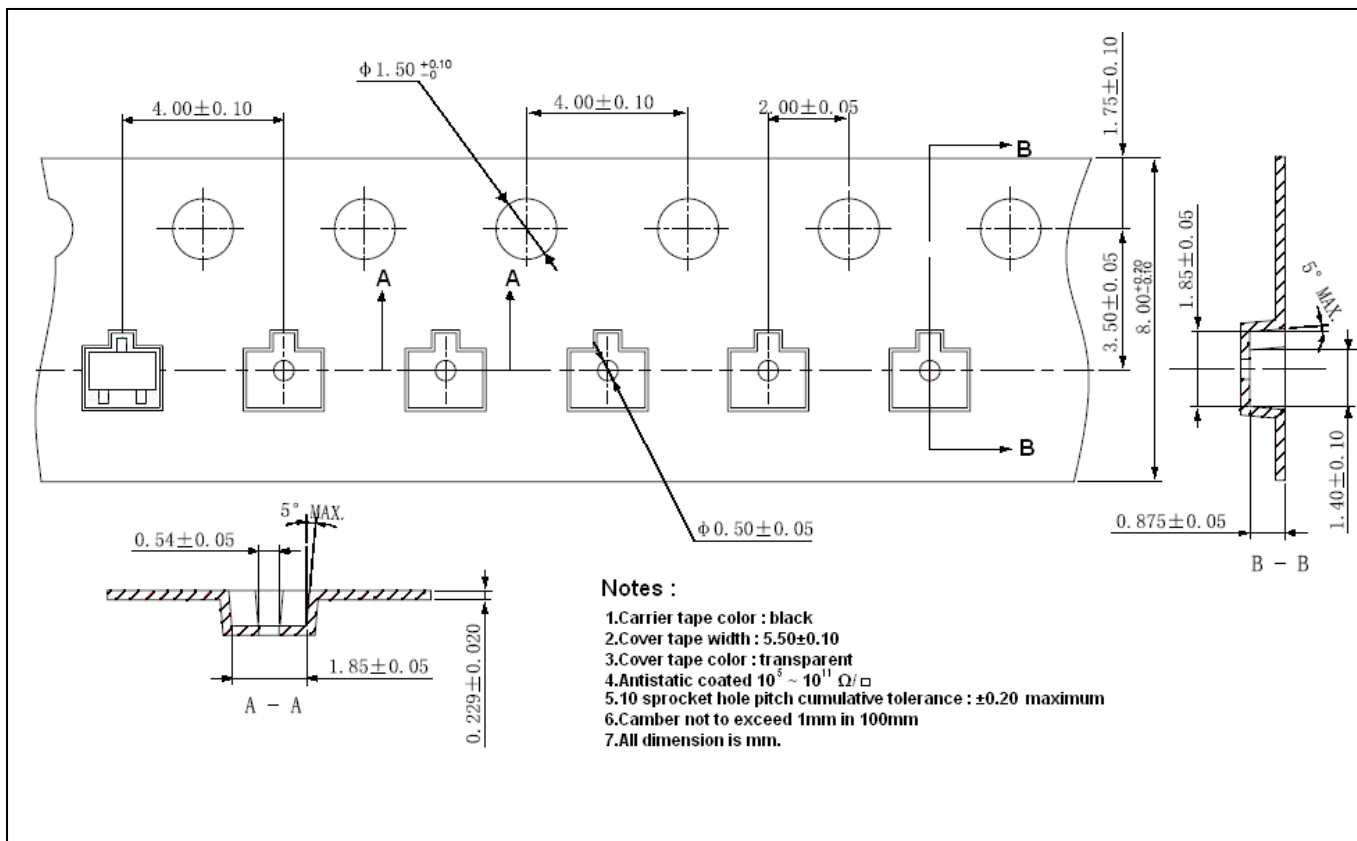
Transient Thermal Response Curves



Reel Dimension

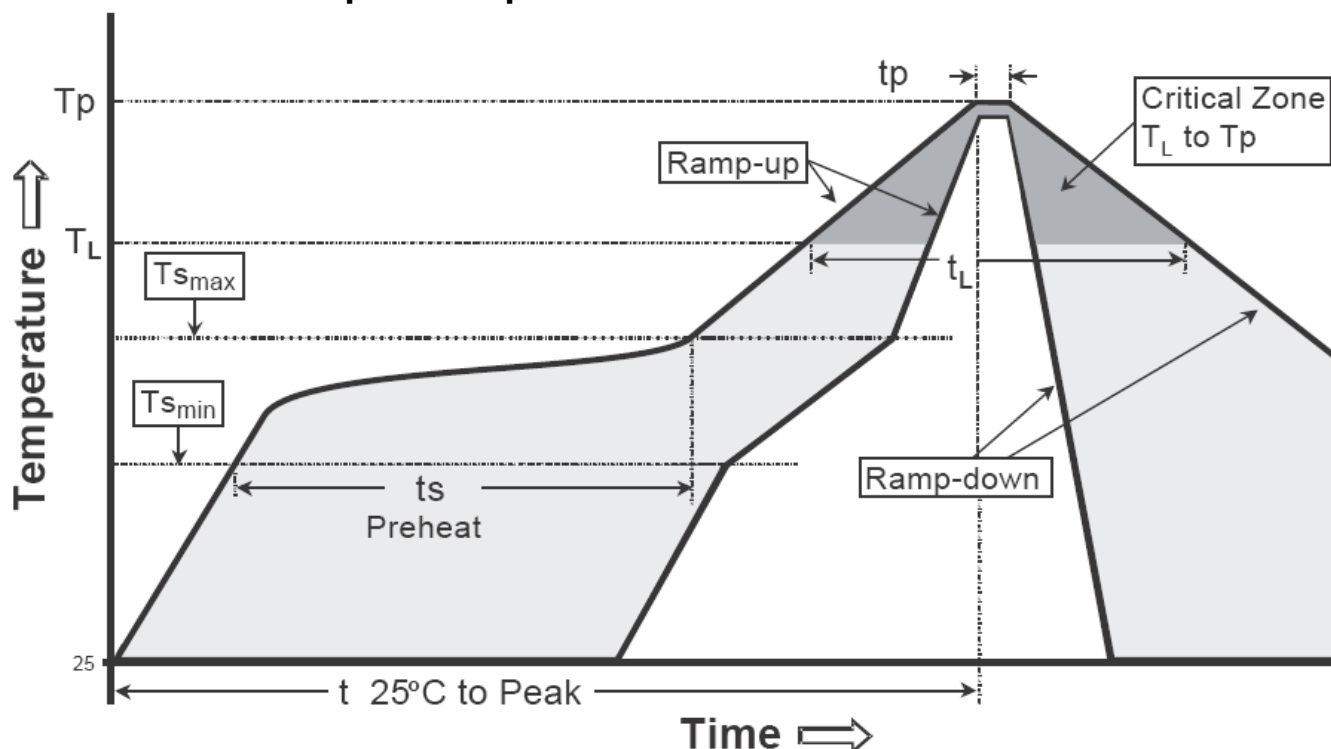


Carrier Tape Dimension



Recommended wave soldering condition

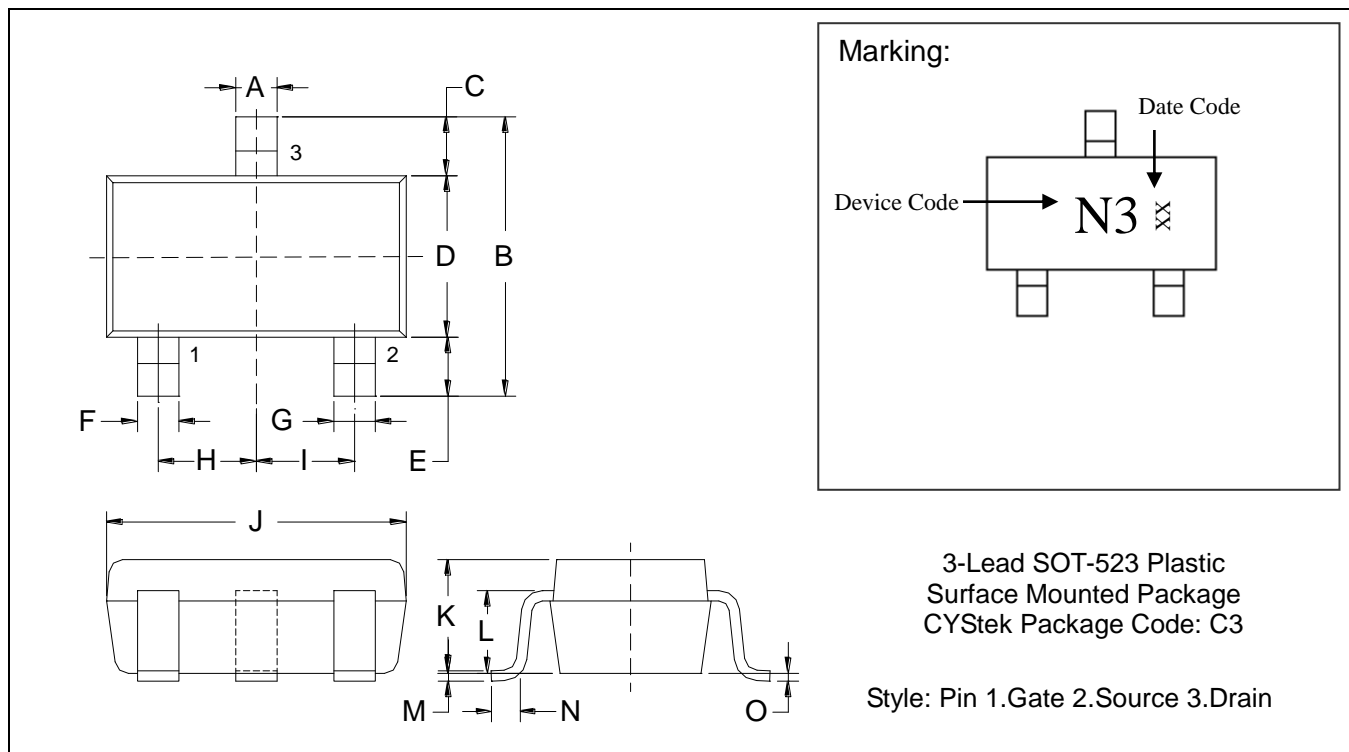
Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

Recommended temperature profile for IR reflow


Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(Ts min)	100°C	150°C
-Temperature Max(Ts max)	150°C	200°C
-Time(ts min to ts max)	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (TL)	183°C	217°C
- Time (tL)	60-150 seconds	60-150 seconds
Peak Temperature(TP)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

SOT-523 Dimension



*: Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.0079	0.0157	0.20	0.40	I	*0.0197	-	*0.50	-
B	0.0591	0.0669	1.50	1.70	J	0.0610	0.0650	1.55	1.65
C	0.0118	0.0197	0.30	0.50	K	0.0276	0.0315	0.70	0.80
D	0.0295	0.0335	0.75	0.85	L	0.0224	0.0248	0.57	0.63
E	0.0118	0.0197	0.30	0.50	M	0.0020	0.0059	0.05	0.15
F	0.0039	0.0118	0.10	0.30	N	0.0039	0.0118	0.10	0.30
G	0.0039	0.0118	0.10	0.30	O	0	0.0031	0	0.08
H	*0.0197	-	*0.50	-					

Notes: 1.Controlling dimension: millimeters.
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0

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