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PNP
2N5875, 2N5876
 NPN
2N5877, 2N5878

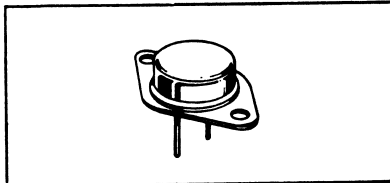
**COMPLEMENTARY SILICON
 HIGH-POWER TRANSISTORS**

... designed for general-purpose power amplifier and switching applications.

- Low Collector-Emitter Saturation Voltage –
 $V_{CE(sat)} = 1.0 \text{ Vdc (Max) @ } I_C = 5.0 \text{ Adc}$
- Low Leakage Current –
 $I_{CEX} = 0.5 \text{ mAdc (Max) @ Rated Voltage}$
- Excellent DC Current Gain –
 $h_{FE} = 20 \text{ (Min) @ } I_C = 4.0 \text{ Adc}$
- High Current Gain – Bandwidth Product –
 $f_T = 4.0 \text{ MHz (Min) @ } I_C = 0.5 \text{ A}$

**10 AMPERE
 COMPLEMENTARY SILICON
 POWER TRANSISTORS**

**60-80 VOLTS
 150 WATTS**

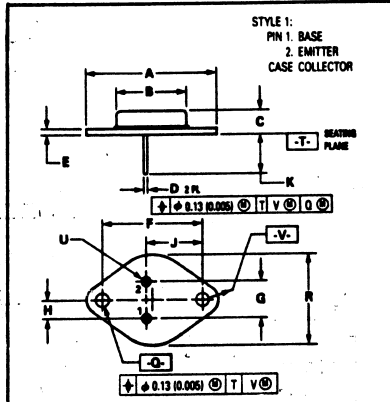
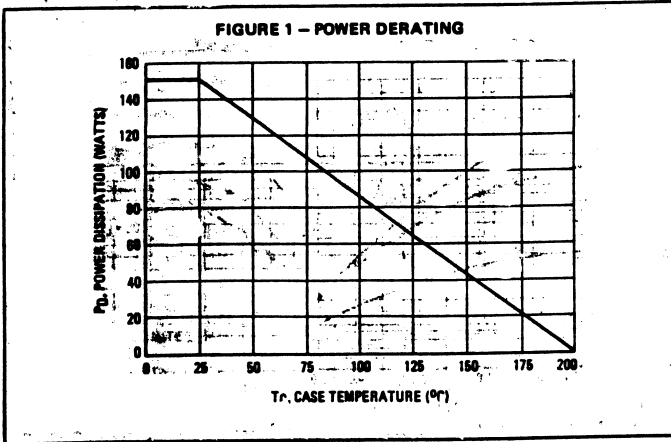


***MAXIMUM RATINGS**

Rating	Symbol	2N5875 2N5877	2N5876 2N5878	Unit
Collector-Emitter Voltage	V_{CEO}	60	80	Vdc
Collector-Base Voltage	V_{CB}	60	80	Vdc
Emitter-Base Voltage	V_{EB}	5.0		Vdc
Collector Current – Continuous Peak	I_C	10	20	Adc
Base Current	I_B	4.0		Adc
Total Device Dissipation @ $T_C = 25^\circ\text{C}$ Derate above 25°C	P_D	150	0.857	Watts W/ $^\circ\text{C}$
Operating and Storage Junction Temperature Range	T_J, T_{stg}	-65 to +200		$^\circ\text{C}$

THERMAL CHARACTERISTICS

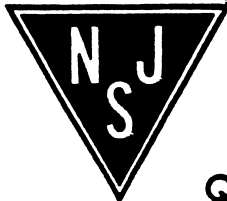
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	θ_{JC}	1.17	$^\circ\text{C/W}$



NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. ALL RULES AND NOTES ASSOCIATED WITH REFERENCED TO-204AA OUTLINE SHALL APPLY.

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	—	28.37	—	1.120
B	—	21.00	—	0.830
C	0.35	0.25	0.250	0.325
D	0.97	1.00	0.038	0.043
E	1.40	1.77	0.055	0.070
F	30.15 BSC		1.187 BSC	
G	10.92 BSC		0.430 BSC	
H	5.40 BSC		0.215 BSC	
J	16.80 BSC		0.665 BSC	
K	11.18	12.19	0.440	0.480
O	3.84	4.19	0.151	0.165
R	—	28.97	—	1.050
U	4.83	5.33	0.190	0.210
V	3.84	4.19	0.151	0.165

**CASE 1-08
 TO-204AA
 (TO-3)**



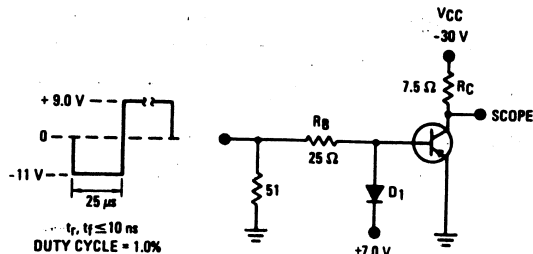
2N5875, 2N5876 PNP, 2N5877, 2N5878 NPN

***ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)**

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector-Emitter Sustaining Voltage (1) (I _C = 200 mA, I _B = 0)	V _{CE(sus)}	60	80	Vdc
Collector Cutoff Current (V _{CE} = 30 Vdc, I _B = 0) (V _{CE} = 40 Vdc, I _B = 0)	I _{CEO}		1.0 1.0	mA
Collector Cutoff Current (V _{CE} = 60 Vdc, V _{BE(off)} = 1.5 Vdc) (V _{CE} = 80 Vdc, V _{BE(off)} = 1.5 Vdc) (V _{CE} = 80 Vdc, V _{BE(off)} = 1.5 Vdc, T _C = 150°C) (V _{CE} = 80 Vdc, V _{BE(off)} = 1.5 Vdc, T _C = 150°C)	I _{CEx}		0.5 0.5 5.0 5.0	mA
Collector Cutoff Current (V _{CB} = 60 Vdc, I _E = 0) (V _{CB} = 80 Vdc, I _E = 0)	I _{CBO}		1.5 0.5	mA
Emitter Cutoff Current (V _{EB} = 5.0 Vdc, I _E = 0)	I _{EBO}		1.0	mA
ON CHARACTERISTICS				
DC Current Gain (1) (I _C = 1.0 A, V _{CE} = 4.0 Vdc) (I _C = 4.0 A, V _{CE} = 4.0 Vdc) (I _C = 10 A, V _{CE} = 4.0 Vdc)	h _{FE}	35 20 4.0	100	
Collector-Emitter Saturation Voltage (1) (I _C = 5.0 A, I _B = 0.5 A) (I _C = 10 A, I _B = 2.5 A)	V _{CE(sat)}		1.0 3.0	Vdc
Base-Emitter Saturation Voltage (1) (I _C = 10 A, I _B = 2.5 A)	V _{BE(sat)}		2.5	Vdc
Base-Emitter On Voltage (1) (I _C = 4.0 A, V _{CE} = 4.0 Vdc)	V _{BE(on)}		1.5	Vdc
DYNAMIC CHARACTERISTICS				
Current-Gain - Bandwidth Product (2) (I _C = 0.5 A, V _{CE} = 10 Vdc, f _{test} = 1.0 MHz)	f _T	4.0		MHz
Output Capacitance (V _{CB} = 10 Vdc, I _E = 0, f = 1.0 MHz)	C _{ob}		500 300	pF
Small-Signal Current Gain (I _C = 1.0 A, V _{CE} = 4.0 Vdc, f = 1.0 kHz)	h _{fe}	20		
SWITCHING CHARACTERISTICS				
Rise Time	t _r		0.7	μs
Storage Time	t _s		1.0	μs
Fall Time	t _f		0.8	μs

*Indicates JEDEC Registered Data.
 (1) Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2.0%.
 (2) f_T = |h_{fe}| * f_{test}

FIGURE 2 - SWITCHING TIME TEST CIRCUIT



For NPN test circuit, reverse all polarities.

FOR CURVES OF FIGURES 3 and 6, R_B and R_C ARE VARIED TO OBTAIN DESIRED CURRENT LEVELS
 D₁ MUST BE FAST RECOVERY TYPE, e.g. MB05300 USED ABOVE I_B ~ 100 mA
 MSD0100 USED BELOW I_B ~ 100 mA

FIGURE 3 - TURN-ON TIME

