

NJM2930

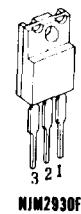
The NJM2930 3-terminal positive voltage regulator features an ability to source 150mA of output current (100mA: L-Type) with an input-output differential of 0.6V or less. Efficient use of low input voltages obtained, for example, from an automotive battery during cold crank conditions, allows 5V circuitry to be properly powered with supply voltages as low as 5.6V.

Familiar regulator features such as current limit and thermal overload protection are also provided.

■ Package Outline**■ Absolute Maximum Ratings** ($T_a = 25^\circ\text{C}$)

Operating Input Voltage Range	V_{IN}	26V
Input Overvoltage Protection	V_{PR}	40V
Input Reverse Voltage	V_{INR1} (100ms)	-12V
Input Reverse Voltage	$V_{INR2}(\text{DC})$	-6V
Maximum Output Current	$I_{OM}(\text{TO-92})$ (TO-220F)	100mA 150mA
Power Dissipation	P_D (TO-92) (TO-220F) (Note)	500mW 7.5W
Operating Temperature Range	T_{opr}	-30~75°C
Storage Temperature Range	T_{sg}	-40~125°C

(TO-220F)



(TO-92)



1. Output
2. GND
3. Input

(note) Case Temperature: $T_{case} \leq 75^\circ\text{C}$, Thermal Resistance: $\theta_{jk} = 5^\circ\text{C}/\text{W}$ TYP.

■ Electrical Characteristics

(All characteristics except noise voltage and ripple rejection ratio are measured using pulse techniques.)

NJM2930 05 ($V_{IN} = 14\text{V}$, $C_2 = 10\mu\text{F}$, $T_j = 25^\circ\text{C}$)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Output Voltage	V_O	$6V \leq V_{IN} \leq 26V$, $5mA \leq I_O \leq 100mA$	4.5	5	5.5	V
Line Regulation	$\Delta V_O - V_I$	$9V \leq V_{IN} \leq 16V$, $I_O = 5mA$	—	7	25	mV
	$\Delta V_O - V_I$	$6V \leq V_{IN} \leq 26V$, $I_O = 5mA$	—	30	80	mV
Load Regulation	$\Delta V_O - I_O$	$5mA \leq I_O \leq 150mA$	—	14	50	mV
Quiescent Current	I_Q1	$I_O = 10mA$	—	4	7	mA
	I_Q2	$I_O = 150mA$	—	30	40	mA
Dropout Voltage	$\Delta V_I - O$	$I_O = 150mA$	—	0.3	0.6	V
Output Noise Voltage	V_{NO}	$10Hz \sim 100kHz$, $I_O = 150mA$	—	100	—	μV
Ripple Rejection	RR	$f = 120Hz$, $I_O = 150mA$	—	60	—	dB

NJM2930L 05 ($V_{IN} = 14\text{V}$, $C_2 = 10\mu\text{F}$, $T_j = 25^\circ\text{C}$)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Output Voltage	V_O	$6V \leq V_{IN} \leq 26V$, $5mA \leq I_O \leq 100mA$	4.5	5	5.5	V
Line Regulation	$\Delta V_O - V_I$	$9V \leq V_{IN} \leq 16V$, $I_O = 5mA$	—	7	25	mV
	$\Delta V_O - V_I$	$6V \leq V_{IN} \leq 26V$, $I_O = 5mA$	—	30	80	mV
Load Regulation	$\Delta V_O - I_O$	$5mA \leq I_O \leq 100mA$	—	14	50	mV
Quiescent Current	I_Q1	$I_O = 10mA$	—	4	7	mA
	I_Q2	$I_O = 100mA$	—	25	40	mA
Dropout Voltage	$\Delta V_I - O$	$I_O = 100mA$	—	0.25	0.6	V
Output Noise Voltage	V_{NO}	$10Hz \sim 100kHz$, $I_O = 40mA$, $V_{IN} = 10V$	—	100	—	μV
Ripple Rejection	RR	$f = 120Hz$, $I_O = 40mA$, V_{IN}	—	60	—	dB

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NJM2930-08 ($V_{IN}=14V$, $C_2=10\mu F$, $T_j = 25^\circ C$)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Output Voltage	V_o	$9.4V \leq V_{IN} \leq 26V$, $5mA \leq I_o \leq 150mA$	7.2	8	8.8	V
Line Regulation	$\Delta V_o - V_l$	$9.4V \leq V_{IN} \leq 16V$, $I_o = 5mA$	—	12	50	mV
	$\Delta V_o - V_l$	$9.4V \leq V_{IN} \leq 26V$, $I_o = 5mA$	—	50	100	mV
Load Regulation	$\Delta V_o - I_o$	$5mA \leq I_o \leq 150mA$	—	25	50	mV
Quiescent Current	I_{Q1}	$I_o = 10mA$	—	4	7	mA
	I_{Q2}	$I_o = 150mA$	—	30	40	mA
Dropout Voltage	ΔV_{1-o}	$I_o = 150mA$	—	0.3	0.6	V
Output Noise Voltage	V_{NO}	$10Hz \sim 100kHz$, $I_o = 150mA$	—	140	—	μV
Ripple Rejection	RR	$f=120Hz$, $I_o = 150mA$	—	57	—	dB

NJM2930L-08 ($V_{IN}=14V$, $C_2=10\mu F$, $T_j = 25^\circ C$)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Output Voltage	V_o	$9.4V \leq V_{IN} \leq 26V$, $5mA \leq I_o \leq 100mA$	7.2	8	8.8	V
Line Regulation	$\Delta V_o - V_l$	$9.4V \leq V_{IN} \leq 16V$, $I_o = 5mA$	—	12	50	mV
	$\Delta V_o - V_l$	$9.4V \leq V_{IN} \leq 26V$, $I_o = 5mA$	—	50	100	mV
Load Regulation	$\Delta V_o - I_o$	$5mA \leq I_o \leq 100mA$	—	25	50	mV
Quiescent Current	I_{Q1}	$I_o = 10mA$	—	4	7	mA
	I_{Q2}	$I_o = 100mA$	—	25	40	mA
Dropout Voltage	ΔV_{1-o}	$I_o = 100mA$	—	0.25	0.6	V
Output Noise Voltage	V_{NO}	$10Hz \sim 100kHz$, $I_o = 40mA$	—	140	—	μV
Ripple Rejection	RR	$f=120Hz$, $I_o = 40mA$	—	57	—	dB

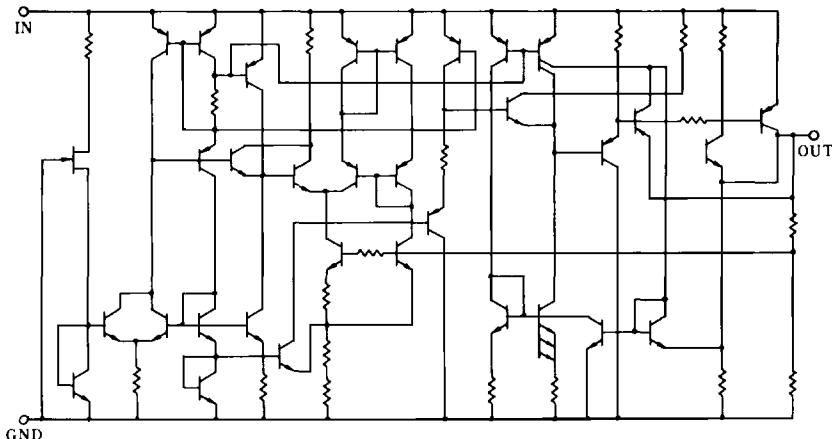
NJM2930-85 ($V_{IN}=14V$, $C_2=10\mu F$, $T_j = 25^\circ C$)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Output Voltage	V_o	$9.95V \leq V_{IN} \leq 26V$, $5mA \leq I_o \leq 150mA$	7.65	8.5	9.35	V
Line Regulation	$\Delta V_o - V_l$	$9.95V \leq V_{IN} \leq 16V$, $I_o = 5mA$	—	12	50	mV
	$\Delta V_o - V_l$	$9.95V \leq V_{IN} \leq 26V$, $I_o = 5mA$	—	50	100	mV
Load Regulation	$\Delta V_o - I_o$	$5mA \leq I_o \leq 150mA$	—	25	50	mV
Quiescent Current	I_{Q1}	$I_o = 10mA$	—	4	7	mA
	I_{Q2}	$I_o = 150mA$	—	30	40	mA
Dropout Voltage	ΔV_{1-o}	$I_o = 150mA$	—	0.3	0.6	V
Output Noise Voltage	V_{NO}	$10Hz \sim 100kHz$, $I_o = 150mA$	—	150	—	μV
Ripple Rejection	RR	$f=120Hz$, $I_o = 150mA$	—	56	—	dB

NJM2930L-85 ($V_{IN}=14V$, $C_2=10\mu F$, $T_j = 25^\circ C$)

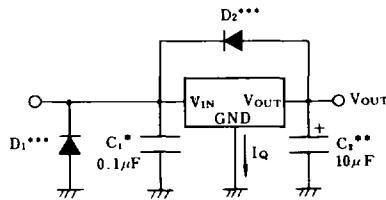
Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Output Voltage	V_o	$9.95V \leq V_{IN} \leq 26V$, $5mA \leq I_o \leq 100mA$	7.65	8.5	9.35	V
Line Regulation	$\Delta V_o - V_l$	$9.95V \leq V_{IN} \leq 16V$, $I_o = 5mA$	—	12	50	mV
	$\Delta V_o - V_l$	$9.4V \leq V_{IN} \leq 26V$, $I_o = 5mA$	—	50	100	mV
Load Regulation	$\Delta V_o - I_o$	$5mA \leq I_o \leq 100mA$	—	25	50	mV
Quiescent Current	I_{Q1}	$I_o = 10mA$	—	4	7	mA
	I_{Q2}	$I_o = 100mA$	—	25	40	mA
Dropout Voltage	ΔV_{1-o}	$I_o = 100mA$	—	0.25	0.6	V
Output Noise Voltage	V_{NO}	$10Hz \sim 100kHz$, $I_o = 40mA$	—	150	—	μV
Ripple Rejection	RR	$f=120Hz$, $I_o = 40mA$	—	56	—	dB

■ Equivalent Circuit

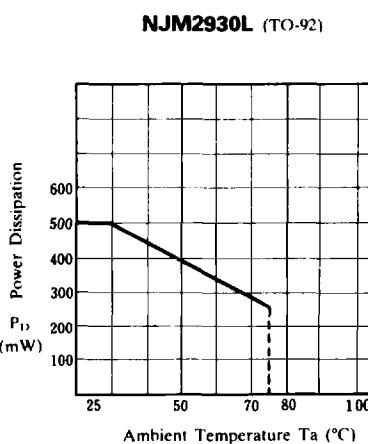
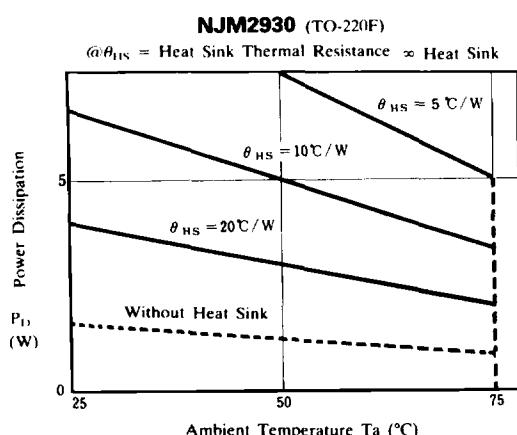


■ Standard Application Examples

- * This NJM2930 is required when the mounting position is separated from the power filter.
- ** Use an aluminum electrolytic capacitor or a tantalum capacitor as C_2 . The temperature guarantee range of capacitors should be down to -30°C . A capacity value of $10\mu\text{F}$ is a minimum requirement for improving the stability and transient response. Mount it at a position as close to the leads as possible.
- *** When application on automobile car operation, the minus pulse might be input on IC. In this case, however, the pulse might trigger to latch up. If it were that, this kind of latching up might be continued, the IC would burn up into defective in many cases. It is advisable to apply D_1, D_2 as described in the drawing, in order to prevent from making any troubles. It is important to make devices D_1, D_2 against V_{IN} to be able to stand for brake down voltage, current volume, and then less volume for V_f .

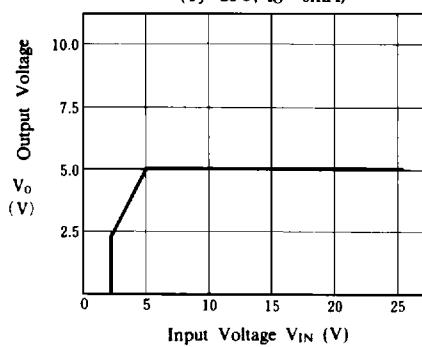


■ Power Dissipation vs. Ambient Temperature

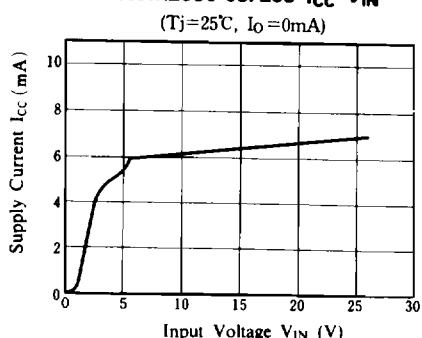


■ Typical Characteristics

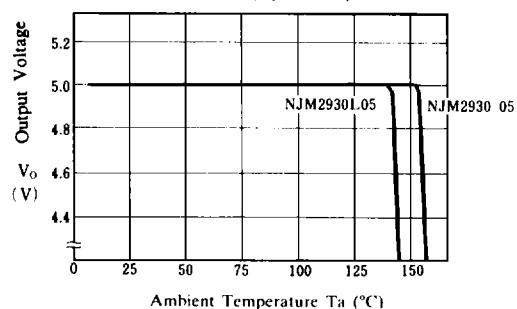
NJM2930-05/L05 Output Voltage
($T_j=25^\circ\text{C}$, $I_o=0\text{mA}$)



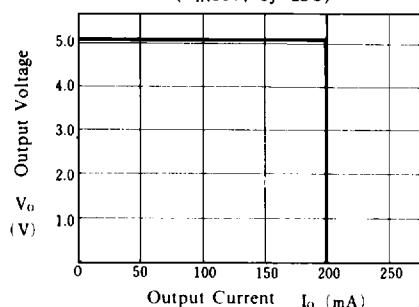
NJM2930-05/L05 $I_{CC} - V_{IN}$



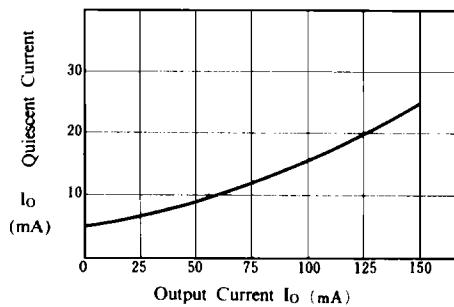
NJM2930-05/L05 Thermal Shutdown
($V_{IN}=14\text{V}$, $I_o=50\text{mA}$)



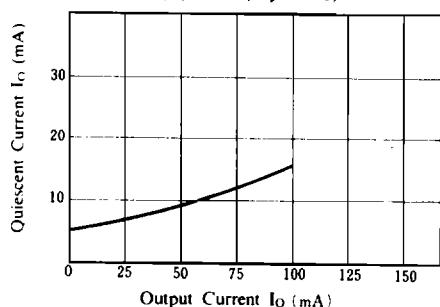
NJM2930-05/L05 Load Characteristics
($V_{IN}14\text{V}$, $T_j=25^\circ\text{C}$)



**NJM2930L05 Output Current
vs. Quiescent Current**
($V_{IN}=14\text{V}$, $T_j=25^\circ\text{C}$)



**NJM2930-05 Output Current
vs. Quiescent Current**
($V_{IN}=14\text{V}$, $T_j=25^\circ\text{C}$)



■ Typical Characteristics

