

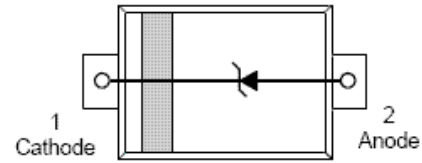
ESD Protection Diode With Ultra-Low Capacitance ESD5L5V

The ESD5L5V is designed to protect voltage sensitive components that require ultra-low capacitance from ESD and transient voltage events. Excellent clamping capability, low capacitance, low leakage, and fast response time, make these parts ideal for ESD protection on designs where board space is at a premium.

Because of its low capacitance, it is suited for use in high frequency designs such as USB high speed and antenna line applications.

FEATURES

- Ultra Low Capacitance 0.5 pF
- Low Clamping Voltage
- Small Body Outline Dimensions:
- Stand-off Voltage: 5 V
- Low Leakage
- Response Time is Typically < 1.0 ns
- IEC61000-4-2 Level 4 ESD Protection
- This is a Pb-Free Device



Complies with the following standards

IEC61000-4-2 Level 4

15 kV (air discharge)

8 kV(contact discharge)

MIL STD 883E - Method 3015-7 Class 3

25 kV HBM (Human Body Model)

Maximum ratings (limiting value)

Parameter	Symbol	Value	Unit
IEC 61000-4-2 (ESD) Contact		8	KV
ESD Voltage Per Human Body Model		25	KV
Per Machine Model	TJ	400.0	V
Peak Pulse Power ($t_p = 8/20\mu s$) @ $T_A = 25^\circ C$	PD	100	W
Junction and Storage Temperature Range	TJ,TSTG	-55 to 150	$^\circ C$
Lead Solder Temperature – Maximum (10 Second Duration)	TL	260	$^\circ C$

Electrical Characteristics

Part Numbers	VBR min	IT	VRWM	VF Max	IF	IR	Cj TYP
	V	mA	V	V	mA	uA	PF
ESD5L5V	6.0	1	5	1.0	10	1	0.9

1. Capacitance is measured at $f=1\text{MHz}$, $V_R=0\text{V}$, $T_A=25^\circ\text{C}$.
2. VBR is measured with a pulse test current I_T at an ambient temperature of 25°C .

Symbol	Parameter
I_{PP}	Maximum Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_{PP}
V_{RWM}	Working Peak Reverse Voltage
I_R	Maximum Reverse Leakage Current @ V_{RWM}
I_T	Test Current
V_{BR}	Breakdown Voltage @ I_T

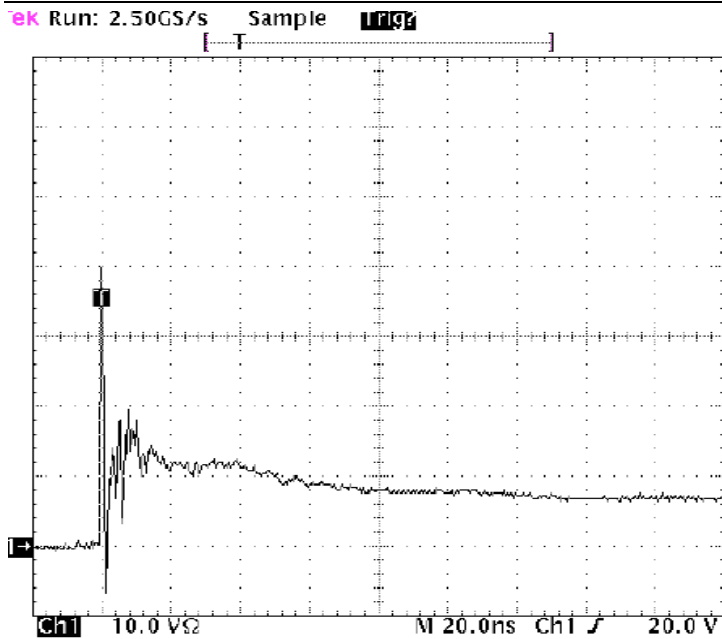
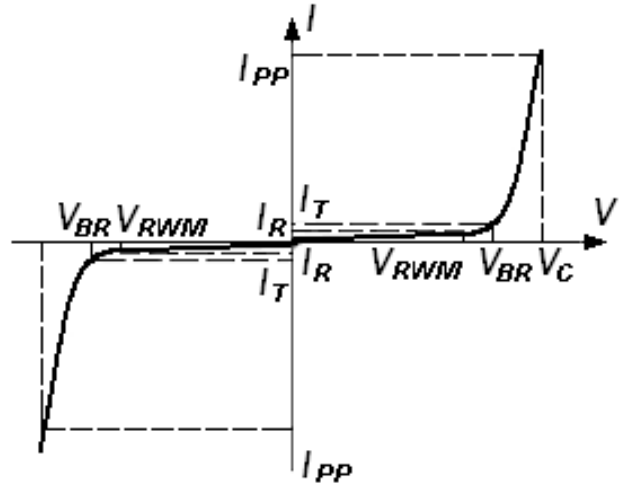


Fig 1. Positive 8kV contact per IEC61000-4-2

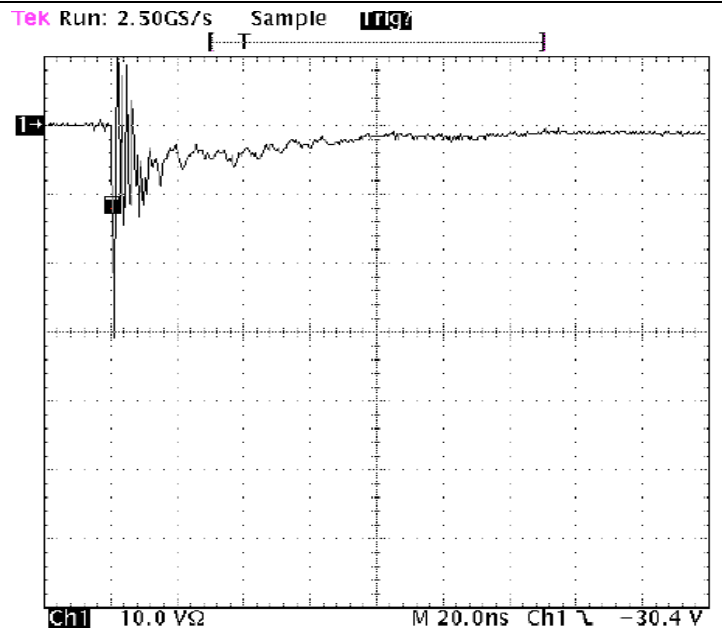
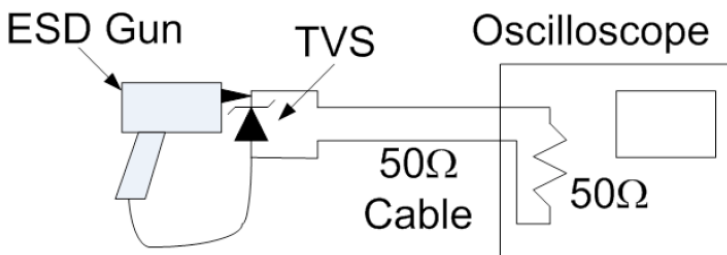
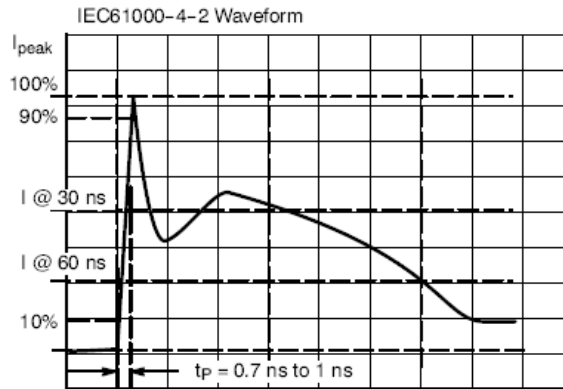


Fig 2. Negative 8kV contact per IEC 61000-4-2-SESD5L5V 61000-4-2

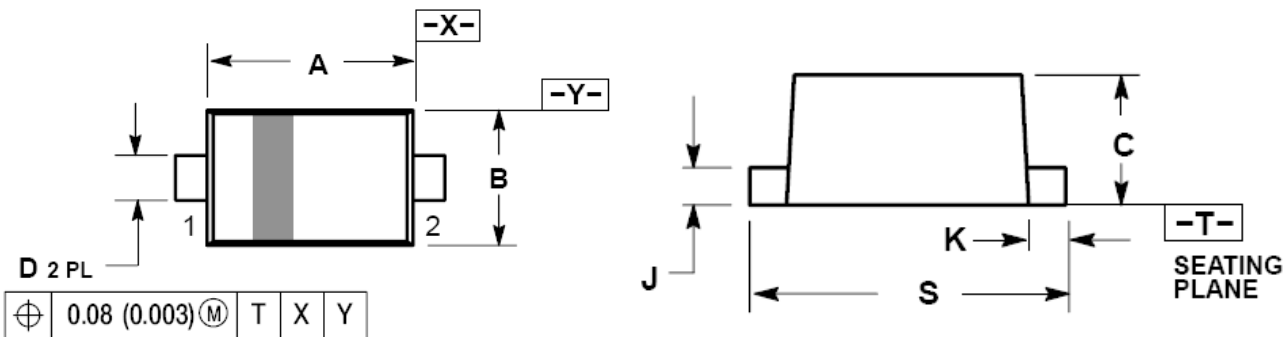
Figure 4. Diagram of ESD Test Setup

IEC 61000-4-2 Spec.

Level	Test Voltage (kV)	First Peak Current (A)	Current at 30 ns (A)	Current at 60 ns (A)
1	2	7.5	4	2
2	4	15	8	4
3	6	22.5	12	6
4	8	30	16	8



SOD-523 package Information



Dim	Millimeters			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	1.10	1.20	1.30	0.043	0.047	0.051
B	0.70	0.80	0.90	0.028	0.032	0.035
C	0.50	0.60	0.70	0.020	0.024	0.028
D	0.25	0.30	0.35	0.010	0.012	0.014
J	0.07	0.14	0.20	0.0028	0.0055	0.0079
K	0.15	0.20	0.25	0.006	0.008	0.010
S	1.50	1.60	1.70	0.059	0.063	0.067