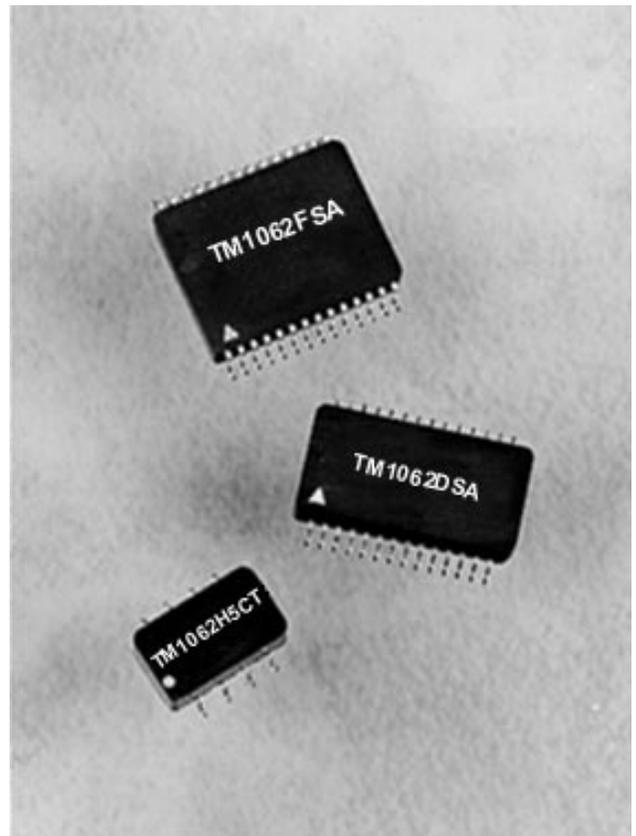


# Copperhead™ Series Fibre Channel transceiver line interface module

*Fiber speed data and communications  
over 100+ meters of copper*

Designed to be inter-operable with existing ECL fiber-transceiver drivers, Copperhead™ fibre channel copper transceiver line interface modules drive high frequency (fiber speed) signals over copper media. Models TM133 thru TM1062 are designed to deliver fibre channel capabilities to the workstation over shielded twisted pair (STP), twinax, mini coax, and video coaxial cable. Model TM1250 provides communications for short haul Gigabit Ethernet applications, typically over twinax or coaxial cable. All Copperhead™ fibre channel copper transceiver line interface modules are supplied as surface mount gull wing packages.



## Features

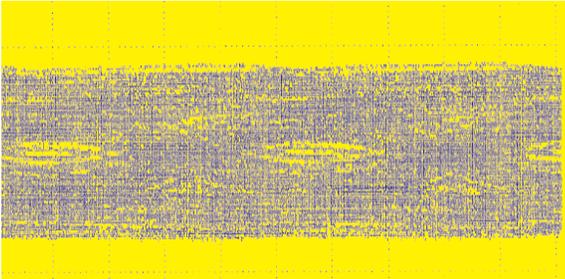
- High performance, low cost alternative to completely fiber systems
- Enhanced distance and signal clarity with complete inter-operability per ANSI X3T11, FC-PH (FC-0 Rev 9.1)
- Small, discrete passive design incorporates equalizer, transformers, and filters for "short haul" applications; industrial temp -40°C to +85°C (-55°C to +125°C available)
- Compact active models for "long haul" applications; -40°C to +85°C and -55° to +125°
- Low EMI emissions; designed for FCC Class B compliance and use over shielded twisted pair cable with high radiated electromagnetic susceptibility - 10 V/m
- Low transmit/receive jitter
- Small footprint for surface mounting
- Low power dissipation; 450 mW typical
- ECL logic interface - directly compatible with proposed standards

## Applications

- Fibre Channel
- Gigabit Ethernet
- SONET

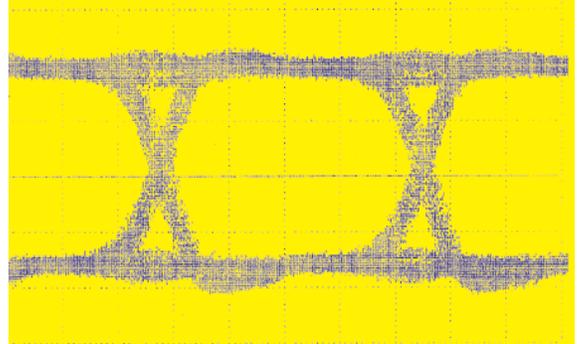


## BEFORE (unequalized)



(Figure 1) Actual oscilloscope tracing of 1.0265 Gb/s unequalized input to receiver pins 13 and 14 after transmission over 30 meters of twinax cable.

## AFTER (equalized for Fibre Channel)



(Figure 2) Tracing of the same signal after equalization. Receiver output measured at pins 2 and 3.

### Technical description

Copperhead™ fibre channel transceiver line interface modules (LIM) take differential ECL level NRZ data signal from the available fibre channel (or Gigabit Ethernet) silicon. The signal is then converted to drive level, pre-conditioned with an output filter, and transmitted over the cable through an interfacing wideband pulse transformer.

The transformer converts the signal to current and voltage levels, with exceptionally low transmit jitter, suitable for transmission on STP, twinax or coaxial cable. The transceiver matches the impedance of both the ECL circuit and the cable plant, providing a resultant output signal with minimum jitter and return loss.

The receiver portion of the transceiver LIM takes the signal from the media cable and equalizes it by using a unique wideband pulse transformer and circuitry. This initiates the conversion process of reducing the composite signal to ECL level NRZ for the next level fibre channel silicon.

### Benefits of transformer coupling

The pulse transformers used in Copperhead™ transceiver LIMs provide high isolation and optimum damping of transients. They also eliminate DC components in the signal and provide common mode signal rejection. The transformer is also used to match the load to the source and provide maximum power transfer, and also to prevent reflections from transmission line effects.



## Product Qualification Test Outline

### I. Subgroup I (all devices - 40 pieces)

#### Electrical tests at 25°

- Receiver (R<sub>x</sub>) tests
  - Jitter (total peak-to-peak)
- Transmit (T<sub>x</sub>) tests
  - CMI Mask Test (rise time, fall time, and amplitude)
  - Jitter (total peak-to-peak)
- Total power supply current

### II. Subgroup II (4 pieces)

#### External visual inspection

- Resistance to solvents per MIL-STD-202, Method 215H
  - 1 part isopropyl alcohol, 3 parts mineral spirits
  - Trichloroethane
  - Terpene defluxer with a minimum of 90% d-limonene and 10% surfactant
  - 42 parts water, 1 part propylene glycol monomethyl ether, 1 part monoethanolamine
- Terminal strength per MIL-STD-883, Method 2004.5
  - Lead tension

### III. Subgroup III (4 pieces)

#### Solderability

- Per MIL-STD-202, Method 208, with a 4-hour steam age

### IV. Subgroup IV (6 pieces)

#### Resistance to soldering heat

- Hand solder (2 pieces), solder dip temperature of 430 ±5° C for 3 seconds maximum
- Vapor phase reflow (4 pieces) for a period of 60 seconds at a temperature of 215 +5/-0° C

### V. Subgroup V (6 pieces)

#### Thermal shock

- Per MIL-STD-202, Method 107, Condition A-1, except temperature range shall be -20° C to +125° C, 25 cycles

#### Vibration

- Per MIL-STD-202, Method 204D, Condition D, 20G peak acceleration, 10 Hz to 2 KHz and return to 10 Hz traversed in 6 minutes

#### Shock

- Per MIL-STD-202, Method 213B, Condition J, 30G, 11msec shock, 3 shocks in each direction along mutually perpendicular axes

### VI. Subgroup VI (6 pieces)

#### Life Test

- Per MIL-STD-202, Method 108, 1000 hours at a temperature of 75° C

### VII. Subgroup VII (8 pieces)

#### Humidity Test

- Humidity test @ 40° C, and 90% humidity (4 pieces)

### VIII. Subgroup VIII (6 pieces)

#### Electrostatic Discharge, ESD Classification

- The ESD classification to be derived from ESD testing per IEC801-2, EN50082-1, Criteria B

**NOTE: There shall be 0 (zero) rejects allowable for each of the subgroups.**

## EMC Immunity and Radiated Emissions Tests

- Radiated Emissions, FCC Class B, 6 dB margin
- IEC 1000-4-2 ESD Test Method Performance Criteria B, Level 2
- IEC 1000-4-3 Radiated Immunity Performance Criteria A, Level 3 (10V/m)
- IEC 10004-4 EFT Test Method Performance Criteria B, Level 3 (at I/O port)

Criteria A: The system shall continue to operate normally. The EUT shall be deemed to have passed the immunity tests if no bit errors occur when receiving data over the cable. Changes of state, reset condition, unrecoverable jab condition, blocked network, or loss of packets is unacceptable.

Criteria B: The system shall operate normally with no data errors through the loop after the test. During the test, data errors, aborted frames and collisions are acceptable. No change of operating state such as system reset or unrecoverable condition is permitted during or after the test.



**Table 1**

**Absolute Maximum Ratings**

PARAMETER	SYM	MIN	MAX	UNIT
Storage temperature	$T_s$	-55	+150	°C
Operating temperature ambient	$T_A$ $T_A$	-40 -55	+85 +125	°C °C
Power supply voltage ( MECL or PECL)	$V_{CC}$	-6.0	6.0	V
Power supply current <sup>2</sup>	$I_C$	-	95	mA
Power dissipation (total)	$P_D$	-	500	mW
Output current	$I_o$	-	100	mA
Data input voltage	$V_I$	0	$V_{CC}+0.5$	V
Differential input voltage	$V_D$	-	2.40	V
Component body temperature/time	-	-	220/60	°C/s

Note 1: Exception: for TM1062DSA1, -55° to +125°C

Note 2: Excluding external pull-down resistors

**Important: This is a hybrid device and is rated for 220°C max for 60 seconds. Contact factory for recommended solder reflow profile.**

**Table 2**

**System Jitter Budget**

Copperhead™ fibre channel copper transceivers require less than 50% (worst case) of system jitter budget

JITTER PARAMETER	TOTAL JITTER				
	133 Mb	266 Mb	531 Mb	1.062 Gb	UNIT
Data rate					
Transmit in ( PHY)	1.204	0.602	0.377	0.188	ns
Transmit out ( PHY + Transmitter)	1.356	0.678	0.414	0.207	ns
Receive out ( PHY + Transmitter + Cable + Receiver)	6.0	2.64	1.32	0.658	ns

Note: These numbers are currently under evaluation by a special Jitter Study Group in ANSI X3T11 and IEEE 802.3z.



**Table 3**

**Transmitter Electrical Characteristics**

**V<sub>CC</sub> = 4.75V to 5.25V**

PARAMETER	SYM	MIN	TYPICAL	MAX	UNIT
Input data voltage Low High	V <sub>IL</sub> V <sub>IH</sub>	V <sub>CC</sub> -2.000 V <sub>CC</sub> -1.110	V <sub>CC</sub> -1.750 V <sub>CC</sub> -0.880	V <sub>CC</sub> -1.500 V <sub>CC</sub> -0.670	V V
Input current Low <sup>1</sup> High <sup>2</sup>	I <sub>IL</sub> I <sub>IH</sub>	0.4 -	- -	- 0.4	mA mA
Data rate ( NRZ Encoding)	DR	133	-	1250	Mb/s
Differential signal level <sup>3</sup> (peak-peak)	V <sub>OT</sub>	1100 <sup>4</sup>	-	2000 <sup>4</sup>	mV
Output rise and fall time <sup>3</sup> for TM1062 (20-80%)	T <sub>R</sub> /T <sub>F</sub>	-	-	300	ps
Return loss (0.1 to 0.5 of baud rate)	S <sub>11</sub>	-12	-	-	dB
Total peak-peak transmit jitter (D <sub>J</sub> +R <sub>J</sub> ) 12-TP-EL-S 133 Mb 25-TP-EL-S 266 Mb 50-TP-EL-S 531 Mb 100-TP-EL-S 1062 Mb	T <sub>PK-PK</sub>	- - - -	500 250 125 62	750 376 188 94	ps ps ps ps

Note 1: Measured with V<sub>IL</sub> Min.

Note 2: Measured with V<sub>IH</sub> Min.

Note 3: Differential signal level (V<sub>OT</sub>), rise time (T<sub>R</sub>) and overshoot (O<sub>S</sub>) shall be measured at the transmitter output with 150 ohm differential termination as load. The data pattern for this test shall be a square wave of 0.1 bit rate.

Note 4: For TM1062DSA1; 600 mV min and 1600 mV max for all other devices.

**Table 4**

**Receiver Electrical Characteristics**

**V<sub>CC</sub> = 4.75V to 5.25V**

PARAMETER	SYM	MIN	TYPICAL	MAX	UNIT
Output data voltage Low High	V <sub>OL</sub> V <sub>OH</sub>	V <sub>CC</sub> -2.000 V <sub>CC</sub> -1.110	V <sub>CC</sub> -1.750 V <sub>CC</sub> -0.880	V <sub>CC</sub> -1.500 V <sub>CC</sub> -0.670	V V
Data rate ( NRZ Encoding)	DR	133	-	1250	Mb/s
Input sensitivity ( D21.5 idle pattern)	-	100	-	-	mV
Input differential voltage	V <sub>DIFF</sub>	-	-	2.40	V
Return loss (0.1 to 0.5 bit rate) STP	S <sub>11</sub>	-15	-17	-	dB
Total peak-peak receive jitter 133 Mb 266 Mb 531 Mb 1062 Mb	T <sub>PK-PK</sub>	- - - -	0.350 0.350 0.240 0.120	1.500 0.900 0.650 0.400	ns ns ns ns



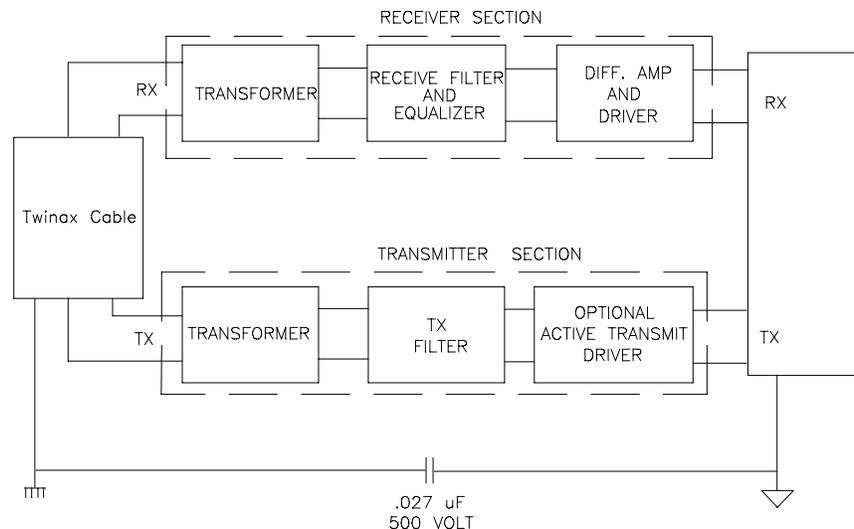
## Application Guide

The transceiver type (passive/short haul or active/long haul) is based on the cable insertion loss (attenuation) for the length of cable in question. Pulse Components Division recommends the product type/cable length combinations listed below. Specific models recommended as examples are based on installation/use within 1 Gbaud /150 ohm systems.

CABLE LENGTH	PART TYPE	EXAMPLE MODEL	DESCRIPTION
10 meters	Equalization rarely required.		For use in applications < 10 meters requiring isolation and common mode signal rejection, see T-1062 data sheet for dual (T <sub>x</sub> & R <sub>x</sub> ) pulse transformer products.
<b>Short haul:</b> 10-30 meters	Copperhead™ passive transceiver module	TM1062HSCT without active logic chip and buffering	For use in applications presenting an input voltage of no less than 400 mV (from cable) to transceiver R <sub>x</sub> . An insertion loss (S21) calculation with respect to cable length should be made in order to determine compliance. <b>If the voltage is less than 400 mV, use "long haul" device (below).</b>
<b>Long haul:</b> > 30 meters	Copperhead™ transceiver module with active logic chip and buffering for receive side only	TM1062DSA with active receive amplifier	For use in long haul applications where losses accumulate and diminish (receive) signal strength. This device requires a minimum input signal of only 100 mV (from cable) to transceiver R <sub>x</sub> .
<b>Long haul:</b> > 30 meters, enhanced transmit	Copperhead™ transceiver module with active logic chip and buffering for both receive and transmit sides	TM1062DSA1 with active transmit driver and receive amplifier	For use in long haul applications requiring buffered transmit signal. The active transmitter acts as a buffer. It reconditions the signal to the appropriate drive level. Transmitter output signal level is 1100 mV minimum.

### Block diagram for "long haul" application

Shown is an application of the Model TM1062DSA with "active receive" amplifier for a "long haul" application presenting an input voltage of less than 400 mV (from cable) to transceiver R<sub>x</sub>.





## Module Pinouts

### Pin number(s)

- 1, 28** . . . . . The positive supply for the line interface module. Connect to +5.0V for PECL applications, and to Gnd for ECL applications.
- 2, 3** . . . . .  $\overline{D}_O$ ,  $D_O$ : Differential ECL data outputs. These outputs can drive 50 ohm loads connected to  $V_{CC}$  –2.0V. Recommend 330 ohms to Gnd.
- 7, 8** . . . . . Gnd ( $V_{EE}$ ): The negative supply for the line interface module. Connect to Gnd for raised ECL (PECL) applications, and to –5.2V for standard ECL applications.
- 13, 14** . . . . .  $R_X^-$ ,  $R_X^+$  : Transformer coupled differential inputs to receiver section. For coax applications,  $R_X^-$  should be connected to shield of cable/earth Gnd;  $R_X^+$  should be connected to the center conductor. Earth Gnd should be AC coupled to DC signal Gnd using a 0.027  $\mu$ F capacitor, ~500V.
- 15, 16** . . . . .  $T_X^+$ ,  $T_X^-$  : Transformer coupled differential outputs to cable. For coax applications,  $T_X^-$  should be connected to shield of cable/earth Gnd;  $T_X^+$  should be connected to the center conductor. Earth Gnd should be AC coupled to DC signal Gnd using a 0.027  $\mu$ F capacitor, 500V.
- 21, 22** . . . . . Gnd ( $V_{EE}$ ): The negative supply for the line interface module. Connect to Gnd for raised ECL (PECL) applications, and to –5.2V for standard ECL applications.
- 23** . . . . .  $V_{BB}$  : Is an output pin, which is used for biasing up a threshold reference level, i.e. PECL (3.67V). This pin can only source 1 mA maximum.
- 24, 25** . . . . .  $\overline{D}_I$ ,  $D_I$  : Differential ECL compatible data inputs to the transmitter side of the module.
- 4, 5, 6, 9, 10, 11, 12, 17, 18, 19, 20, 26, 27** . . . These pins are "no connect;" do not apply Gnd, VCC, or signal lines to these pins.

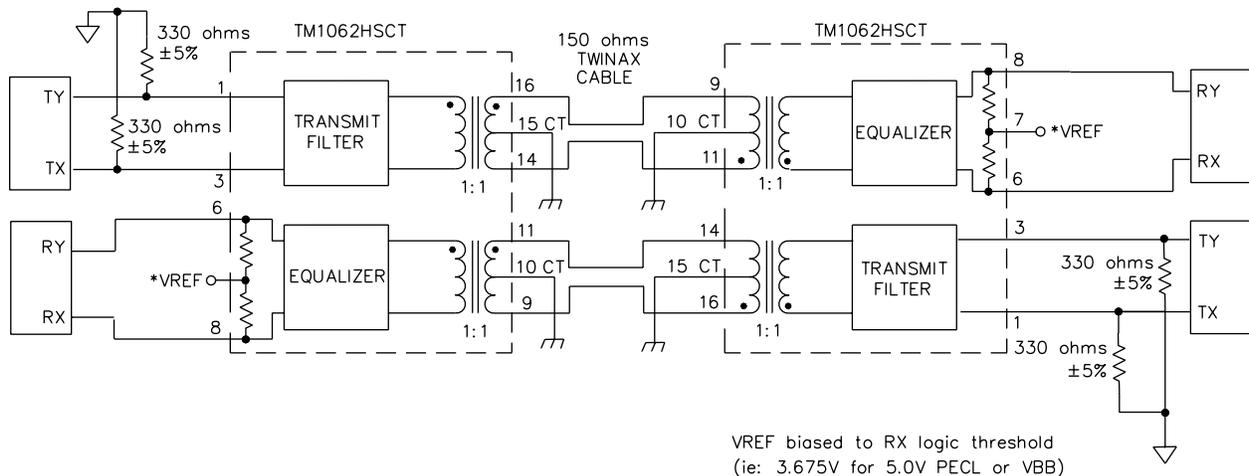
## Board Layout Considerations

- 0.1  $\mu$ F capacitors should be ceramic and placed as close to the  $V_{CC}$  pins (1 & 28) of the transceiver as possible.
- Differential lines should be of equal length.
- Data out and signal detect output lines should be as short as possible and isolated from noisy sources.
- Controlled impedance traces from pins 14 ( $R_X^+$ ) and 15 ( $T_X^+$ ) of the transceiver to the BNCs or DB9 connector must be used in order to control return loss.
- A DC signal ground plane should occupy the complete area directly beneath the transceiver LIM from pins 1-2 and 17-28.
- The earth ground plane should occupy the complete area directly beneath pins 13-14 and 15-16.
- There should be a separation of 0.050" between earth and DC ground.
- The earth ground should be AC coupled to DC ground with a 0.027  $\mu$ F capacitor on both sides of the transceiver ( $T_X$  side and  $R_X$  side).



## Block diagram for "short haul" application

Shown is an application of the Model TM1062HSCT passive transceiver LIM, used in a "short haul" application presenting an input voltage of no less than 400 mV (from cable) to transceiver RX.

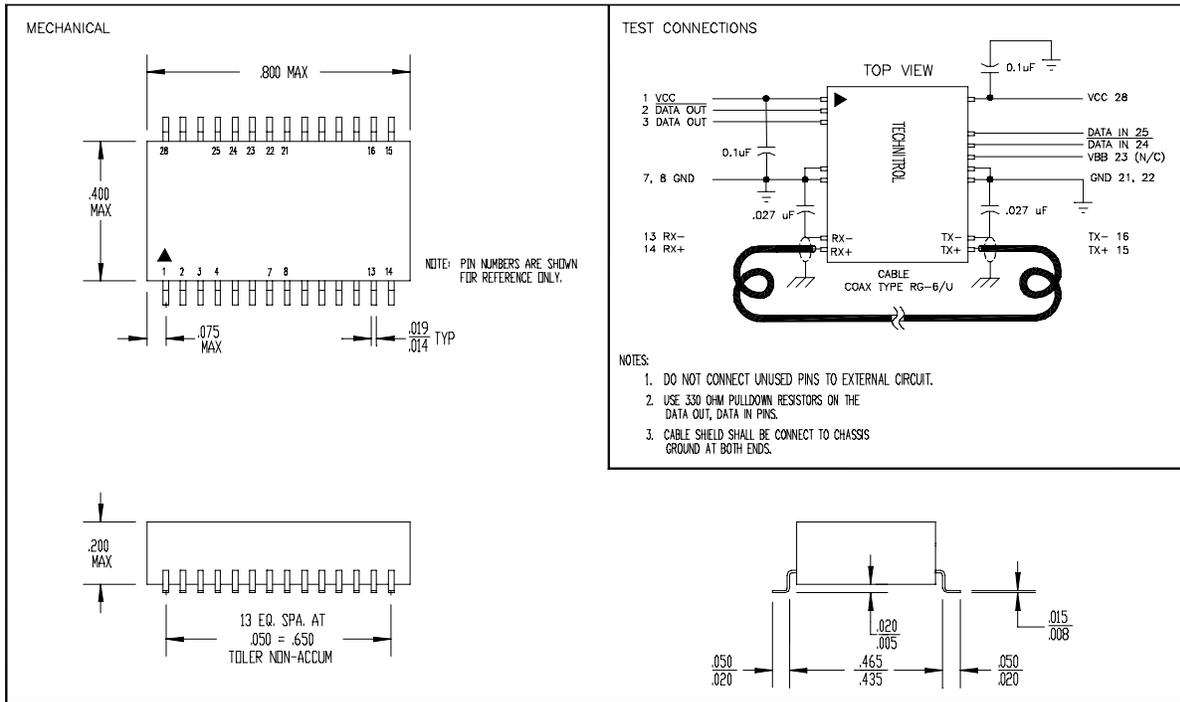


**Request dimensions and pin assignment for Model TM1062HSCT completely passive transceiver for "short haul" applications**



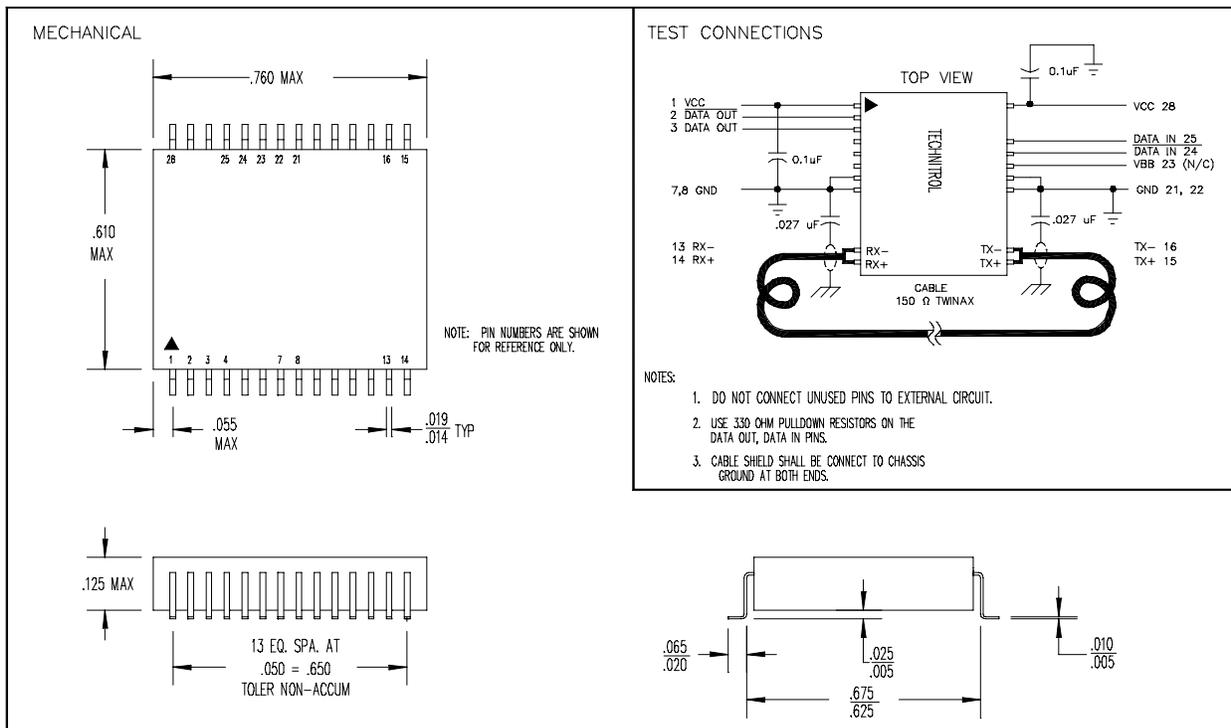
## Dimensions and test circuit "D" package (DIP)

Test circuit shown is for coax single-ended application



## Dimensions and test circuit "F" package (flat pack)

Test circuit shown is for twinax/STP differential application



Note: Test circuits for both package types are interchangeable.



## WARRANTY

Pulse Specialty Components warrants for a period of 90 days from the date of shipment, that under normal use and service, its products will be free from defects in workmanship and material. Pulse Specialty Components' sole responsibility under this warranty is, at its option, to repair or replace, without charge, any defective product or part, or to credit buyer for the purchase price of such defective product, provided:

- 1) Buyer promptly notifies Pulse Specialty Components in writing within the warranty period, and
- 2) The defective product or part is returned to Pulse Specialty Components with transportation charges prepaid by Buyer, and
- 3) Pulse Specialty Components examination of such product shall disclose to its satisfaction that said defect exists and has not been caused by misuse, neglect, improper installation, repair or alteration, or accident.

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