

# NE/SA/SE558 Quad Timer

## Product Specification

### Linear Products

### DESCRIPTION

The 558 Quad Timers are monolithic timing devices which can be used to produce four independent timing functions. The 558 output sinks current. These highly stable, general purpose controllers can be used in a monostable mode to produce accurate time delays; from microseconds to hours. In the time delay mode of operation, the time is precisely controlled by one external resistor and one capacitor. A stable operation can be achieved by using two of the four timer sections.

The four timer sections in the 558 are edge-triggered; therefore, when connected in tandem for sequential timing applications, no coupling capacitors are required. Output current capability of 100mA is provided in both devices.

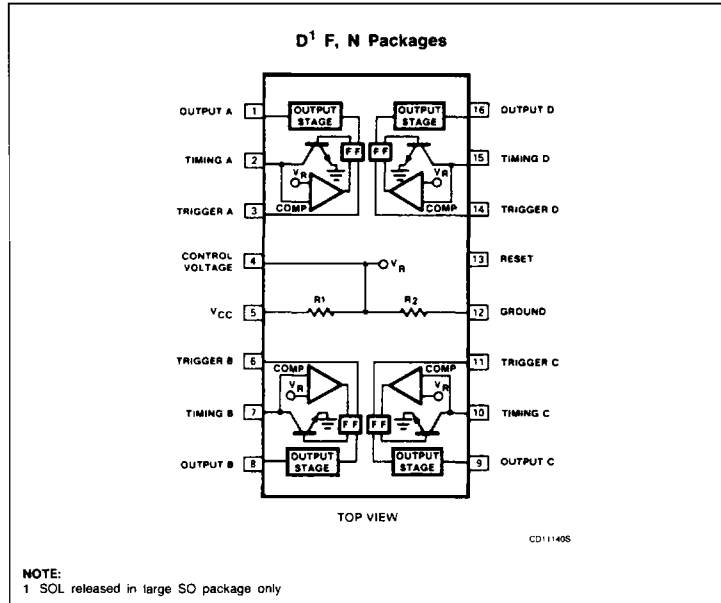
### FEATURES

- 100mA output current per section
- Edge-triggered (no coupling capacitor)
- Output independent of trigger conditions
- Wide supply voltage range 4.5V to 18V
- Timer intervals from microseconds to hours
- Time period equals RC
- Military qualifications pending

### APPLICATIONS

- Sequential timing
- Time delay generation
- Precision timing
- Industrial controls
- Quad one-shot

### PIN CONFIGURATION



### ORDERING INFORMATION

DESCRIPTION	TEMPERATURE RANGE	ORDER CODE
16-Pin Plastic SOL	0 to +70°C	NE558D
16-Pin Cerdip	0 to +70°C	NE558F
16-Pin Plastic DIP	0 to +70°C	NE558N
16-Pin Cerdip	-40°C to +85°C	SA558F
16-Pin Plastic DIP	-40°C to +85°C	SA558N
16-Pin Cerdip	-55°C to +125°C	SE558F
16-Pin Plastic DIP	-55°C to +125°C	SE558N

## Quad Timer

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## ABSOLUTE MAXIMUM RATINGS

SYMBOL	PARAMETER	RATING	UNIT
V <sub>CC</sub>	Supply voltage NE/SA558 SE558	+16	V
		+18	V
P <sub>D</sub>	Maximum power dissipation T <sub>A</sub> = 25°C ambient (still-air) <sup>1</sup> F package N package D package	1190	mW
		1450	mW
		1090	mW
T <sub>A</sub>	Operating ambient temperature range NE558 SA558 SE558	0 to +70	°C
		-40 to +85	°C
		-55 to +125	°C
T <sub>STG</sub>	Storage temperature range	-65 to +150	°C
T <sub>SOLD</sub>	Lead soldering temperature (10sec max)	+300	°C

## NOTE:

- Derate above 25°C, at the following rates:  
F package at 9.5mW/°C  
N package at 11.6mW/°C  
D package at 8.7mW/°C

DC AND AC ELECTRICAL CHARACTERISTICS T<sub>A</sub> = 25°C, V<sub>CC</sub> = +5V to +15V, unless otherwise specified.

SYMBOL	PARAMETER	TEST CONDITIONS	SE558			NE/SA558			UNIT
			Min	Typ	Max	Min	Typ	Max	
V <sub>CC</sub>	Supply voltage		4.5		18	4.5		16	V
I <sub>CC</sub>	Supply current	V <sub>CC</sub> = Reset = 15V		16	32		16	36	mA
t <sub>A</sub> Δt <sub>A</sub> /ΔT Δt <sub>A</sub> /ΔV <sub>S</sub>	Timing accuracy (t = RC)	R = 2kΩ to 100kΩ, C = 1μF							
	Initial accuracy			± 1.0	3		± 2	5	%
	Drift with temperature			30	100		30	150	ppm/°C
	Drift with supply voltage			0.1	0.9		0.1	0.9	%/V
V <sub>TRIG</sub>	Trigger voltage <sup>1</sup>	V <sub>CC</sub> = 15V	0.8		2.4	0.8		2.4	V
I <sub>TRIG</sub>	Trigger current	Trigger = 0V		5	30		5	100	μA
V <sub>RESET</sub>	Reset voltage <sup>2</sup>		0.8		2.4	0.8		2.4	V
I <sub>RESET</sub>	Reset current	Reset		50	300		50	500	μA
V <sub>TH</sub>	Threshold voltage			0.63			0.63		× V <sub>CC</sub>
	Threshold leakage			15			15		nA
V <sub>OUT</sub>	Output voltage <sup>3</sup>	I <sub>L</sub> = 10mA		0.1	0.2		0.1	0.4	V
		I <sub>L</sub> = 100mA		0.7	1.5		1.0	2.0	V
	Output leakage			10	500		10	500	nA
t <sub>PD</sub>	Propagation delay			1.0			1.0		μs
t <sub>R</sub>	Rise time of output	I <sub>L</sub> = 100mA		100			100		ns
t <sub>F</sub>	Fall time of output	I <sub>L</sub> = 100mA		100			100		ns

## NOTES:

- The trigger functions only on the falling edge of the trigger pulse only after previously being high. After reset, the trigger must be brought high and then low to implement triggering.
- For reset below 0.8V, outputs set low and trigger inhibited. For reset above 2.4V, trigger enabled.
- The 558 output structure is open-collector which requires a pull-up resistor to V<sub>CC</sub> to sink current. The output is normally low sinking current.

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## 558 EQUIVALENT CIRCUIT

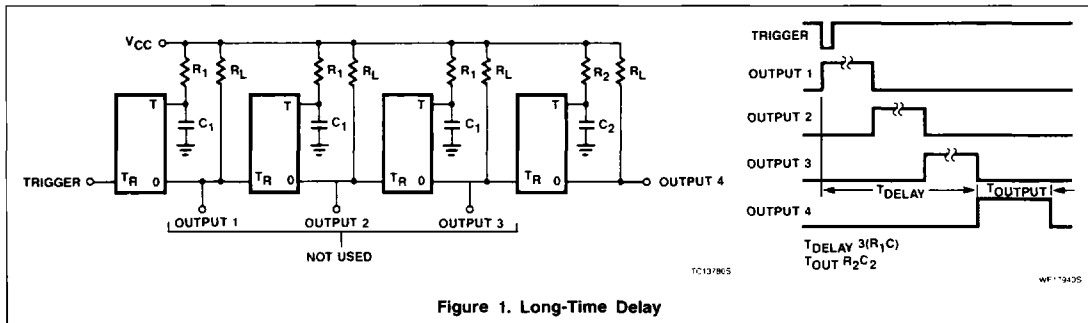
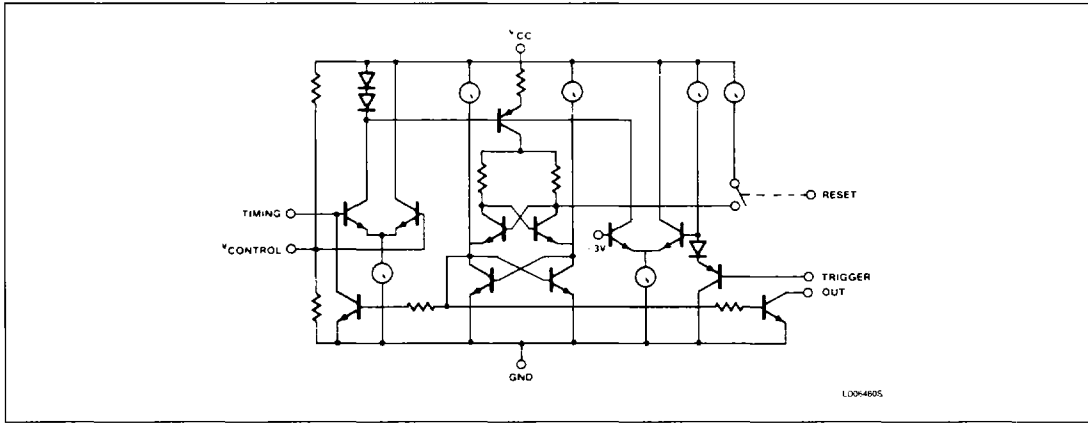
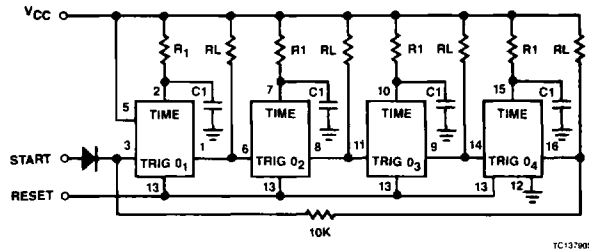


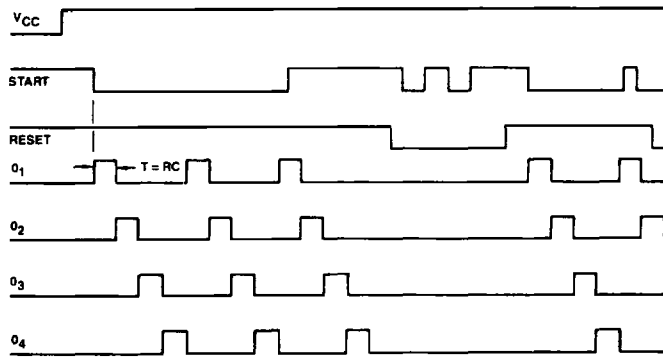
Figure 1. Long-Time Delay

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a. Ring Counter



b. Expected Waveforms

Figure 2