

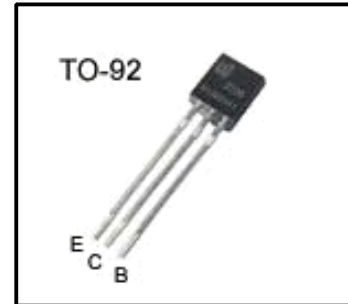
High Voltage Fast-Switching NPN Power Transistor

Features

- Very High Switching Speed
- High Voltage Capability
- Wide Reverse Bias SOA

General Description

This Device is designed for high voltage , High speed switching Characteristics required such as lighting system,switching mode power supply.



Absolute Maximum Ratings

Symbol	Parameter	Test Conditions	Value	Units
V_{CES}	Collector-Emitter Voltage	$V_{BE}=0$	700	V
V_{CEO}	Collector-Emitter voltage	$I_B=0$	400	V
V_{EBO}	Emitter -Base voltage	$I_C=0$	9.0	V
I_C	Collector Current		1.5	A
I_{CP}	Collector pulse Current		3.0	A
I_B	Base Current		0.75	A
I_{BM}	Base Peak Current	$t_p=5ms$	1.5	A
P_C	Total Dissipation at $T_c^*=25^{\circ}C$		18	W
	Total Dissipation at $T_a^*=25^{\circ}C$		1.14	
T_J	Operation Junction Temperature		-40~150	$^{\circ}C$
T_{STG}	Storage Temperature		-40~150	$^{\circ}C$

T_c :Case temperature(good cooling)

T_a :Ambient temperature(without heat sink)

Thermal Characteristics

Symbol	Parameter	Value	Units
R_{QJA}	Thermal Resistance Junction to Ambient	13.6	$^{\circ}C/W$

Electrical Characteristics($T_c=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Value			Units
			Min	Typ	Max	
$V_{CE(sus)}$	Collector-Emitter Breakdown Voltage	$I_c=10\text{mA}, I_b=0$	400	-	-	V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_c=0.5\text{A}, I_b=0.1\text{A}$ $I_c=1.0\text{A}, I_b=0.25\text{A}$ $I_c=1.5\text{A}, I_b=0.5\text{A}$	-	-	0.5 1.0 3.0	V
$V_{BE(sat)}$	Base -Emitter Saturation Voltage	$I_c=0.5\text{A}, I_b=0.1\text{A}$ $I_c=1.0\text{A}, I_b=0.25\text{A}$	-	-	1.0 1.2	V
I_{CBO}	Collector-Base Cutoff Current ($V_{be}=-1.5\text{V}$)	$V_{cb}=700\text{V}$ $V_{cb}=700\text{V}, T_c=100^{\circ}\text{C}$	-	-	1.0 5.0	mA
hFE	DC Current Gain	$V_{ce}=2\text{V}, I_c=1\text{A}$ $V_{ce}=2\text{V}, I_c=1.0\text{A}$	8 3	- -	20	
t_{on} t_s t_f	Resistive Load Turn-on Time Storage Time Fall Time	$V_{CC}=125\text{V}, I_c=1\text{A}$ $I_{B1}=0.2\text{A}, I_{B2}=-0.5\text{A}$ $T_p=25\mu\text{s}$	-	0.25 1.32 0.23	1.0 3.0 0.4	μs
t_s t_f	Inductive Load Storage Time Fall Time	$V_{CC}=15\text{V}, I_c=1\text{A}$ $I_{B1}=0.2\text{A}, I_{B2}=-0.5\text{A}$ $L=0.35\text{mH}, V_{clamp}=300\text{V}$	- -	1.2 0.12	4.0 0.3	μs
t_s t_f	Inductive Load Storage Time Fall Time	$V_{CC}=15\text{V}, I_c=1\text{A}$ $I_{B1}=0.2\text{A}, I_{B2}=-0.5\text{A}$ $L=0.35\text{mH}, V_{clamp}=300\text{V}$ $T_c=100^{\circ}\text{C}$	- -	1.8 0.16	5.0 0.4	μs

Note:

Pulse Test : Pulse width 300,Duty cycle 2%

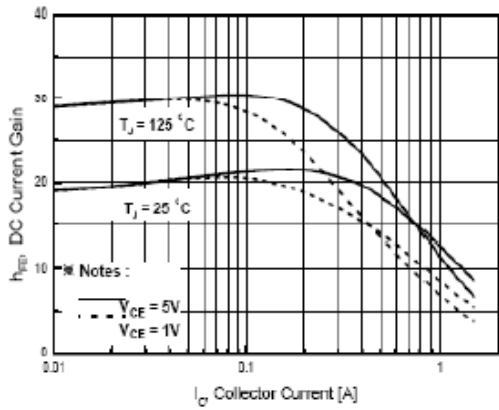


Fig.1 DC Current Gain

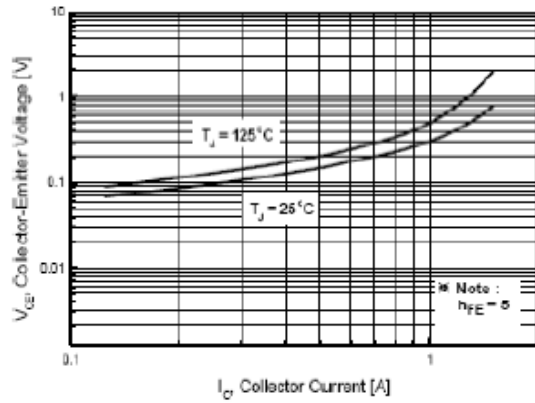


Fig.2 Base -Emitter Saturation Voltage

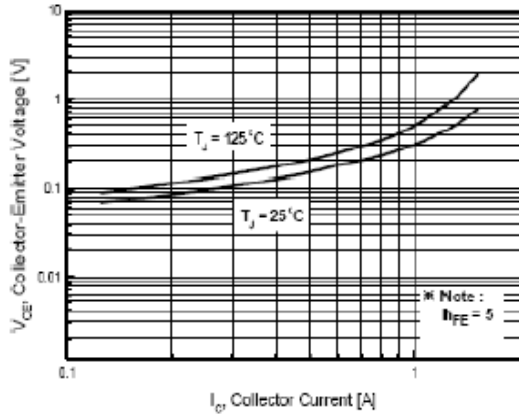


Fig.3 Collector -Emitter saturation Voltage

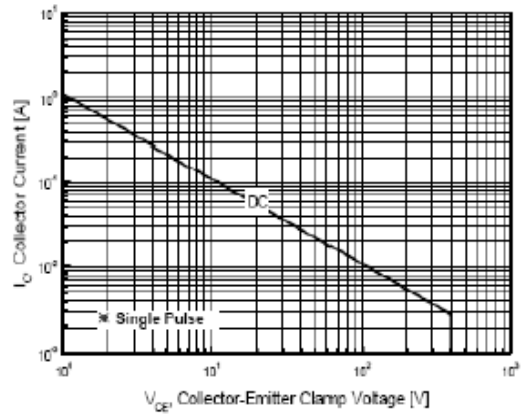


Fig.4 Safe Operation Area

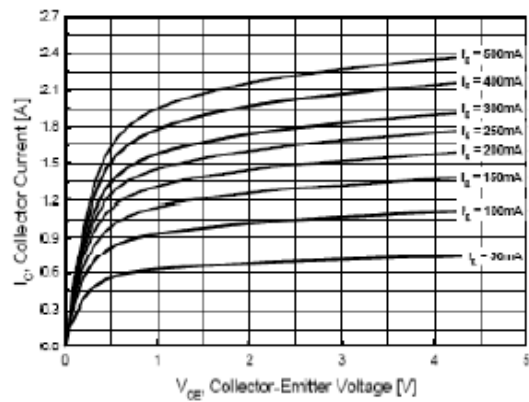


Fig.5 Static Characteristics

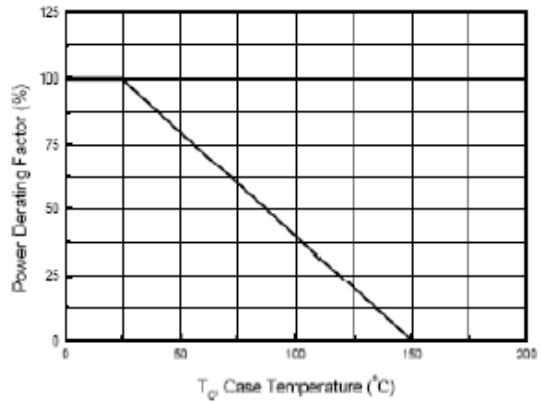
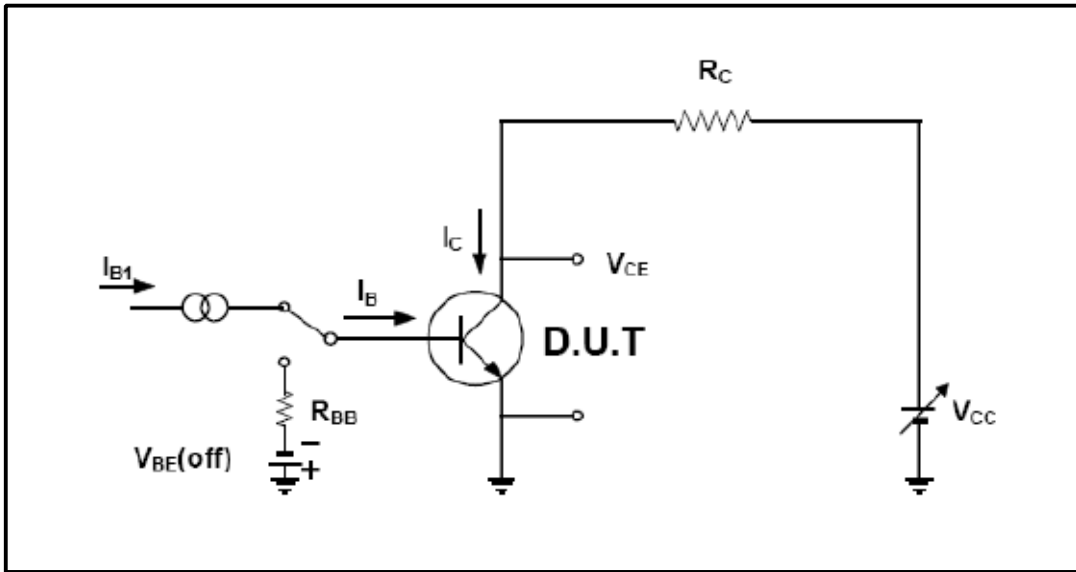
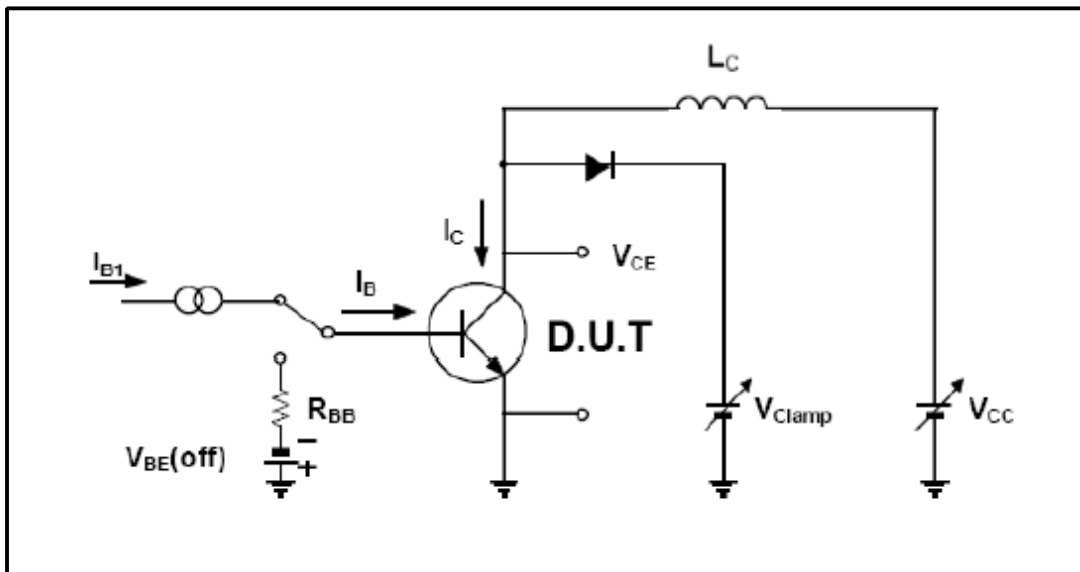


Fig.6 Power Derating



Resistive Load Switching Test Circuit



Inductive Load Switching & RBSOA Test Circuit

To-92 Package Dimension

