

# 1-3 Lighting ICs

## 1-3-1 LED Lighting ICs

### LC5200 Series LED Driver ICs

#### ■Features

- AC rectification voltage can be applied directly
- Output current  $I_o$ : Two types available: 0.5 A (LC5205D) and 1 A (LC5210D)
- Self-excitation PWM current control method
- Undervoltage lockout (UVLO)
- Overcurrent protection (OCP)
- Thermal shutdown (TSD)
- DIP-8 type mold package
- Supports driving without input electrolytic capacitor
- Power factor correction (PFC) application circuit
- Triac dimmer control application circuit

#### ■Absolute Maximum Ratings

Parameter	Symbol	Ratings		Unit	Conditions
		LC5205D	LC5210D		
Main Supply Voltage	$V_{BB}$	450	450	V	
Output MOSFET Breakdown Voltage	$V_o$	450	450	V	
Output current <sup>*1</sup>	$I_o$	0.5	1.0	A	Excluding when $t_w$ is less than 1μs
REF Input Voltage	$V_{Ref}$	-0.3 to $V_{REG} + 0.3$	-0.3 to $V_{REG} + 0.3$	V	
Sense Voltage	$V_{Sen}$	-0.3 to +4	-0.3 to +4	V	Excluding when $t_w$ is less than 1μs
Power Dissipation <sup>*2</sup>	$P_D$	1.73	1.73	W	When using a Sanken evaluation board
Junction Temperature	$T_j$	150	150	°C	
Thermal Resistance	$\theta_{j-a}$	72	72	°C/W	
	$\theta_{c-a}$	60	60	°C/W	When using a Sanken evaluation board
Operating Ambient Temperature	$T_a$	-40 to +105	-40 to +105	°C	
Storage Temperature	$T_{Stg}$	-40 to +150	-40 to +150	°C	

\*1: The output current value may be limited depending on the duty ratio, ambient temperature, and heating conditions. Do not exceed the junction temperature  $T_j$  under any circumstances.

\*2: The power dissipation  $P_D$  depends on the pattern layout of the circuit board used.

#### ■Applications

- LED light bulbs
- LED lighting equipment

#### ■Recommended Operating Conditions

Parameter	Symbol	Ratings				Unit
		LC5205D		LC5210D		
		min.	max.	min.	max.	
Main Supply Voltage	$V_{BB}$	25	400	25	400	V
Output Current (Average)	$I_o$		0.4		0.8	A
REF Input Voltage	$V_{Ref}$		0.8		0.8	V
Case temperature*	$T_c$		105		105	°C

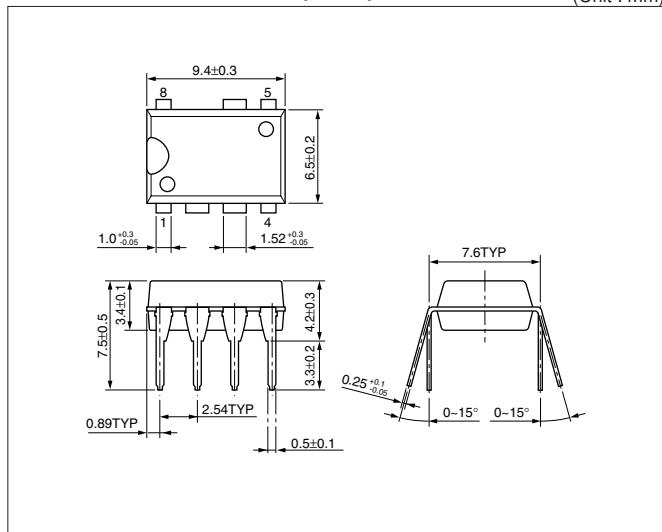
\*: At the center of the marking (when there is no fin)

#### ■Electrical Characteristics

Parameter	Symbol	Ratings						Unit	
		LC5205D		LC5210D					
		min.	typ.	max.	min.	typ.	max.		
Main Supply Current	$I_{BB}$		2.0			2.0		mA	
	Conditions	During operation				During operation			
	$I_{BS}$		0.8	1.2		0.8	1.2		
Output MOSFET Breakdown Voltage	$V_{DS(BR)}$	450			450			V	
	Conditions	$I_D=1\text{mA}$				$I_D=1\text{mA}$			
Output MOSFET ON Resistance	$R_{DS(on)}$		3.5			1.7		$\Omega$	
	Conditions	$I_D=0.5\text{A}$				$I_D=1.0\text{A}$			
Output MOSFET Diode Forward Voltage	$V_F$		0.8			0.88		V	
	Conditions	$I_D=0.5\text{A}$				$I_D=1.0\text{A}$			
Reg Output Voltage	$V_{Reg}$	11.5	12.0	12.5	11.5	12.0	12.5	V	
	Conditions	$I_{Reg}=0.1\text{mA}$				$I_{Reg}=0.1\text{mA}$			
Reg Maximum Output Current	$I_{Reg}$			2			2	mA	
	Conditions	$f_{clk}=200$				200			
Maximum Input Response Frequency	$f_{clk}$					duty=50%		kHz	
	Conditions	duty=50%				duty=50%			
REF Input Voltage	$V_{Ref}$	0		1	0		1	V	
REF Input Current	$I_{Ref}$		±10			±10		$\mu\text{A}$	
Sense Voltage	$V_{Sen}$	$V_{Ref}-30$	$V_{Ref}$	$V_{Ref}+30$	$V_{Ref}-30$	$V_{Ref}$	$V_{Ref}+30$	$\text{mV}$	
Sense Input Current	$I_{Sen}$		±10			±10		$\mu\text{A}$	
Overcurrent Sense Voltage	$V_{ocp}$		3			3		V	
UVLO Release Conditions		At the Sen pin				At the Sen pin			
UVLO Release Voltage	$V_{UVLoon}$		14			14		V	
UVLO Operation Conditions		At the $V_{BB}$ voltage				At the $V_{BB}$ voltage			
UVLO Operation Voltage	$V_{UVLooff}$		13			13		V	
UVLO Operation Conditions		At the $V_{BB}$ voltage				At the $V_{BB}$ voltage			
TSD Operating Temperature	$T_{TSD}$		150			150		°C	
TSD Temperature Conditions		Control IC chip temperature				Control IC chip temperature			
TSD Hysteresis	$T_{TSDhys}$		55			55		°C	
Blanking Time Conditions		Control IC chip temperature				Control IC chip temperature			
Blanking Time	$t_{BLK}$		400			400		ns	

### ■External Dimensions (DIP8)

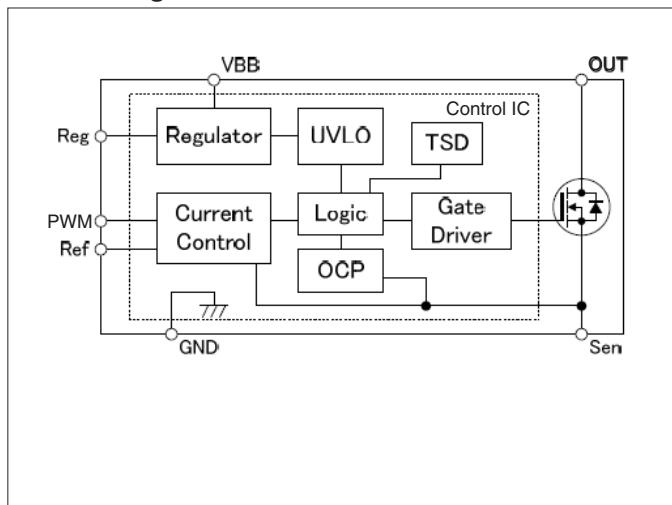
(Unit : mm)



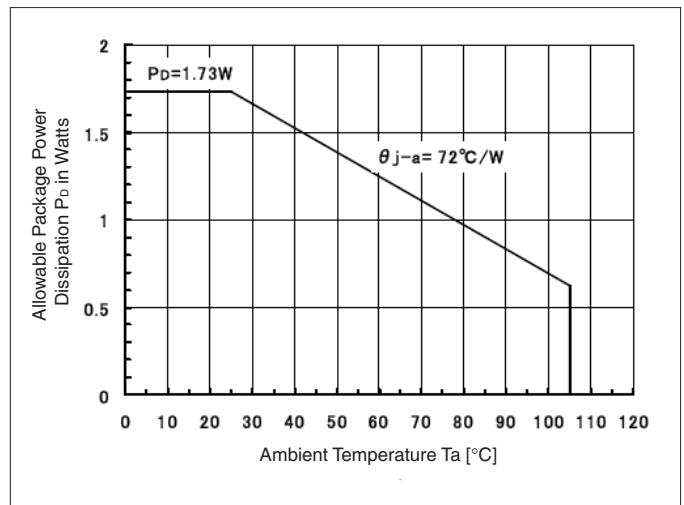
### ■Pin Assignment

Pin No.	Symbol	Function
1	Reg	12-V Reg output pin
2	PWM	CR connection pin for setting PWM
3	Ref	Reference voltage input pin for controlling PWM
4	Sen	Load current sense pin
5	OUT	Load output pin
6	VBB	Main power supply pin
7	NC	Pin removed
8	GND	Device ground pin

### ■Block Diagram



### ■Ta-Pd Characteristics



### ■Typical Connection Diagram

