

FGAF40N60UF

Ultrafast IGBT

General Description

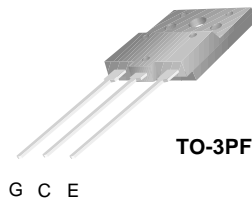
Fairchild's UF series of Insulated Gate Bipolar Transistors (IGBTs) provides low conduction and switching losses. The UF series is designed for applications such as motor control and general inverters where high speed switching is a required feature.

Features

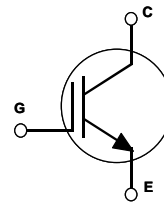
- High speed switching
- Low saturation voltage : $V_{CE(sat)} = 2.3 \text{ V @ } I_C = 20\text{A}$
- High input impedance

Applications

AC & DC motor controls, general purpose inverters, robotics, and servo controls.



TO-3PF



Absolute Maximum Ratings T_C = 25°C unless otherwise noted

Symbol	Description	FGAF40N60UF	Units
V _{CES}	Collector-Emitter Voltage	600	V
V _{GES}	Gate-Emitter Voltage	± 20	V
I _C	Collector Current @ T _C = 25°C	40	A
	Collector Current @ T _C = 100°C	20	A
I _{CM(1)}	Pulsed Collector Current	160	A
P _D	Maximum Power Dissipation @ T _C = 25°C	100	W
	Maximum Power Dissipation @ T _C = 100°C	40	W
T _J	Operating Junction Temperature	-55 to +150	°C
T _{stg}	Storage Temperature Range	-55 to +150	°C
T _L	Maximum Lead Temp. for Soldering Purposes, 1/8" from Case for 5 Seconds	300	°C

Notes :

(1) Repetitive rating : Pulse width limited by max. junction temperature

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Units
R _{θJC} (IGBT)	Thermal Resistance, Junction-to-Case	--	1.2	°C/W
R _{θJA}	Thermal Resistance, Junction-to-Ambient	--	40	°C/W

Electrical Characteristics of the IGBT T_C = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
Off Characteristics						
BV _{CES}	Collector-Emitter Breakdown Voltage	V _{GE} = 0V, I _C = 250uA	600	--	--	V
ΔBV _{CES} /ΔT _J	Temperature Coefficient of Breakdown Voltage	V _{GE} = 0V, I _C = 1mA	--	0.6	--	V/°C
I _{CES}	Collector Cut-Off Current	V _{CE} = V _{CES} , V _{GE} = 0V	--	--	250	uA
I _{GES}	G-E Leakage Current	V _{GE} = V _{GES} , V _{CE} = 0V	--	--	± 100	nA

On Characteristics

V _{GE(th)}	G-E Threshold Voltage	I _C = 20mA, V _{CE} = V _{GE}	3.5	5.1	6.5	V
V _{CE(sat)}	Collector to Emitter Saturation Voltage	I _C = 20A, V _{GE} = 15V	--	2.3	3.0	V
		I _C = 40A, V _{GE} = 15V	--	3.1	--	V

Dynamic Characteristics

C _{ies}	Input Capacitance	V _{CE} = 30V, V _{GE} = 0V, f = 1MHz	--	1075	--	pF
C _{oes}	Output Capacitance		--	170	--	pF
C _{res}	Reverse Transfer Capacitance		--	50	--	pF

Switching Characteristics

t _{d(on)}	Turn-On Delay Time	V _{CC} = 300 V, I _C = 20A, R _G = 10Ω, V _{GE} = 15V, Inductive Load, T _C = 25°C	--	15	--	ns	
t _r	Rise Time		--	30	--	ns	
t _{d(off)}	Turn-Off Delay Time		--	65	130	ns	
t _f	Fall Time		--	35	100	ns	
E _{on}	Turn-On Switching Loss		--	470	--	uJ	
E _{off}	Turn-Off Switching Loss		--	130	--	uJ	
E _{ts}	Total Switching Loss		--	600	1000	uJ	
t _{d(on)}	Turn-On Delay Time		V _{CC} = 300 V, I _C = 20A, R _G = 10Ω, V _{GE} = 15V, Inductive Load, T _C = 125°C	--	30	--	ns
t _r	Rise Time			--	37	--	ns
t _{d(off)}	Turn-Off Delay Time	--		110	200	ns	
t _f	Fall Time	--		80	250	ns	
E _{on}	Turn-On Switching Loss	--		500	--	uJ	
E _{off}	Turn-Off Switching Loss	--		310	--	uJ	
E _{ts}	Total Switching Loss	--		810	1200	uJ	
Q _g	Total Gate Charge	V _{CE} = 300 V, I _C = 20A, V _{GE} = 15V		--	77	150	nC
Q _{ge}	Gate-Emitter Charge			--	20	30	nC
Q _{gc}	Gate-Collector Charge		--	25	40	nC	
L _e	Internal Emitter Inductance	Measured 5mm from PKG	--	14	--	nH	

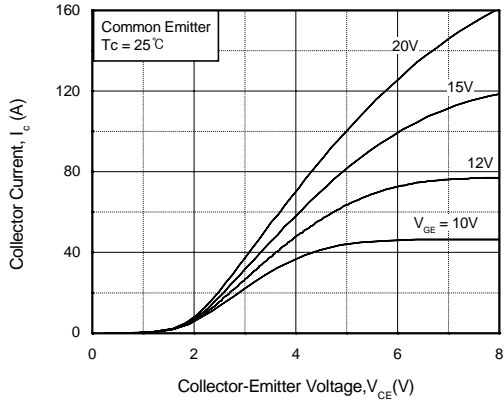


Fig 1. Typical Output Characteristics

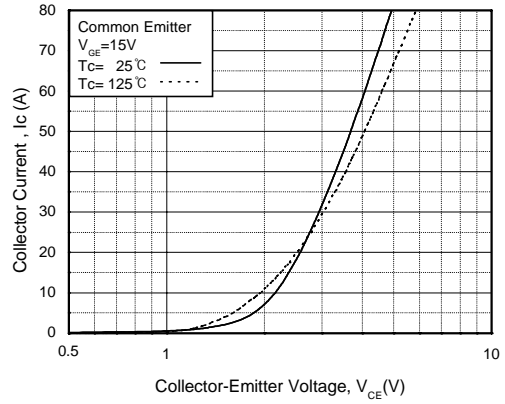


Fig 2. Typical Saturation Voltage Characteristics

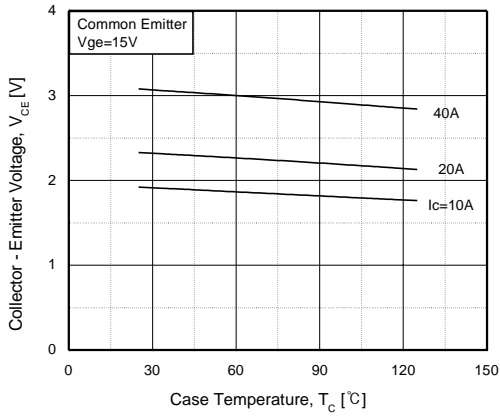


Fig 3. Saturation Voltage vs. Case Temperature at Variant Current Level

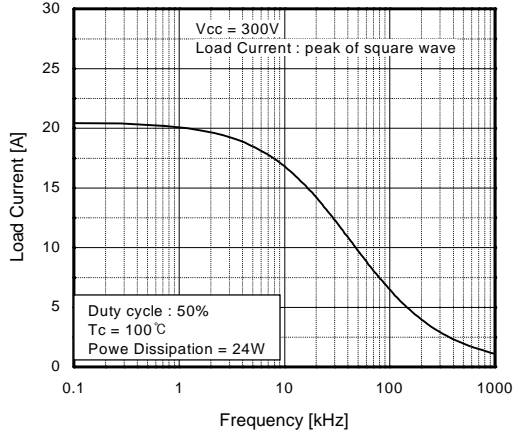


Fig 4. Load Current vs. Frequency

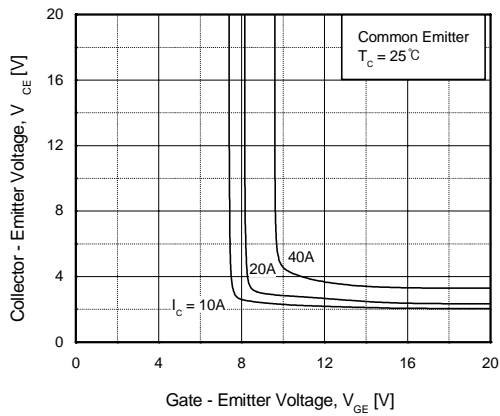


Fig 5. Saturation Voltage vs. V_{GE}

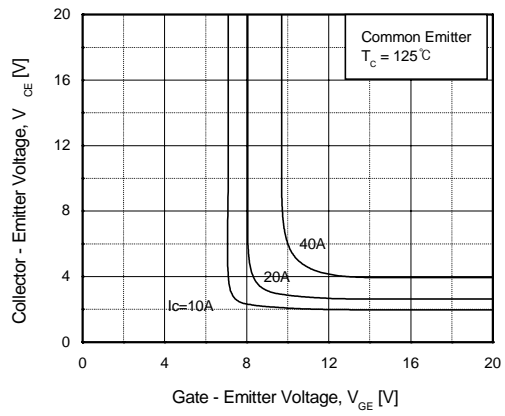


Fig 6. Saturation Voltage vs. V_{GE}

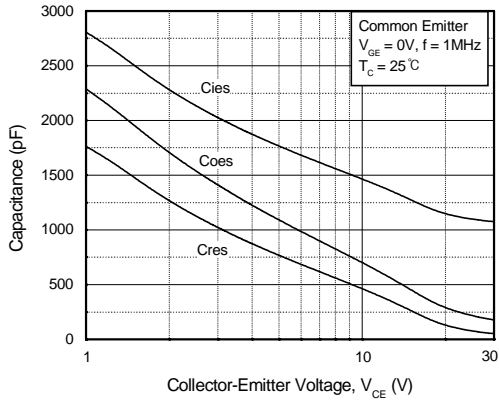


Fig 7. Capacitance Characteristics

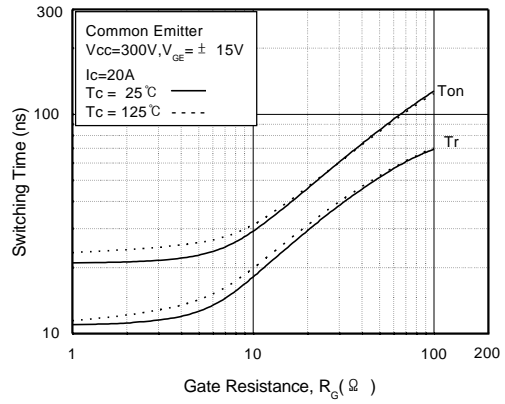


Fig 8. Turn-On Characteristics vs. Gate Resistance

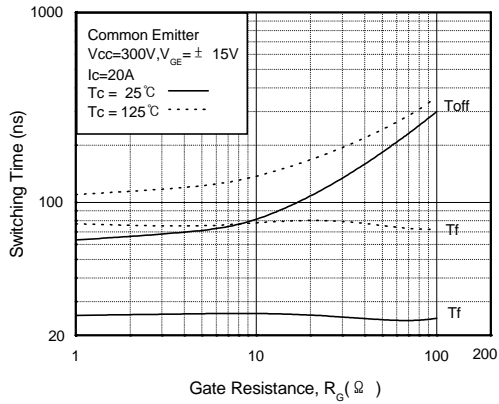


Fig 9. Turn-Off Characteristics vs. Gate Resistance

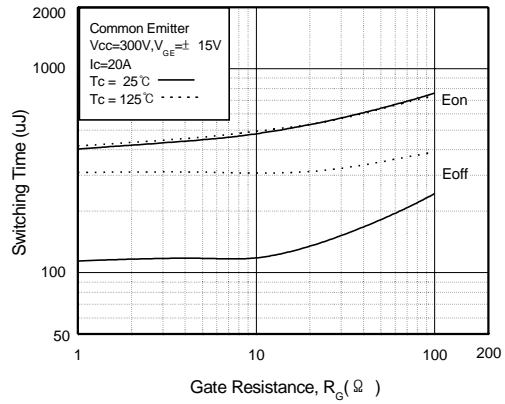


Fig 10. Switching Loss vs. Gate Resistance

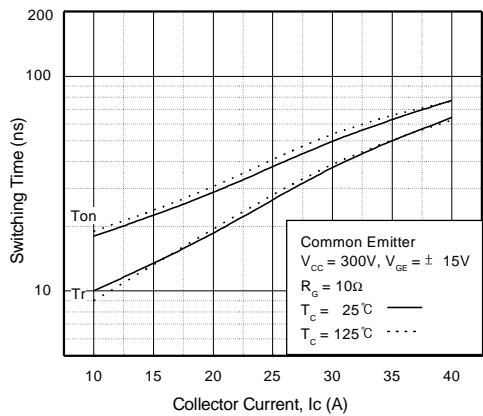


Fig 11. Turn-On Characteristics vs. Collector Current

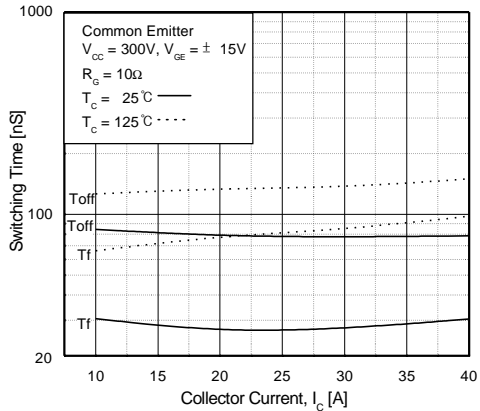


Fig 12. Turn-Off Characteristics vs. Collector Current

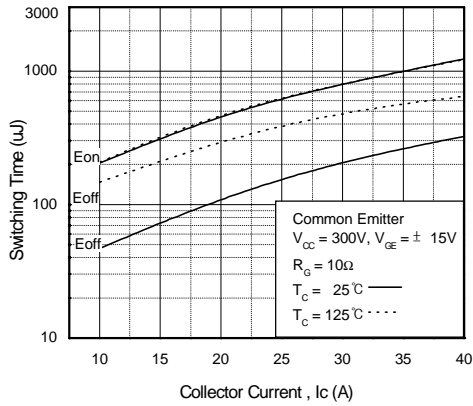


Fig 13. Switching Loss vs. Collector Current

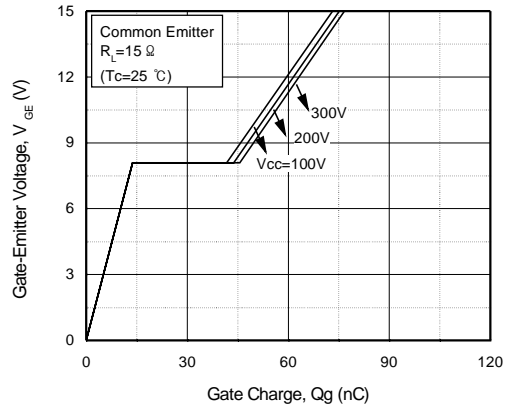


Fig 14. Gate Charge Characteristics

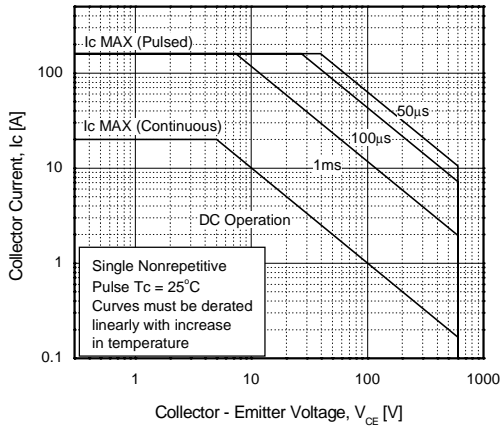


Fig 15. SOA Characteristics

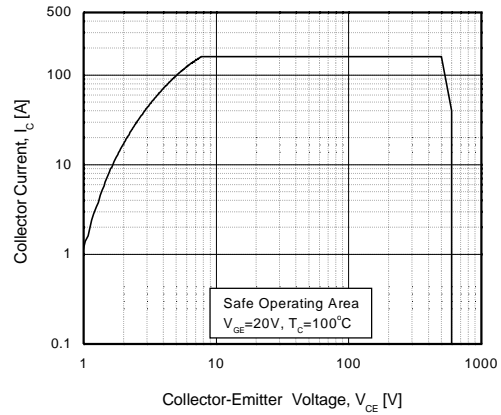


Fig 16. Turn-Off SOA Characteristics

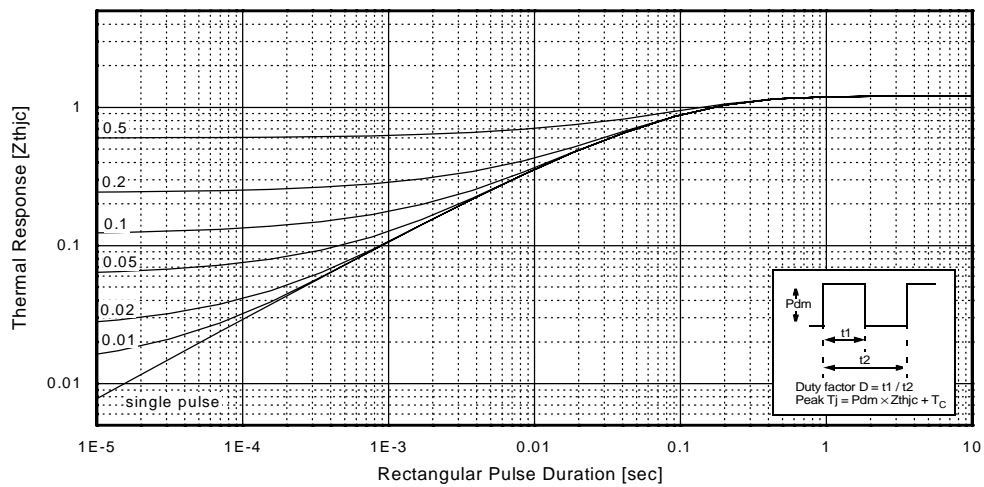
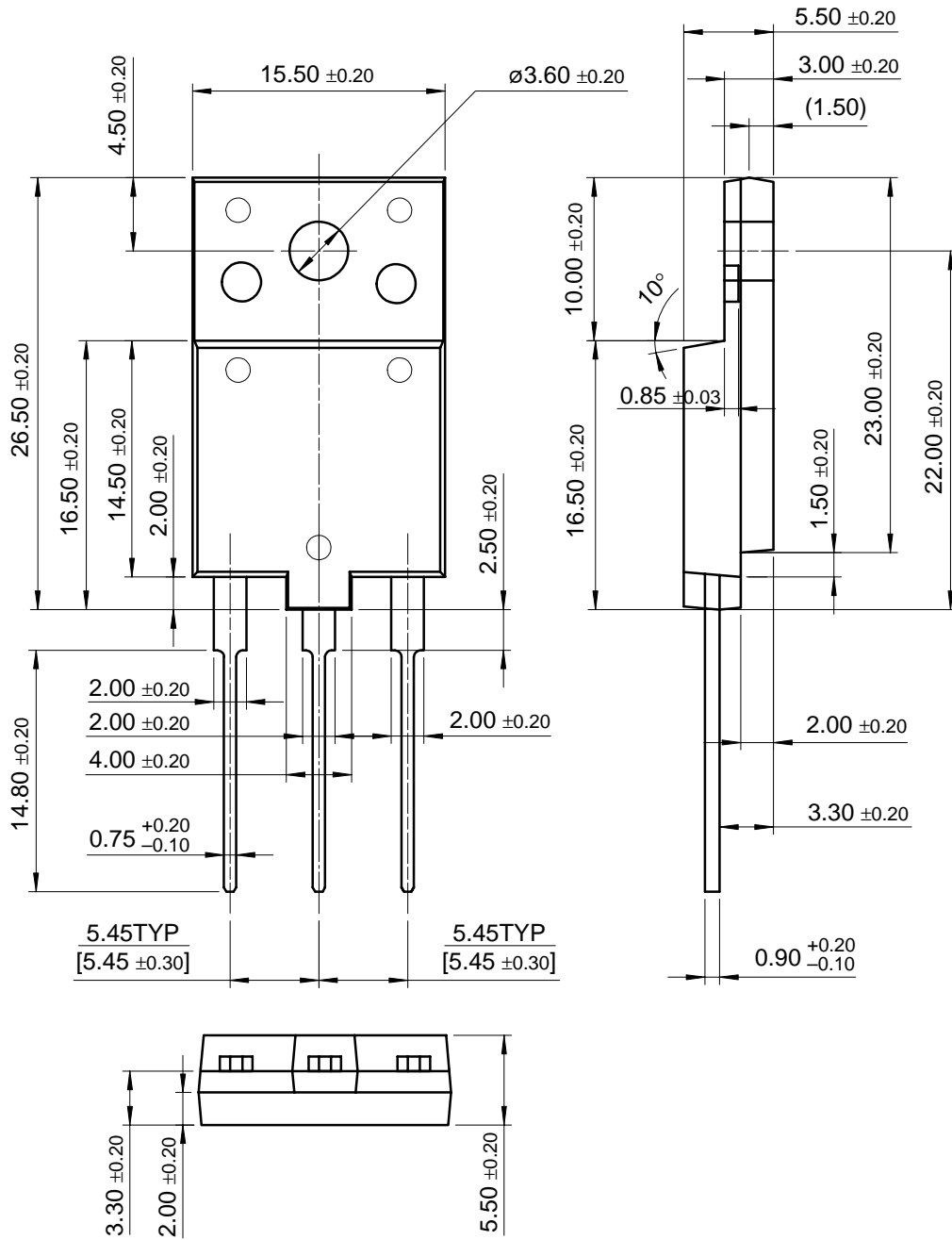


Fig 17. Transient Thermal Impedance of IGBT

Package Dimensions

TO-3PF

FGAF40N60UF



Dimensions in Millimeters

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Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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Applications

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Product	Product status	Pb-free Status	Pricing*	Package type	Leads	Packing method	Package Marking Convention**
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FGAF40N60UFTU	Full Production	 Full Production	\$3.12	TO-3PF	3	RAIL	Line 1: \$Y (Fairchild logo)
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* Fairchild 1,000 piece Budgetary Pricing

** A sample button will appear if the part is available through Fairchild's on-line samples program. If there is no sample button, please contact a [Fairchild distributor](#) to obtain samples



Indicates product with Pb-free second-level interconnect. For more information [click here](#).

Package marking information for product FGAF40N60UF is available. [Click here for more information](#).

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Qualification Support

Click on a product for detailed qualification data

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