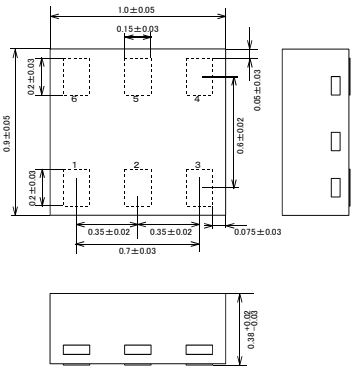
TOSHIBA Transistor Silicon NPN Epitaxial Type (PCT process) (Bias Resistor built-in Transistor)

RN1961CT,RN1962CT,RN1963CT RN1964CT,RN1965CT,RN1966CT

Switching Applications
Inverter Circuit Applications
Interface Circuit Applications
Driver Circuit Applications

- Two devices are incorporated into a fine pitch Small Mold (6 pin) package.
- Incorporating a bias resistor into a transistor reduces parts count. Reducing the parts count enable the manufacture of ever more compact equipment and save assembly cost.
- Complementary to RN2961CT to RN2966CT

Unit: mm

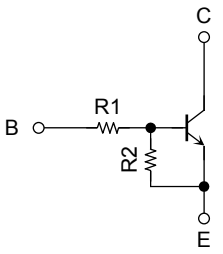


1.EMITTER1	(E1)
2.EMITTER2	(E2)
3.BASE2	(B2)
4.COLLECTOR2	(C2)
5.BASE1	(B1)
CST6 6.COLLECTOR 1	(C1)

JEDEC	—
JEITA	—
TOSHIBA	2-1K1A

Weight: 1 mg (typ.)

Equivalent Circuit and Bias Resistor Values

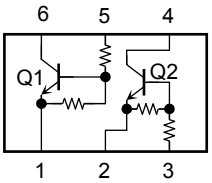


Type No.	R1 (kΩ)	R2 (kΩ)
RN1961CT	4.7	4.7
RN1962CT	10	10
RN1963CT	22	22
RN1964CT	47	47
RN1965CT	2.2	47
RN1966CT	4.7	47

Absolute Maximum Ratings (Ta = 25°C) (Q1, Q2 common)

Characteristics		Symbol	Rating	Unit
Collector-base voltage	RN1961CT to 1966CT	V_{CBO}	20	V
Collector-emitter voltage		V_{CEO}	20	V
Emitter-base voltage	RN1961CT to 1964CT	V_{EBO}	10	V
	RN1965CT, 1966CT		5	
Collector current	RN1961CT to RN1966CT	I_C	50	mA
Collector power dissipation		P_C (Note1)	50	mW
Junction temperature		T_j	150	°C
Storage temperature range		T_{stg}	−55 to 150	°C

Equivalent Circuit (top view)



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/“Derating Concept and Methods”) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

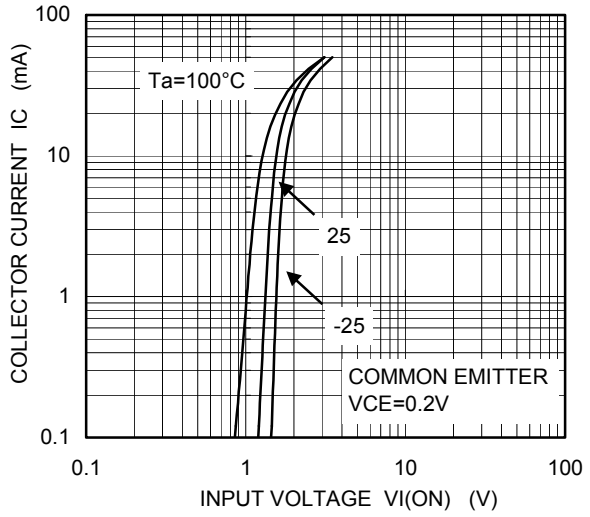
Note1: Total rating

Electrical Characteristics (Ta = 25°C) (Q1, Q2 common)

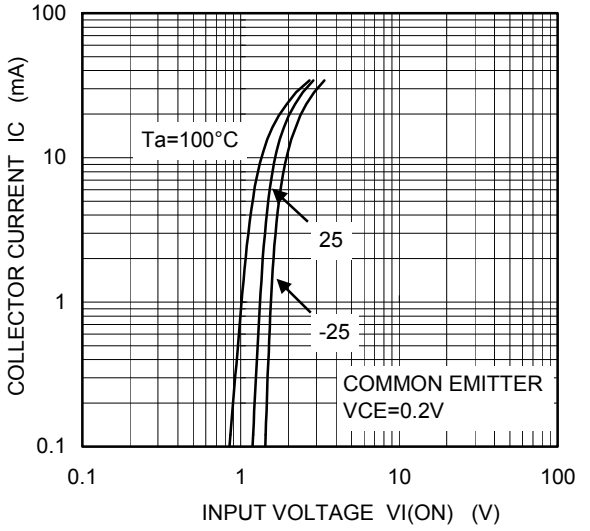
Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Collector cut-off current	RN1961CT to 1966CT	I_{CBO}	V_{CB} = 20 V, I_E = 0	—	—	100	nA
		I_{CEO}	V_{CE} = 20 V, I_B = 0	—	—	500	
Emitter cut-off current	RN1961CT	I_{EBO}	V_{EB} = 10 V, I_C = 0	0.89	—	1.33	mA
	RN1962CT			0.41	—	0.63	
	RN1963CT			0.18	—	0.29	
	RN1964CT			0.088	—	0.133	
	RN1965CT		V_{EB} = 5 V, I_C = 0	0.085	—	0.127	
	RN1966CT			0.08	—	0.121	
DC current gain	RN1961CT	h_{FE}	V_{CE} = 5 V, I_C = 10 mA	30	—	—	
	RN1962CT			60	—	—	
	RN1963CT			100	—	—	
	RN1964CT			120	—	—	
	RN1965CT			120	—	—	
	RN1966CT			120	—	—	
Collector-emitter saturation voltage	RN1961CT to 1966CT	$V_{CE\ (sat)}$	I_C = 5 mA, I_B = 0.25 mA	—	—	0.15	V
Input voltage (ON)	RN1961CT	$V_{I\ (ON)}$	V_{CE} = 0.2 V, I_C = 5 mA	1.0	—	2.0	V
	RN1962CT			1.0	—	2.2	
	RN1963CT			1.1	—	2.7	
	RN1964CT			1.2	—	3.6	
	RN1965CT			0.6	—	1.1	
	RN1966CT			0.6	—	1.2	
Input voltage (OFF)	RN1961CT to 1964CT	$V_{I\ (OFF)}$	V_{CE} = 5 V, I_C = 0.1 mA	0.8	—	1.5	V
	RN1965CT, 1966CT			0.4	—	0.8	
Collector output capacitance	RN1961CT to 1966CT	C_{ob}	V_{CB} = 10 V, I_E = 0, f = 1 MHz	—	1.2	—	pF
Input resistor	RN1961CT	R1	—	3.76	4.7	5.64	kΩ
	RN1962CT			8	10	12	
	RN1963CT			17.6	22	26.4	
	RN1964CT			37.6	47	56.4	
	RN1965CT			1.76	2.2	2.64	
	RN1966CT			3.76	4.7	5.64	
Resistor ratio	RN1961CT to 1964CT	R1/R2	—	0.8	1.0	1.2	
	RN1965CT			0.0376	0.0468	0.0562	
	RN1966CT			0.08	0.1	0.12	

(Q1,Q2 common)

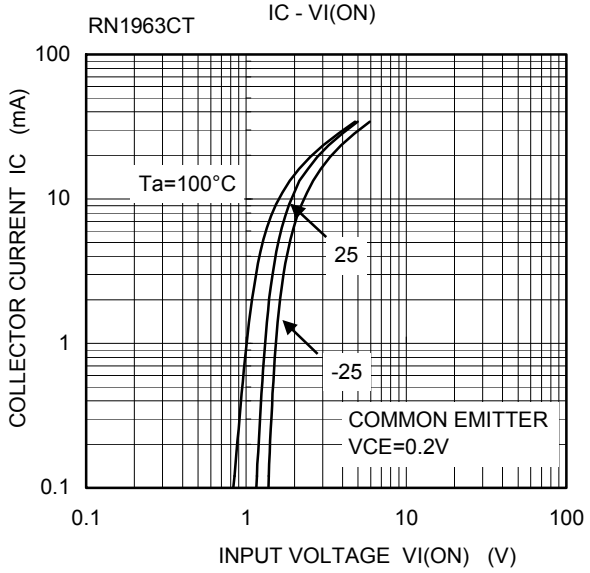
RN1961CT IC - VI(ON)



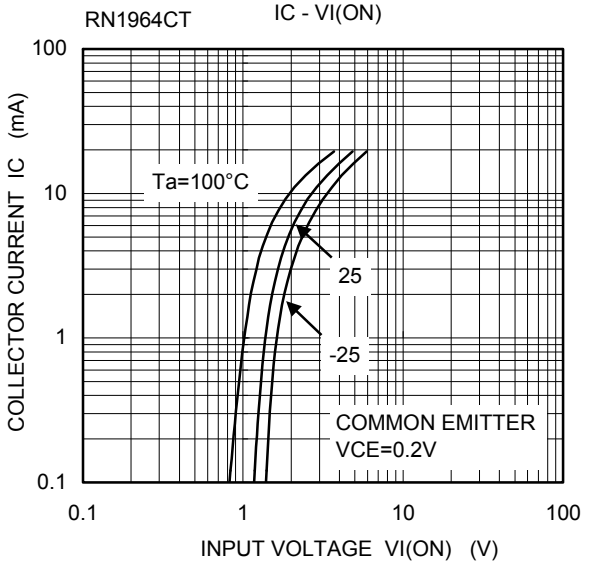
RN1962CT IC - VI(ON)



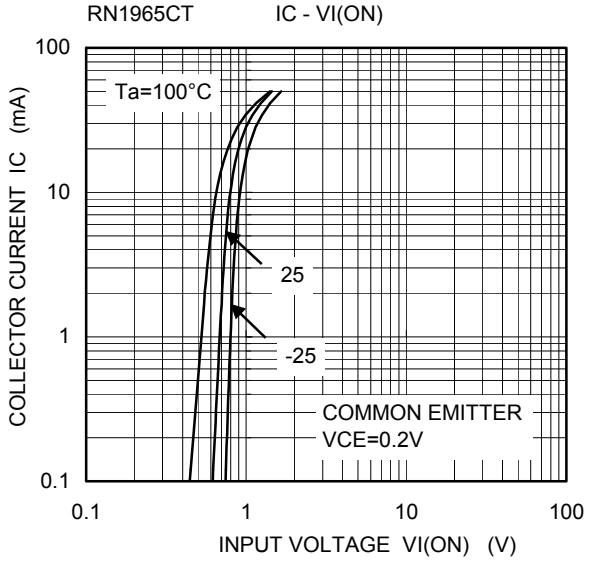
RN1963CT IC - VI(ON)



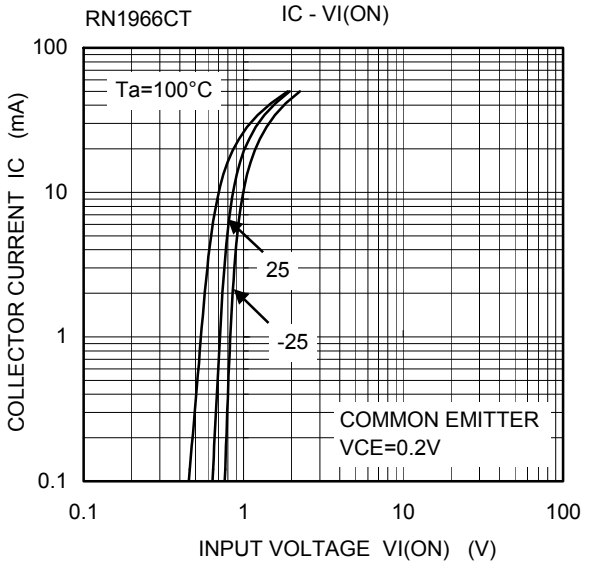
RN1964CT IC - VI(ON)



RN1965CT IC - VI(ON)

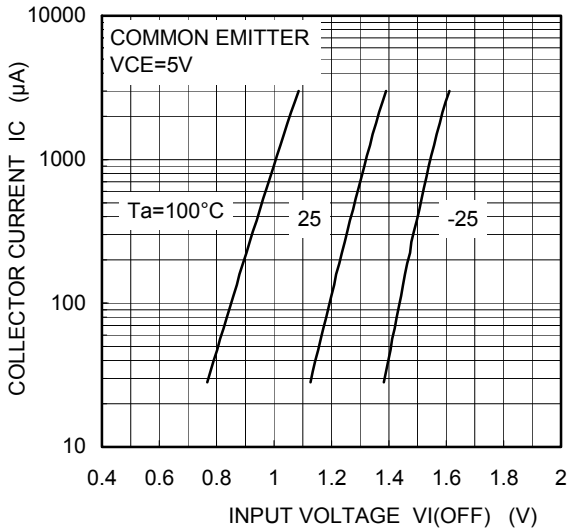


RN1966CT IC - VI(ON)

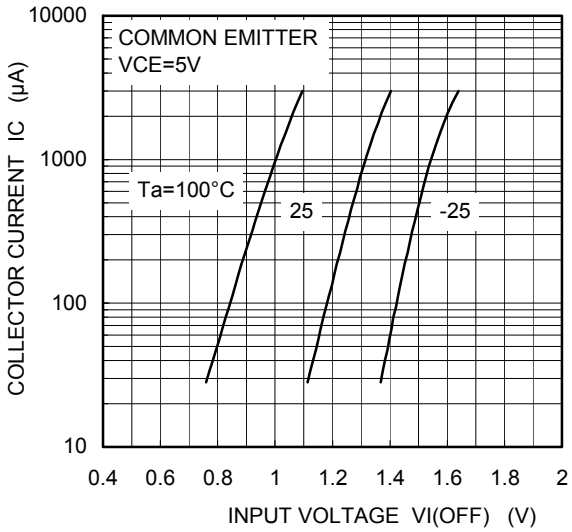


(Q1,Q2 common)

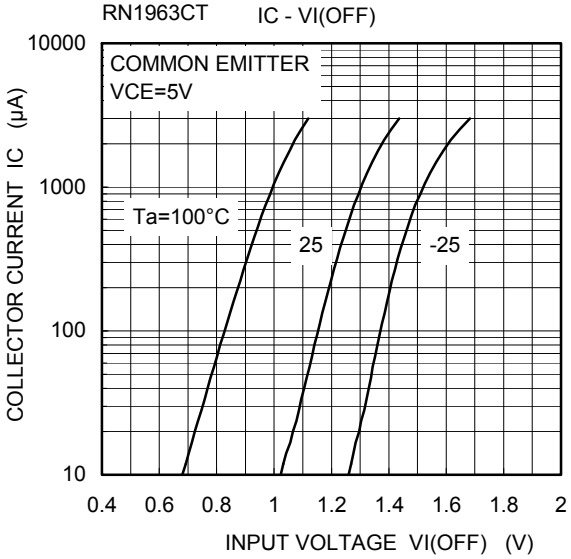
RN1961CT IC - VI(OFF)



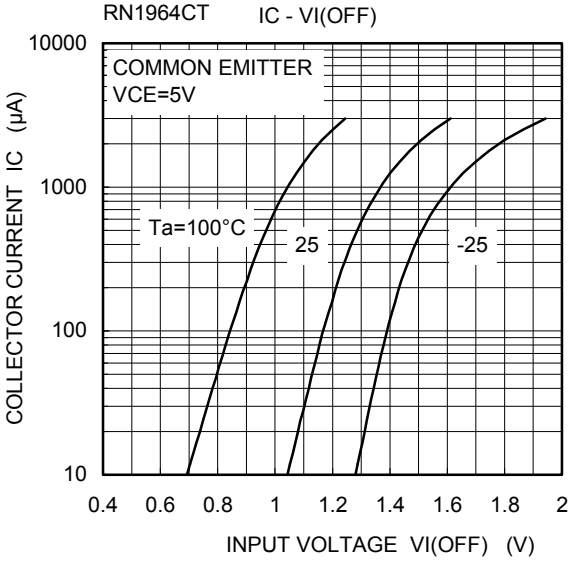
RN1962CT IC - VI(OFF)



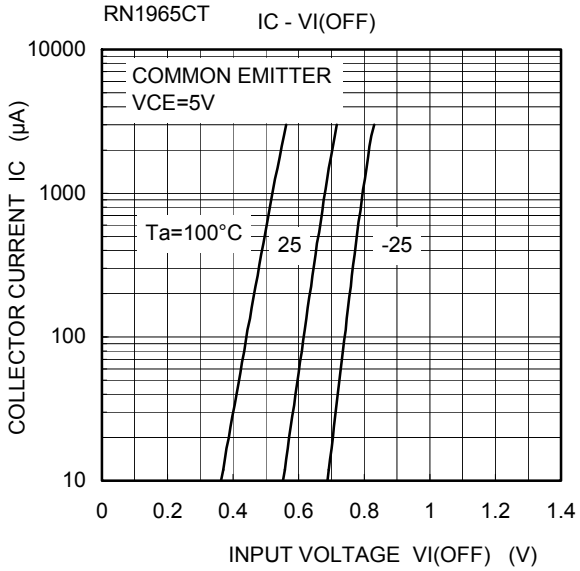
RN1963CT IC - VI(OFF)



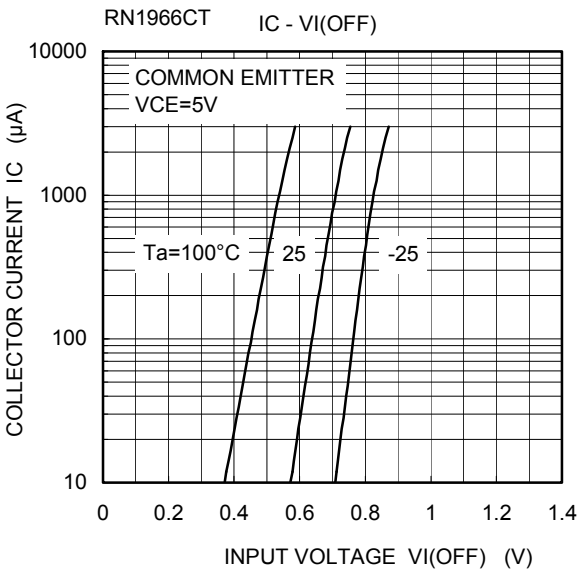
RN1964CT IC - VI(OFF)



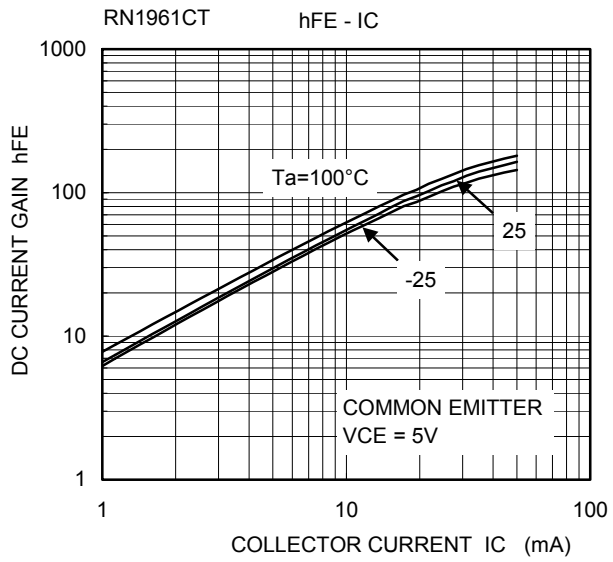
RN1965CT IC - VI(OFF)

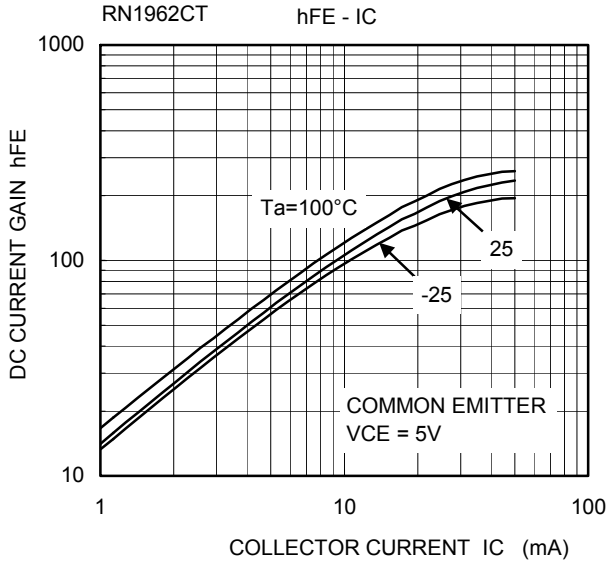


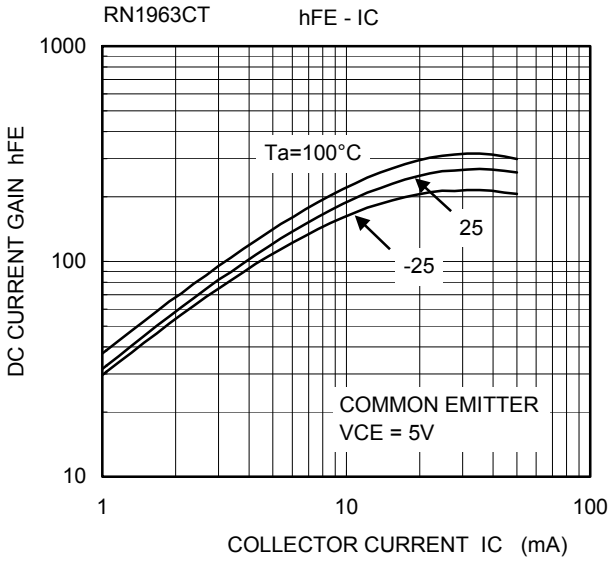
RN1966CT IC - VI(OFF)

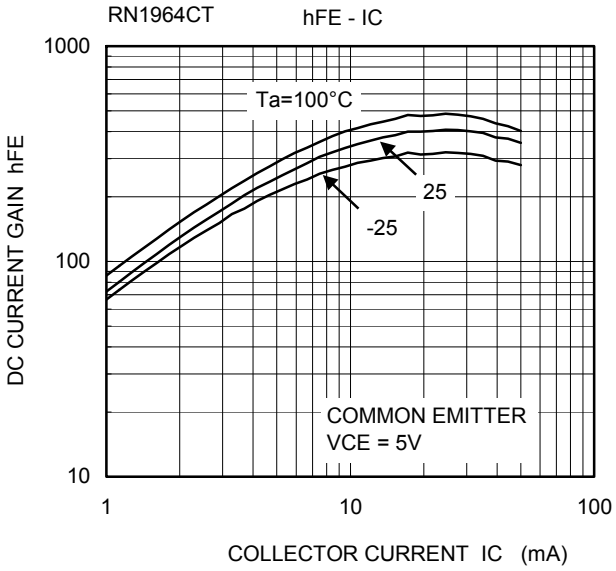


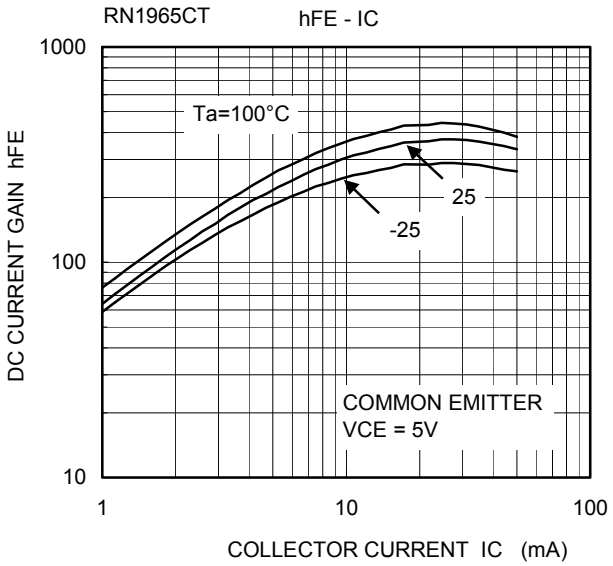
(Q1,Q2 common)

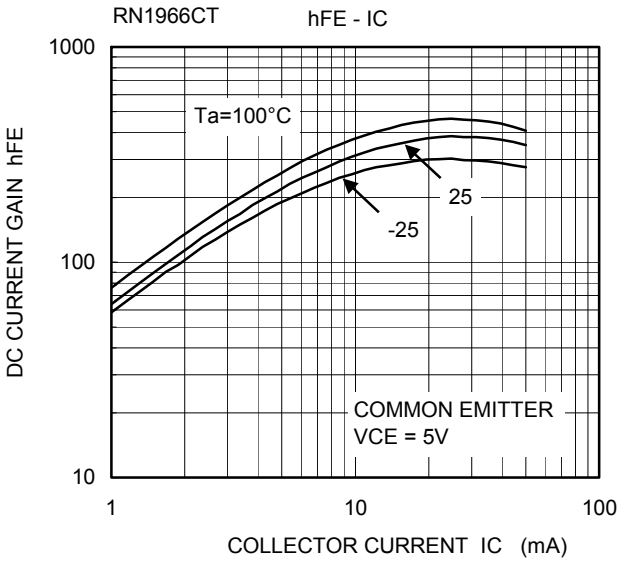

RN1961CT
hFE - IC
DC CURRENT GAIN hFE
1000
100
10
1
Ta=100°C
25
-25
COMMON EMITTER
VCE = 5V
1
10
100
COLLECTOR CURRENT IC (mA)


RN1962CT
hFE - IC
DC CURRENT GAIN hFE
1000
100
10
Ta=100°C
25
-25
COMMON EMITTER
VCE = 5V
1
10
100
COLLECTOR CURRENT IC (mA)

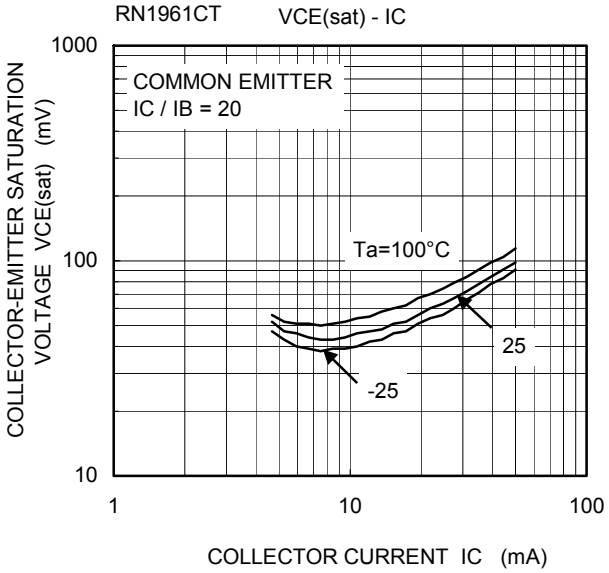

RN1963CT
hFE - IC
DC CURRENT GAIN hFE
1000
100
10
Ta=100°C
25
-25
COMMON EMITTER
VCE = 5V
1
10
100
COLLECTOR CURRENT IC (mA)

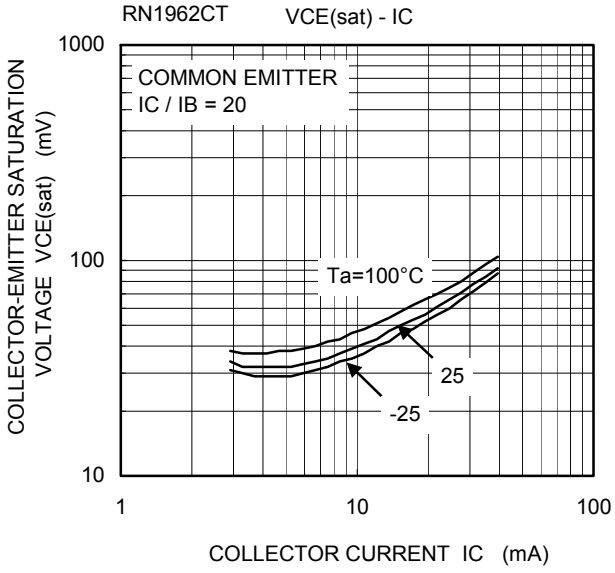

RN1964CT
hFE - IC
DC CURRENT GAIN hFE
1000
100
10
Ta=100°C
25
-25
COMMON EMITTER
VCE = 5V
1
10
100
COLLECTOR CURRENT IC (mA)

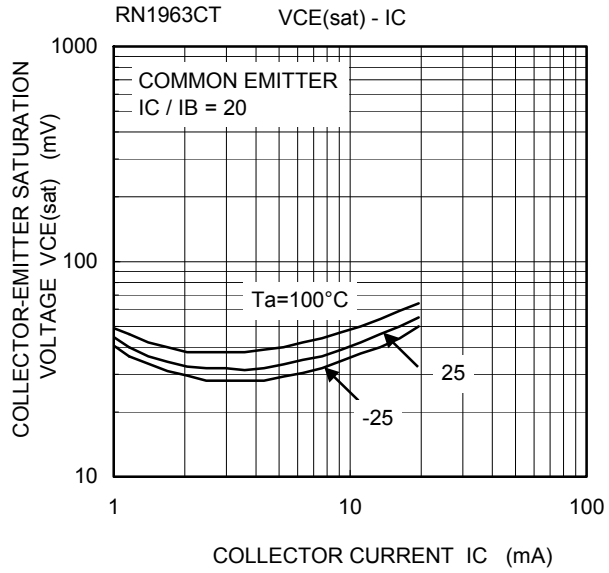

RN1965CT
hFE - IC
DC CURRENT GAIN hFE
1000
100
10
Ta=100°C
25
-25
COMMON EMITTER
VCE = 5V
1
10
100
COLLECTOR CURRENT IC (mA)

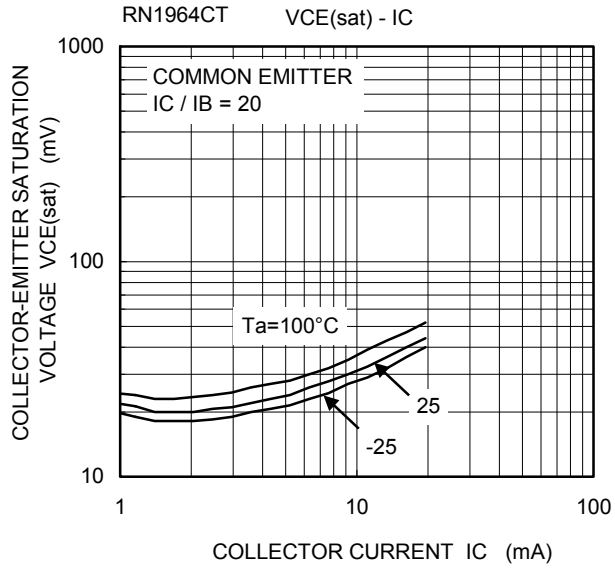

RN1966CT
hFE - IC
DC CURRENT GAIN hFE
1000
100
10
Ta=100°C
25
-25
COMMON EMITTER
VCE = 5V
1
10
100
COLLECTOR CURRENT IC (mA)

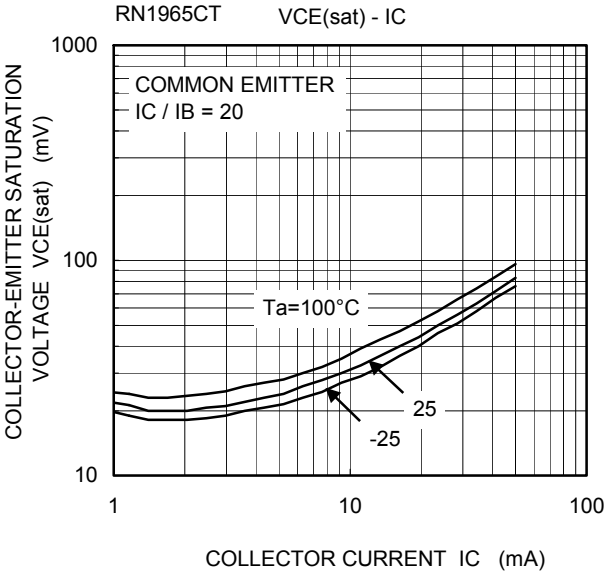
(Q1,Q2 common)

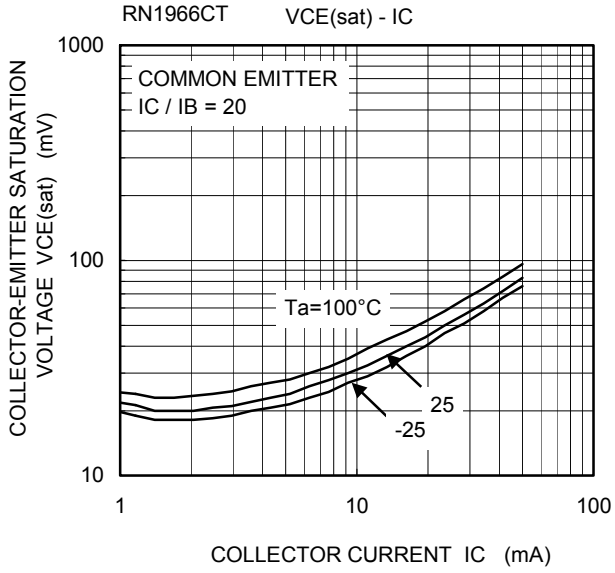

RN1961CT VCE(sat) - IC
COMMON EMITTER
IC / IB = 20
Ta=100°C
25
-25
COLLECTOR-EMITTER SATURATION VOLTAGE VCE(sat) (mV)
COLLECTOR CURRENT IC (mA)
1000
100
10
1
10
100

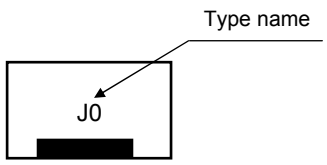
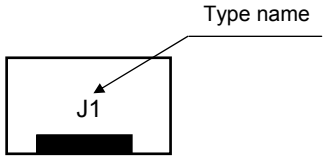
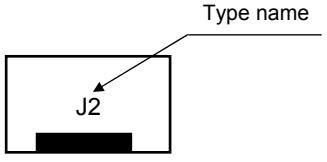
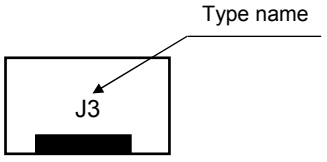
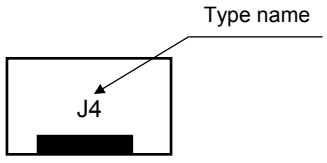
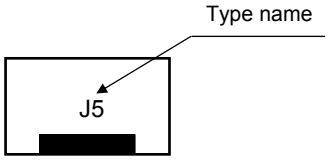

RN1962CT VCE(sat) - IC
COMMON EMITTER
IC / IB = 20
Ta=100°C
25
-25
COLLECTOR-EMITTER SATURATION VOLTAGE VCE(sat) (mV)
COLLECTOR CURRENT IC (mA)
1000
100
10
1
10
100


RN1963CT VCE(sat) - IC
COMMON EMITTER
IC / IB = 20
Ta=100°C
25
-25
COLLECTOR-EMITTER SATURATION VOLTAGE VCE(sat) (mV)
COLLECTOR CURRENT IC (mA)
1000
100
10
1
10
100


RN1964CT VCE(sat) - IC
COMMON EMITTER
IC / IB = 20
Ta=100°C
25
-25
COLLECTOR-EMITTER SATURATION VOLTAGE VCE(sat) (mV)
COLLECTOR CURRENT IC (mA)
1000
100
10
1
10
100


RN1965CT VCE(sat) - IC
COMMON EMITTER
IC / IB = 20
Ta=100°C
25
-25
COLLECTOR-EMITTER SATURATION VOLTAGE VCE(sat) (mV)
COLLECTOR CURRENT IC (mA)
1000
100
10
1
10
100


RN1966CT VCE(sat) - IC
COMMON EMITTER
IC / IB = 20
Ta=100°C
25
-25
COLLECTOR-EMITTER SATURATION VOLTAGE VCE(sat) (mV)
COLLECTOR CURRENT IC (mA)
1000
100
10
1
10
100

Type Name	Marking
RN1961CT	
RN1962CT	
RN1963CT	
RN1964CT	
RN1965CT	
RN1966CT	

Handling Precaution

When handling individual devices (which are not yet mounted on a circuit board), be sure that the environment is protected against electrostatic electricity. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.

RESTRICTIONS ON PRODUCT USE

- Toshiba Corporation, and its subsidiaries and affiliates (collectively “TOSHIBA”), reserve the right to make changes to the information in this document, and related hardware, software and systems (collectively “Product”) without notice.
- This document and any information herein may not be reproduced without prior written permission from TOSHIBA. Even with TOSHIBA’s written permission, reproduction is permissible only if reproduction is without alteration/omission.
- Though TOSHIBA works continually to improve Product’s quality and reliability, Product can malfunction or fail. Customers are responsible for complying with safety standards and for providing adequate designs and safeguards for their hardware, software and systems which minimize risk and avoid situations in which a malfunction or failure of Product could cause loss of human life, bodily injury or damage to property, including data loss or corruption. Before creating and producing designs and using, customers must also refer to and comply with (a) the latest versions of all relevant TOSHIBA information, including without limitation, this document, the specifications, the data sheets and application notes for Product and the precautions and conditions set forth in the “TOSHIBA Semiconductor Reliability Handbook” and (b) the instructions for the application that Product will be used with or for. Customers are solely responsible for all aspects of their own product design or applications, including but not limited to (a) determining the appropriateness of the use of this Product in such design or applications; (b) evaluating and determining the applicability of any information contained in this document, or in charts, diagrams, programs, algorithms, sample application circuits, or any other referenced documents; and (c) validating all operating parameters for such designs and applications. **TOSHIBA ASSUMES NO LIABILITY FOR CUSTOMERS’ PRODUCT DESIGN OR APPLICATIONS.**
- Product is intended for use in general electronics applications (e.g., computers, personal equipment, office equipment, measuring equipment, industrial robots and home electronics appliances) or for specific applications as expressly stated in this document. Product is neither intended nor warranted for use in equipment or systems that require extraordinarily high levels of quality and/or reliability and/or a malfunction or failure of which may cause loss of human life, bodily injury, serious property damage or serious public impact (“Unintended Use”). Unintended Use includes, without limitation, equipment used in nuclear facilities, equipment used in the aerospace industry, medical equipment, equipment used for automobiles, trains, ships and other transportation, traffic signaling equipment, equipment used to control combustions or explosions, safety devices, elevators and escalators, devices related to electric power, and equipment used in finance-related fields. Do not use Product for Unintended Use unless specifically permitted in this document.
- Do not disassemble, analyze, reverse-engineer, alter, modify, translate or copy Product, whether in whole or in part.
- Product shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable laws or regulations.
- The information contained herein is presented only as guidance for Product use. No responsibility is assumed by TOSHIBA for any infringement of patents or any other intellectual property rights of third parties that may result from the use of Product. No license to any intellectual property right is granted by this document, whether express or implied, by estoppel or otherwise.
- **ABSENT A WRITTEN SIGNED AGREEMENT, EXCEPT AS PROVIDED IN THE RELEVANT TERMS AND CONDITIONS OF SALE FOR PRODUCT, AND TO THE MAXIMUM EXTENT ALLOWABLE BY LAW, TOSHIBA (1) ASSUMES NO LIABILITY WHATSOEVER, INCLUDING WITHOUT LIMITATION, INDIRECT, CONSEQUENTIAL, SPECIAL, OR INCIDENTAL DAMAGES OR LOSS, INCLUDING WITHOUT LIMITATION, LOSS OF PROFITS, LOSS OF OPPORTUNITIES, BUSINESS INTERRUPTION AND LOSS OF DATA, AND (2) DISCLAIMS ANY AND ALL EXPRESS OR IMPLIED WARRANTIES AND CONDITIONS RELATED TO SALE, USE OF PRODUCT, OR INFORMATION, INCLUDING WARRANTIES OR CONDITIONS OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, ACCURACY OF INFORMATION, OR NONINFRINGEMENT.**
- Do not use or otherwise make available Product or related software or technology for any military purposes, including without limitation, for the design, development, use, stockpiling or manufacturing of nuclear, chemical, or biological weapons or missile technology products (mass destruction weapons). Product and related software and technology may be controlled under the Japanese Foreign Exchange and Foreign Trade Law and the U.S. Export Administration Regulations. Export and re-export of Product or related software or technology are strictly prohibited except in compliance with all applicable export laws and regulations.
- Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. Please use Product in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. TOSHIBA assumes no liability for damages or losses occurring as a result of noncompliance with applicable laws and regulations.