International IOR Rectifier

H-SERIES

ADVANCED ANALOG HIGH RELIABILITY RADIATION TOLERANT DC/DC CONVERTERS

+28V Input, Single/Dual Output

Description

The H-Series of DC/DC converters are radiation tolerant, high reliability devices designed for moderate radiation environments such as those encountered by low earth orbit satellites and launch vehicles. For higher radiation environments, the G-Series of DC/DC converters is recommended. System upgrades to higher radiation tolerance applications can be easily accommodated with the G-Series converters because they have the same mechanical outline and are pin compatible. For physically smaller, lower output power single and dual output applications the L-Series of DC/DC converters are recommended. Features of the H-Series include up to 40 watt output power, small size, low weight and a high tolerance to total ionizing dose, single event effects and environmental stresses such as temperature extremes, mechanical shock, and vibration. All components are fully derated to meet the requirements of MIL-STD-975 and MIL-STD-1547. Extensive documentation including Radiation Susceptibility, Thermal Analysis, Stress Analysis and MTBF are available.

The converters incorporate a fixed frequency single forward topology with magnetic feedback and an internal EMI filter. These converters are capable of meeting the conducted emissions and conducted susceptibility requirements of MIL-STD-461C without any additional components. All models include an external inhibit port and have an adjustable output voltage. They are enclosed in a hermetic 3.0" x 2.5" x 0.445" steel package and weigh less than 125 grams. The package utilizes rugged ceramic feed-through copper core pins and is sealed using parallel seam welding.

Full environmental screening includes temperature cycling, constant acceleration, fine and gross leak, and burn-in.

Non-flight versions of the H-Series converters are available for system development purposes. Variations in

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Features

- Total Dose > 25K Rad(Si)
- SEE > 37 MeV_•cm²/ma
- Internal EMI filter; Converter Capable of meeting MIL-STD-461C CE03 and CS01
- Low Weight, < 125 grams</p>
- Magnetically Coupled Feedback
- 18V to 50V Input Range (50V, 70V, and 120V Models Available)
- Up to 40W Output Power
- Single and Dual Output Models Include 3.3, 5, 5.2, 12, 15, ±5, ±12, ±15V
- High Efficiency to 82%
- -55°C to +125°C Operating Temperature Range
- 100M Ω @ 500VDC Isolation
- Under-Voltage Lockout
- Short Circuit and Overload Protection
- Output Over Voltage Limiter
- Adjustable Output Voltage
- External Inhibit
- 1,500,000 Hour MTBF

Applications

- Low Earth Orbit Satellites (LEO)
- Launch Vehicles

electrical specifications and screening to meet custom requirements can be accommodated.

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Circuit Description

The H-Series converters utilize a single forward topology with resonant reset. The nominal switching frequency is 500kHz. Electrical isolation and tight output regulation are achieved through the use of a magnetically coupled feedback. Voltage feed-forward with duty factor limiting provides high line rejection and protection against output over voltage in the event of an internal control loop failure. This mechanism limits the maximum output voltage to approximately 20% over the nominal regardless of the line voltage.

An internal EMI filter allows the converter to meet the conducted emissions requirements of MIL-STD-461C on the input power leads. A two-stage output filter reduces the typical output ripple to less than 20mV peak-to-peak.

Output current is limited under any load fault condition to approximately 125% of rated. An overload condition causes the converter output to behave like a constant current source with the output voltage dropping below nominal. The converter will resume normal operation when the load current is reduced below the current limit point. This protects the converter from both overload and short circuit conditions. The current limit point exhibits a slightly negative temperature coefficient to reduce the possibility of thermal runaway.

An under-voltage lockout circuit prohibits the converter from operating when the line voltage is too low to maintain the output voltage. The converter will not start until the line voltage rises to approximately 17 volts and will shut down when the input voltage drops below 16 volts. The one volt of hysteresis reduces the possibility of line noise interfering with the converter's start-up and shut down.

An external inhibit port is provided to control converter operation. The converter's operation is inhibited when this pin is pulled low. It is intended to be driven by an open collector logic device. The pin may be left open for normal operation and has a nominal open circuit voltage of 10.5V with respect to the input return (pin 2).

Single output models feature remote sense to automatically correct for voltage drops between the converter output anf the load. The output voltage of dual output models can be adjusted using a single external resistor.

Design Methodology

The H-Series was developed using a proven conservative design methodology which includes selecting radiation tolerant and established reliability components and fully derating to the requirements of MIL-STD-975 and MIL-STD-1547. Heavy derating of the power MOSFET gate and drain voltages minimize the possibility of SEGR and SEB. A magnetic feedback circuit is utilized instead of opto-couplers to minimize temperature, radiation and aging sensitivity. PSPICE was used extensively to predict and optimize circuit performance for both beginning and end-of-life. Thorough design analyses include Radiation Susceptibility (TREE), Stress, Thermal, and Reliability (MTBF).

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TOR Rectifier

Absolute Maximum Ratings

Input voltage range - -0.5Vdc to +60Vdc

Output power - Internally limited

Lead temperature - +300°C for 10 seconds

Operating temperature - -55°C to +135°C

Storage temperature - -55°C to +135°C

Recommended Operating Conditions

Input voltage range Input voltage range¹ Output power Operating temperature² Operating temperature¹ Operating temperature¹ Soc to +40Vdc
18Vdc to +38Vdc
18Vdc to +40Vdc
18Vdc to +38Vdc
18Vdc to +40Vdc
18Vdc to +38Vdc
18Vdc to +40Vdc
18Vdc to

Electrical Performance Characteristics

		Conditions		Limits		
Parameter	Group A Subgroup	$-55^{\circ}C \le T_C \le +85^{\circ}C$ $V_{IN} = 28V$ DC \pm 5%, $C_L = 0$ unless otherwise specified	Min	Nom	Max	Unit
Input Voltage			18	28	40	V
Output Voltage (Vout)	1 1 1 1 1 1 1	I _{OUT} = 100% rated load Note 4	3.27 4.95 5.17 11.88 14.85 ±4.95 ±11.88 ±14.85	3.30 5.00 5.20 12.00 15.00 ±5.00 ±12.00 ±15.00	3.33 5.05 5.23 12.12 15.15 ±5.05 ±12.12 ±15.15	V V V V V
M3H2803R3S M3H2805S M3H2805R2S M3H2812S M3H2815S M3H2805D M3H2805D M3H2815D	2,3 2,3 2,3 2,3 2,3 2,3 2,3 2,3	I _{OUT} = 100% rated load Note 4	3.23 4.90 5.13 11.76 14.70 ±4.90 ±11.76 ±14.70		3.37 5.10 5.27 12.24 15.30 ±5.10 ±12.24 ±15.30	V V V V V
Output power (P _{OUT}) M3H2803R3S All Others	1,2,3	V _{IN} = 18, 28, 40 Volts, Note 2	0 0		30 40	W W
Output current (I _{OUT}) M3H2803R3S M3H2805S M3H2805R2S M3H2812S M3H2815S M3H2805D M3H2812D M3H2815D	1,2,3	V _{IN} = 18, 28, 40 Volts, Note 2 Either Output, Note 3 Either Output, Note 3 Either Output, Note 3	0 0 0 0 0 0		9.10 8.00 7.70 3.34 2.67 6.40 2.67 2.14	A A A A A A
Line regulation (VR _{LINE})	1,2,3	V _{IN} = 18, 28, 40 Volts I _{OUT} = 0, 50%, 100% rated, Note 4	-0.5		0.5	%
Load regulation (VR _{LOAD})	1,2,3	I _{OUT} = 0, 50%, 100% rated, Note 4 V _{IN} = 18, 28, 40 Volts	-1.0		1.0	%
Cross regulation (VR _{CROSS}) M3H2805D M3H2812D M3H2815D	1,2,3	Duals only, Note 5 V _{IN} = 18, 28, 40 Volts	-5.0 -3.0 -3.0		5.0 3.0 3.0	% % %

For Notes to Specifications, refer to page 5

¹ Meets derating per MIL-STD-975

²For operation at +125°C see table note 13

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Electrical Performance Characteristics (continued)

		Conditions	Limits			
Parameter	Group A Subgroup	$-55^{\circ}\text{C} \le T_{\text{C}} \le +85^{\circ}\text{C}$ $V_{\text{IN}} = 28\text{V DC} \pm 5\%, \ C_{\text{L}} = 0$ unless otherwise specified	Min	Nom	Max	Unit
Output Ripple (Vrip) M3H2803R3S M3H2805S M3H2805R2S M3H2812S M3H2815S M3H2805D M3H2812D M3H2815D	1,2,3	V _{IN} = 18, 28, 40 Volts I _{OUT} = 100% rated load Notes 4, 6			35 50 50 70 80 80 80	mV p-p mV p-p mV p-p mV p-p mV p-p mV p-p mV p-p mV p-p
Input current, no load (I _{IN}) M3H2803R3S M3H2805S M3H2805R2S M3H2812S M3H2815S M3H2815S M3H2815D M3H2815D	1,2,3	I _{OUT} = 0, Pin 4 open			60 70 70 70 70 70 100	mA mA mA mA mA mA
Input current inhibited	1,2,3	Pin 4 shorted to pin 2			8	mA
Switching frequency (F _S)	1,2,3		450	500	550	KHz
Efficiency (E _{FF}) M3H2803R3S M3H2805S M3H2805R2S M3H2812S M3H2815S M3H2805D M3H2812D M3H2815D	1,2,3	I _{OUT} = 100% rated load Note 4	68 77 77 78 78 77 78 78	73 80 80 81 82 80 81 82		% % % % % %
Enable Input (Inhibit Function) open circuit voltage drive current (sink) voltage range	1,2,3	Note 1	9.5 -0.5		11.5 500 50	V μΑ V
Current Limit Point Expressed as a percentage of full rated load current	1,2,3	V _{out} = 90% of Nominal, Note 4	105		130	%
Power dissipation, load fault (PD)	1,2,3	Short Circuit, Overload, Note 8			12	W
Output response to step load changes (V _{TLD})	4,5,6	Half Load to/from Full Load, Notes 4,9	-300		300	mV pk
Recovery time, step load changes (T _{TLD})	4,5,6	Half Load to/from Full Load, Note 4,9,10			200	μS
Output response to step line changes (V _{TLN})	4,5,6	18V to/from 40V I _{OUT} = 100% rated load, Notes 1,4,11	-300		300	mV pk
Recovery time, step line changes (T _{TLN})	4,5,6	18V to/from 40V I _{OUT} = 100% rated load, Notes 1,4,10,11			200	μs
Turn-on Response Overshoot (V _{OS}) Turn-on Delay (T _{DLY})	4,5,6	No Load, Full Load Notes 4,12	2		500 10	mV ms



Electrical Performance Characteristics (continued)

	Group A	Conditions $-55^{\circ}C \le T_{C} \le +85^{\circ}C$ $V_{IN} = 28V DC \pm 5\%, C_{I} = 0$	LIMITS			
Parameter	Subgroup	Unless otherwise specified	Min	Nom	Max	Unit
Capacitive Load (CL) M3H2803R3S M3H2805S M3H2005R2S M3H2812S M3H2815S M3H2805D M3H2812D M3H2815D	1	I _{OUT} = 100% rated load No effect on DC performance Notes 1, 4, 7			2200 1000 1000 180 120 500 90 60	<u> </u>
Line Rejection	1	I _{OUT} = 100% rated load DC to 50KHz, Notes 1, 4	40	50		dB
Isolation	1	Input to Output or Any Pin to Case except pin 3, test @ 500VDC	100			МΩ
Device Weight					125	grams
MTBF		MIL-HDBK-217F2, SF, 35°C	1.5 x 10 ⁶			Hours

Table I. <u>Electrical Performance Characteristics</u> - notes

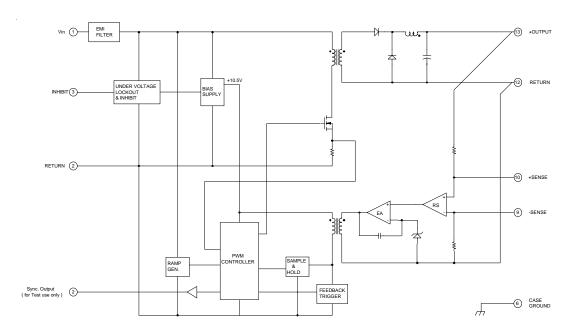
- 1) Parameter is tested as part of design characterization or after design changes. Thereafter, parameter shall be guaranteed to the limits specified.
- Parameter verified during line and load regulation tests.
- 3) Output load current must be distributed such that at least 20% of the total load current is being provided by one of the outputs.
- 4) Load current split equally between outputs on dual output models.
- Cross regulation is measured with 20% rated load on output under test while changing the load on the other output from 20% to 80% of rated.
- 6) Guaranteed for a D.C. to 20MHz bandwidth. Tested using a 20KHz to 10MHz bandwidth using the circuit shown below.
- 7) Capacitive load may be any value from 0 to the maximum limit without compromising dc performance. A capacitive load in excess of the maximum limit may interfere with the proper operation of the converter's overload protection, causing erratic behavior during turn-on
- 8) Overload power dissipation is defined as the device power dissipation with the load set such that $V_{\text{OUT}} = 90\%$ of nominal.
- 9) Load step transition time \leq 10 μ Sec.
- Recovery time is measured from the initiation of the transient to where V_{OUT} has returned to within ±1% of its steady state value.
- 11) Line step transition time \leq 100 $\mu Sec.$
- 12) Turn-on delay time from either a step application of input power or a logic low to a logic high transition on the inhibit pin (pin 4) to the point where $V_{OUT} = 90\%$ of nominal.
- 13) For operation at temperatures between +85°C and +125°C, derate the maximum output power linearly from 100% to 75%.

Radiation Performance Characteristics

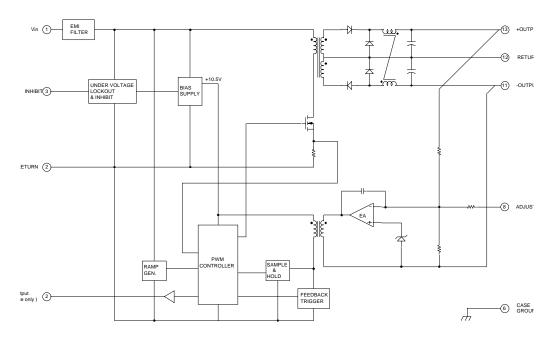
Test	Conditions	Min	Тур	Highest Level Tested	Unit
Total Ionizing Dose (Gamma)	MIL-STD-883, Method 1019 Operating bias applied during exposure, Full Rated Load, $V_{\scriptscriptstyle IN}$ = 28V	25	40	60	Krads (Si)
Single Event Effects SEU, SEL, SEGR, SEB	Heavy ions (LET) Operating bias applied during exposure, Full Rated Load, $V_{\scriptscriptstyle IN}=28V$	37	82	82	MeV•cm² /mg
Dose Rate (Gamma Dot) Temporary Saturation Survival	MIL-STD-883, Method 1023 Operating bias applied during exposure, Full Rated Load, V _{IN} = 28V	1E8 1E10		1.5E9	Rads (Si)/sec
Neutron Fluence	MIL-STD-883, Method 1017	3E12		8E12	Neutrons /cm²
Proton Fluence	E > 10MeV	TBD		N/A	protons /cm²

Device Screening

Test Inspection	Method	Condition
Element Evaluation	MIL-PRF-38534 class H equivalent with SEM	
Nondestructive Bond Pull	MIL-STD-883, Method 2023	
Internal Visual	MIL-STD-883, Method 2017	
Temperature Cycling	MIL-STD-883, Method 1010	С
Constant Acceleration	MIL-STD-883, Method 2001 (2k-g)	A, Y1 axis only
Electrical	In accordance with device specification	
Burn-in	MIL-STD-883, Method 1015	160 Hours
Final Electrical (Group A)	In accordance with device specification	
Seal Fine Leak Gross Leak	MIL-STD-883, Method 1014	A1 C
External Visual	MIL-STD-883, Method 2009	

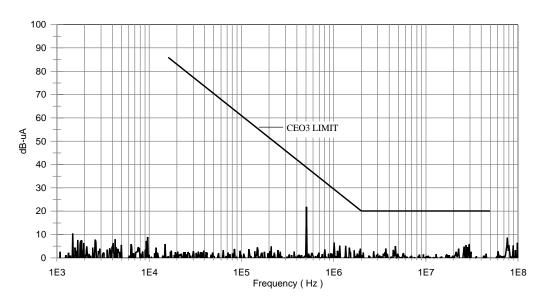


Block Diagram - Single Output

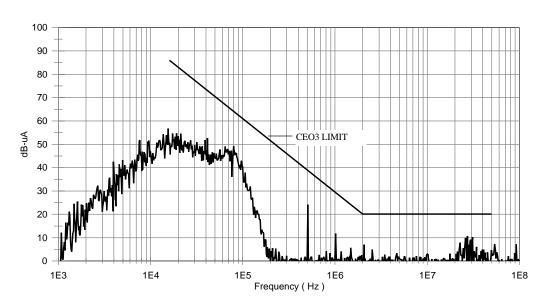


Block Diagram - Dual Output

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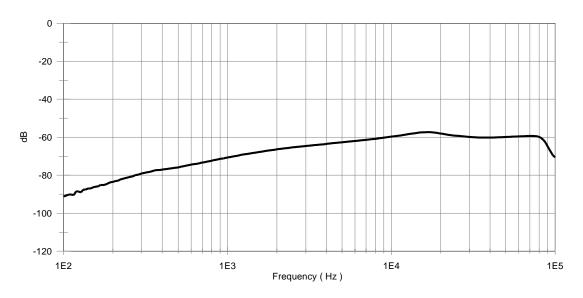
Conducted Emmissions, Common Mode



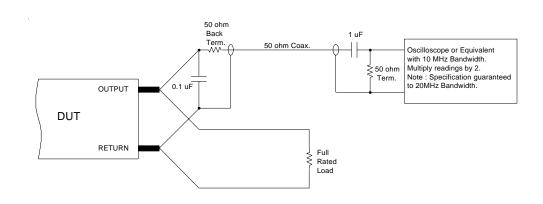
Conducted Emmissions, Normal Mode



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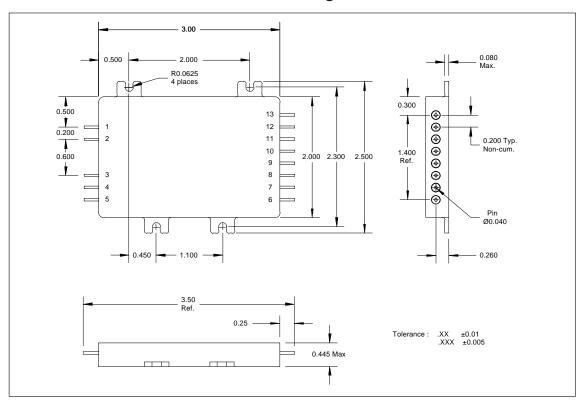


Line Rejections, 28Vdc + 8Vac-p



Circuit for Measuring Output Ripple Voltage

Mechanical Diagram



Pin Designation (Single/Dual)

Part Number

Pin Pin 1 Pin 2 Pin 3 Pin 4 Pin 5 Pin 6	Signal +V Input Input Return Inhibit NC Sync. Output (T.P.) Case Ground	Pin 8 Pin 9 Pin 10 Pin 11 Pin 12 Pin 13	Signal NC/ Adjust -Sense/ NC +Sense/ NC NC/ -Output Output Return +Output	M3H 28 Model — Nominal Input Voltage — 28 = 28V	Outputs S = Single D = Dual Nominal Output Voltages 03R3 = 3.3V, 05R2 = 5.2V 05 = 5V, 12 = 12V, 15 = 15V
Pin 7	NC				



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Data and specifications subject to change without notice. 02/03