

IRK.F132.. SERIES

**FAST THYRISTOR/ DIODE and
 THYRISTOR/ THYRISTOR**

INT-A-pak™ Power Modules

130 A

Features

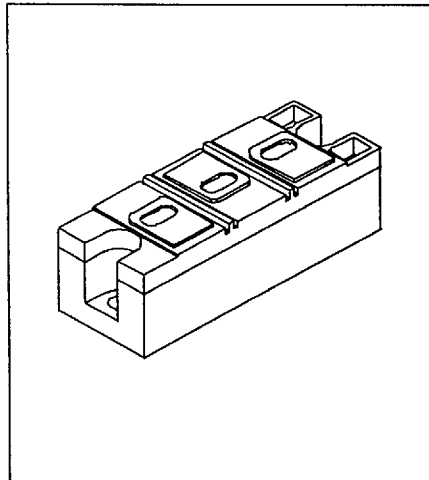
- Fast turn-off thyristor
- Fast recovery diode
- High surge capability
- Electrically isolated baseplate
- 3000 V_{RMS} Isolating voltage
- Industrial standard package
- UL E78996 approved 

Description

These series of INT-A-pak modules are intended for applications such as self-commutated inverters, DC choppers, electronic welders, induction heating and others where fast switching characteristics are required.

Major Ratings and Characteristics

Parameters	IRK.F132..	Units
$I_{T(AV)}$	130	A
@ T_C	90	°C
$I_{T(RMS)}$	293	A
I_{TSM} @ 50Hz	3210	A
@ 60Hz	3360	A
i^2t @ 50Hz	51.5	KA ² s
@ 60Hz	47.0	KA ² s
i^2/t	515	KA ² /s
t_q	15	μs
t_{rr}	2	μs
V_{DRM}/V_{RRM}	up to 800	V
T_J range	-40 to 125	°C



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ELECTRICAL SPECIFICATIONS

Voltage Ratings

Type number	Voltage Code	V_{RRM}/V_{DRM} maximum repetitive peak reverse voltage V	V_{RSM} maximum non-repetitive peak rev. voltage V	I_{RRM}/I_{DRM} max. @ $T_J = 125^\circ\text{C}$ mA
IRK.F132..	04	400	400	30
	08	800	800	

Current Carrying Capacity

Frequency f	180°el		180°el		100µs		Units
	t_{TM}	t_{TM}	t_{TM}	t_{TM}	t_{TM}	t_{TM}	
50Hz	250	420	408	640	2465	3460	A
400Hz	320	530	485	800	1470	2150	A
2500Hz	240	390	400	650	540	830	A
5000Hz	210	340	340	530	340	530	A
10000Hz	160	275	300	415	-	-	A
Recovery voltage Vr	50	50	50	50	50	50	V
Voltage before turn-on Vd	80% V_{DRM}		80% V_{DRM}		80% V_{DRM}		V
Rise of on-state current di/dt	50	50	-	-	-	-	A/µs
Case temperature	90	60	90	60	90	60	°C
Equivalent values for RC circuit	47 Ω / 0.22 µF		47 Ω / 0.22 µF		47 Ω / 0.22 µF		

On-state Conduction

Parameter	IRK.F132..	Units	Conditions								
$I_{T(AV)}$ Maximum average on-state current @ Case temperature	130 90	A °C	180° conduction, half sine wave								
$I_{T(RMS)}$ Maximum RMS current	293	A	$T_C = 90^\circ\text{C}$, as AC switch								
I_{TSM} Maximum peak, one-cycle, non-repetitive surge current	3210 3360 2700 2825	A	<table border="1"> <tr> <td>t = 10ms</td> <td>No voltage reappplied</td> </tr> <tr> <td>t = 8.3ms</td> <td>reappplied</td> </tr> <tr> <td>t = 10ms</td> <td>100% V_{RRM} reappplied</td> </tr> <tr> <td>t = 8.3ms</td> <td>reappplied</td> </tr> </table>	t = 10ms	No voltage reappplied	t = 8.3ms	reappplied	t = 10ms	100% V_{RRM} reappplied	t = 8.3ms	reappplied
t = 10ms	No voltage reappplied										
t = 8.3ms	reappplied										
t = 10ms	100% V_{RRM} reappplied										
t = 8.3ms	reappplied										
I^2t Maximum I^2t for fusing	51.5 47.0 36.5 33.3	KA ² s	<table border="1"> <tr> <td>t = 10ms</td> <td>No voltage reappplied</td> </tr> <tr> <td>t = 8.3ms</td> <td>reappplied</td> </tr> <tr> <td>t = 10ms</td> <td>100% V_{RRM} reappplied</td> </tr> <tr> <td>t = 8.3ms</td> <td>reappplied</td> </tr> </table>	t = 10ms	No voltage reappplied	t = 8.3ms	reappplied	t = 10ms	100% V_{RRM} reappplied	t = 8.3ms	reappplied
t = 10ms	No voltage reappplied										
t = 8.3ms	reappplied										
t = 10ms	100% V_{RRM} reappplied										
t = 8.3ms	reappplied										
F^2t Maximum F^2t for fusing	515	KA ² µs	t = 0 to 10ms, no voltage reappplied								
$V_{T(RO1)}$ Low level value of threshold voltage	1.16	V	$(16.7\% \times \pi \times I_{T(AV)} < I < \pi \times I_{T(AV)})$, $T_J = T_J \text{ max.}$								
$V_{T(RO2)}$ High level value of threshold voltage	1.25	V	$(I > \pi \times I_{T(AV)})$, $T_J = T_J \text{ max.}$								
r_{11} Low level value of on-state slope resistance	0.92	mW	$(16.7\% \times \pi \times I_{T(AV)} < I < \pi \times I_{T(AV)})$, $T_J = T_J \text{ max.}$								
r_{12} High level value of on-state slope resistance	0.77	mW	$(I > \pi \times I_{T(AV)})$, $T_J = T_J \text{ max.}$								
V_{TM} Maximum on-state voltage drop	1.71	V	$I_{pk} = 600\text{A}$, $T_J = T_J \text{ max.}$, $t_p = 10\text{ms}$ sine pulse								
I_H Maximum holding current	600	mA	$T_J = 25^\circ\text{C}$, $I_p > 30\text{A}$								
I_L Typical latching current	1000	mA	$T_J = 25^\circ\text{C}$, $V_A = 12\text{V}$, $R_a = 6\Omega$, $I_g = 1\text{A}$								

Switching

Parameter	IRK.F132..	Units	Conditions
di/dt Maximum non-repetitive rate of rise	800	A/μs	Gate drive 20V, 20Ω, tr ≤ 1ms, V _D = 80% V _{DRM} T _J = 25°C
t _{rr} Maximum recovery time	2	μs	I _{TM} = 350A, di/dt = -25A/μs, V _R = 50V, T _J = 25°C
t _q Maximum turn-off time	L		I _{TM} = 350A, T _J = 125°C, di/dt = -25A/μs, V _R = 50V, dv/dt = 400V/μs linear to 80% V _{DRM}
	15	μs	

Blocking

Parameter	IRK.F132..	Units	Conditions
dv/dt Maximum critical rate of rise of off-state voltage	1000	V/μs	T _J = 125°C., exponential to = 67% V _{DRM}
V _{INS} RMS isolation voltage	3000	V	50 Hz, circuit to base, T _J = 25°C, t = 1 s
I _{RRM} Maximum peak reverse and off-state leakage current	30	mA	T _J = 125°C, rated V _{DRM} /V _{RRM} applied

Triggering

Parameter	IRK.F132..	Units	Conditions
P _{GM} Maximum peak gate power	60	W	f = 50 Hz, d% = 50
P _{GI(AV)} Maximum peak average gate power	10	W	T _J = 125°C, f = 50Hz, d% = 50
I _{GM} Maximum peak positive gate current	10	A	T _J = 125°C, t _p ≤ 5ms
-V _{GM} Maximum peak negative gate voltage	5	V	
I _{GT} Max. DC gate current required to trigger	200	mA	T _J = 25°C, V _{bk} 12V, Ra = 6
V _{GT} DC gate voltage required to trigger	3	V	
I _{GD} DC gate current not to trigger	20	mA	T _J = 125°C, rated V _{DRM} applied
V _{GD} DC gate voltage not to trigger	0.25	V	

Thermal and Mechanical Specifications

Parameter	IRK.F132..	Units	Conditions
T _J Max. junction operating temperature range	-40 to 125	°C	
T _{stg} Max. storage temperature range	-40 to 150		
R _{thJC} Max. thermal resistance, junction to case	0.17	K/W	Per junction, DC operation
R _{thCHs} Max. thermal resistance, case to heatsink	0.035	K/W	Mounting surface flat and greased Per module
T Mounting torque ± 10%	IAP to heatsink	4 - 6 (35 - 53)	A mounting compound is recommended. The torque should be rechecked after a period of 3 hours to allow for the spread of the compound. Use of cable lugs is not recommended, busbars should be used and restrained during tightening. Threads must be lubricated with a compound
	busbar to IAP	4 - 6 (35 - 53)	
wt Approximate weight	500 (17.8)	g (oz)	

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ΔR_{thJC} Conduction

(The following table shows the increment of thermal resistance R_{thJC} when devices operate at different conduction angles than DC)

Conduction angle	Sinusoidal conduction	Rectangular conduction	Units	Conditions
180°	0.016	0.011	K/W	$T_J = 125^\circ\text{C}$
120°	0.019	0.020		
90°	0.024	0.026		
60°	0.035	0.037		
30°	0.060	0.060		

Ordering Information Table

Device Code

IRK	T	F	13	2	-	08	H	L	N
①	②	③	④	⑤		⑥	⑦	⑧	⑨

- ① - Module type
- ② - Circuit configuration
- ③ - Fast SCR
- ④ - Current rating: $I_{T(AV)} \times 10$ rounded
- ⑤ - 1 = option with spacers and longer terminal screws
2 = option with standard terminal screws
- ⑥ - Voltage code: Code $\times 100 = V_{RRM}$ (See Voltage Ratings Table)
- ⑦ - dv/dt code: $H \leq 400V/\mu s$
- ⑧ - t_q code: $L \leq 15\mu s$
- ⑨ - None = Standard devices
N = Aluminum nitride substrate

NOTE: To order the Optional Hardware see Bulletin I27900

Outline Table

- All dimensions in millimeters (Inches)
- Dimensions are nominal
- Full engineering drawings are available on request
- UL Identification number for gate and cathode wire: UL 1385
- UL Identification number for package: UL 94V0

For all types	A	B	C	D	E
IRK...1	25 (0.98)	---	---	41 (1.61)	47 (1.85)
IRK...2	23 (0.91)	30 (1.18)	36 (1.42)	---	---

IRKTF.. IRKHF.. IRKLF.. IRKUF.. IRKVF.. IRKKF.. IRKNF..

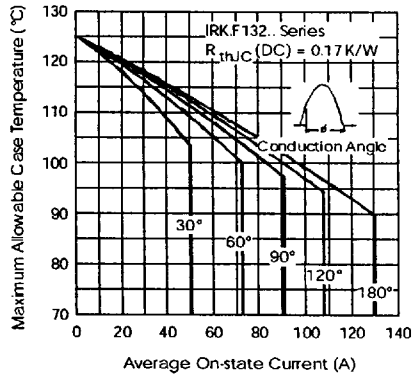


Fig. 1 - Current Ratings Characteristics

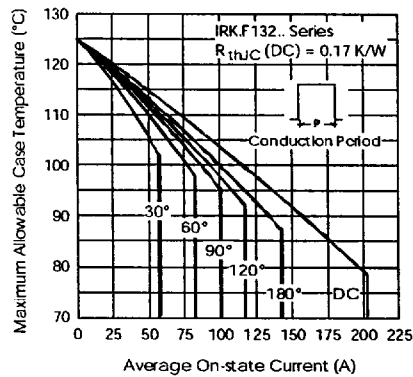


Fig. 2 - Current Ratings Characteristics

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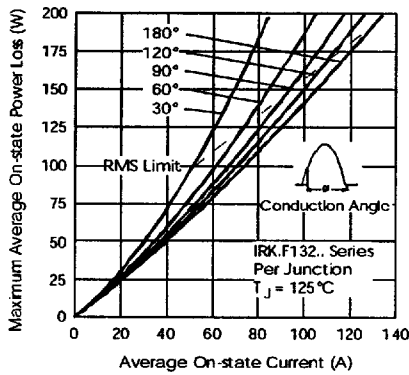


Fig. 3 - On-state Power Loss Characteristics

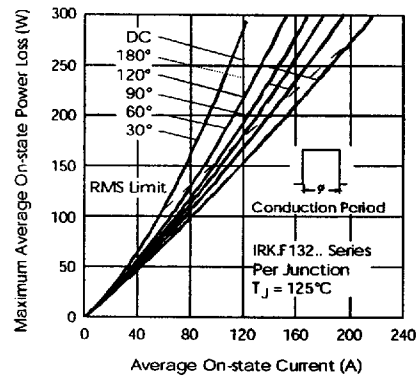


Fig. 4 - On-state Power Loss Characteristics

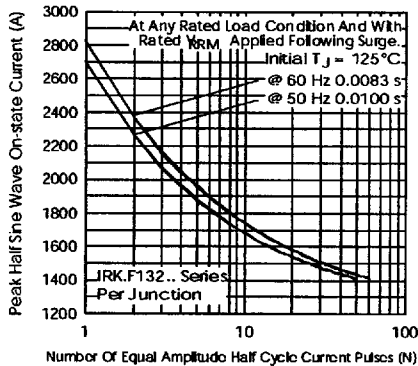


Fig. 5 - Maximum Non-Repetitive Surge Current

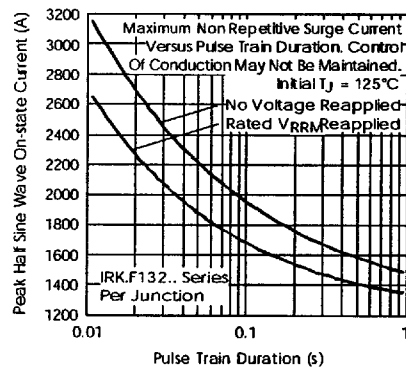


Fig. 6 - Maximum Non-Repetitive Surge Current

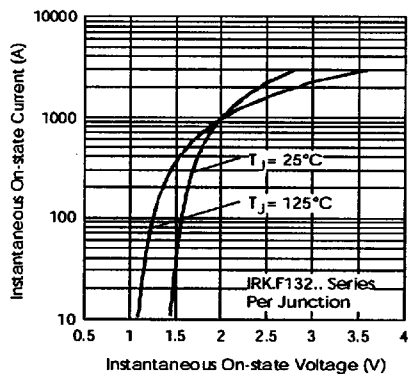


Fig. 7 - On-state Voltage Drop Characteristics

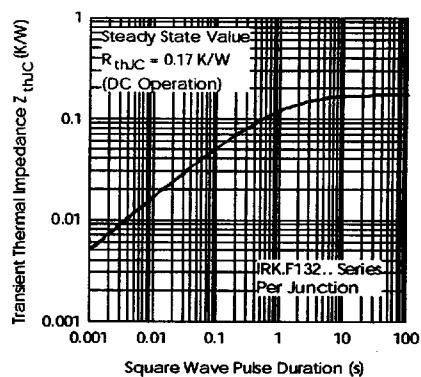


Fig. 8 - Thermal Impedance Z_{thJC} Characteristic

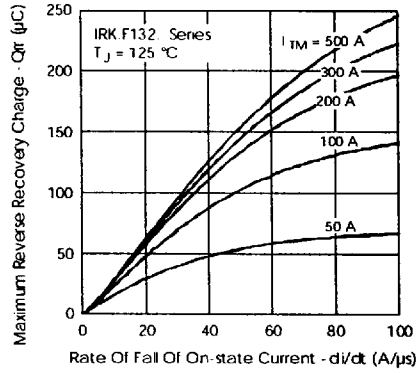


Fig. 9 - Reverse Recovery Charge Characteristics

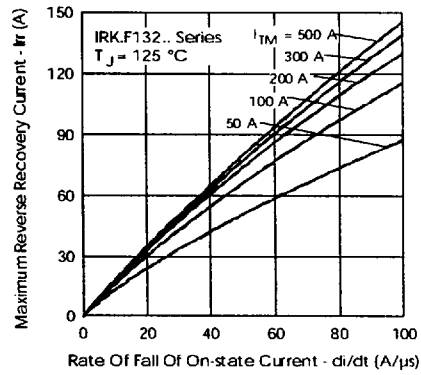


Fig. 10 - Reverse Recovery Current Characteristics

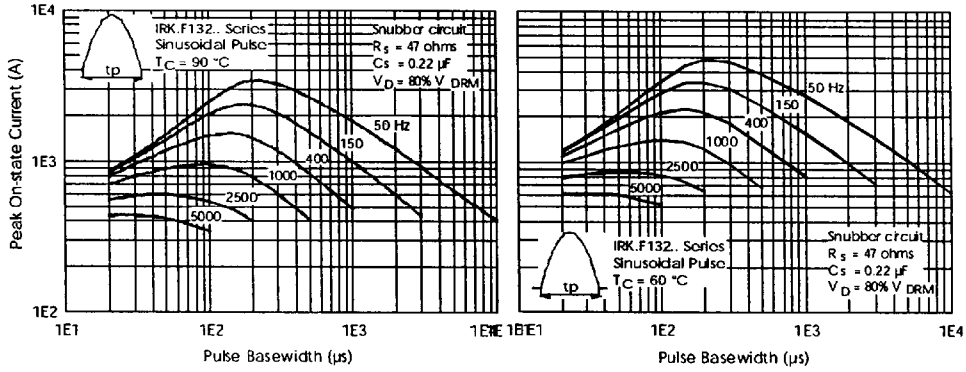


Fig. 11 - Frequency Characteristics

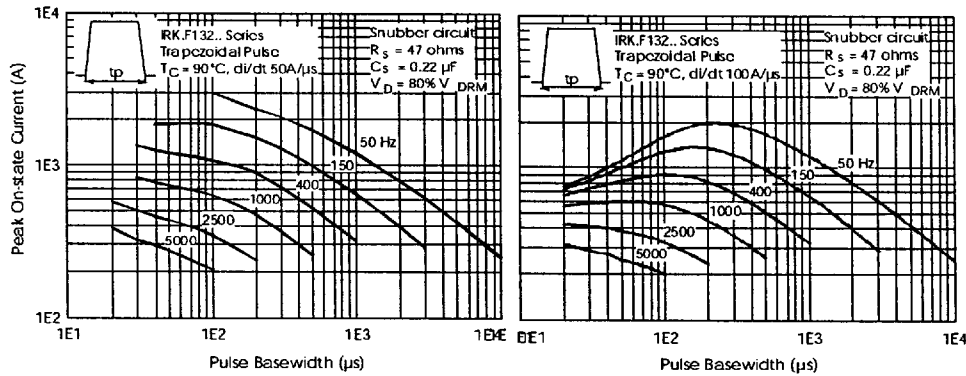


Fig. 12 - Frequency Characteristics

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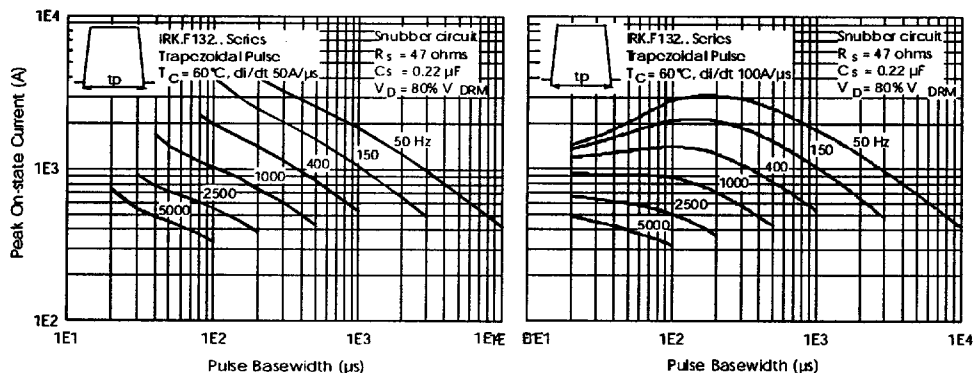


Fig. 13 - Frequency Characteristics

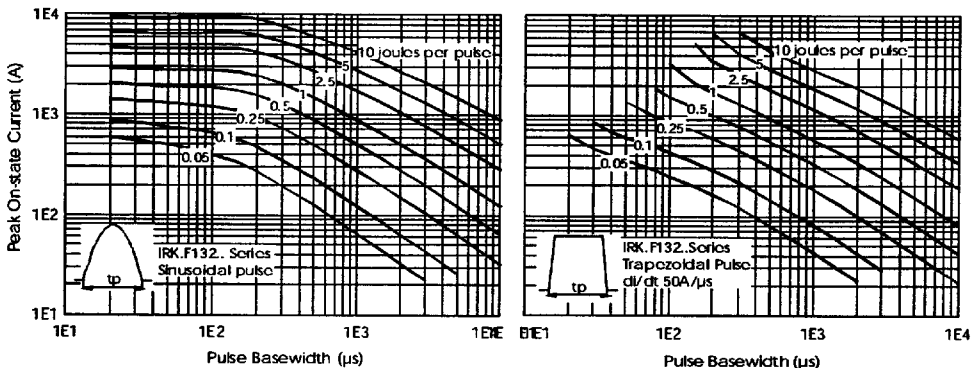


Fig. 14 - Maximum On-state Energy Power Loss Characteristics

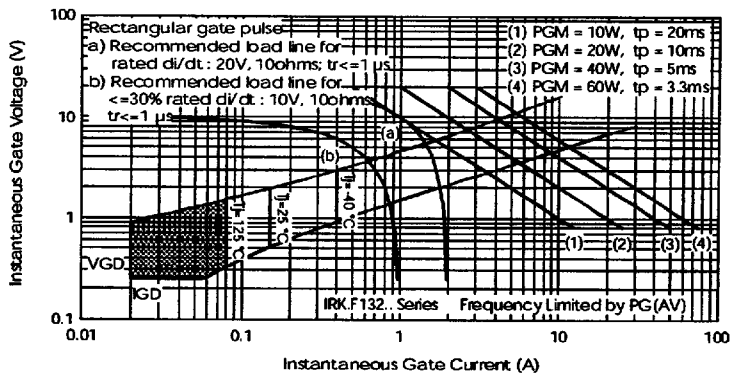


Fig. 15 - Gate Characteristics