

DA2J104

Silicon epitaxial planar type

For high speed switching circuits

■ Features

- Small reverse current I_R
- Low terminal capacitance C_t
- Contributes to miniaturization of sets, reduction of component count.
- Eco-friendly Halogen-free package

■ Packaging

Embossed type (Thermo-compression sealing): 3000 pcs / reel (standard)

■ Package

- Code
SMini2-F5-B
- Pin Name
1: Cathode
2: Anode

■ Marking Symbol: C1

■ Absolute Maximum Ratings $T_a = 25^\circ\text{C}$

| Parameter | Symbol | Rating | Unit |
|---|-----------|-------------|------------------|
| Reverse voltage | V_R | 80 | V |
| Maximum peak reverse voltage | V_{RM} | 80 | V |
| Forward current | I_F | 200 | mA |
| Peak forward current | I_{FM} | 600 | mA |
| Non-repetitive peak forward surge current * | I_{FSM} | 1 | A |
| Junction temperature | T_j | 150 | $^\circ\text{C}$ |
| Storage temperature | T_{stg} | -55 to +150 | $^\circ\text{C}$ |

Note) *: 1 t = 1 s

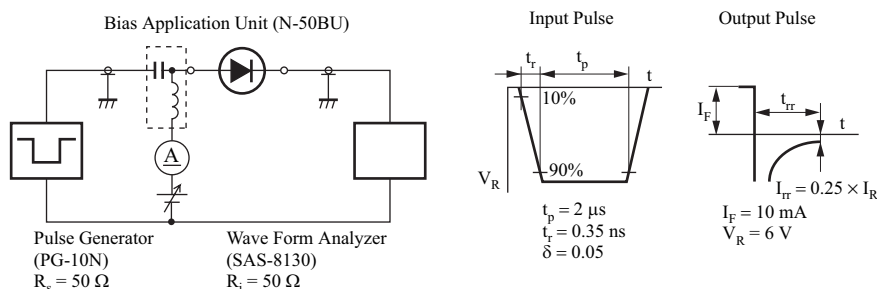
■ Electrical Characteristics $T_a = 25^\circ\text{C} \pm 3^\circ\text{C}$

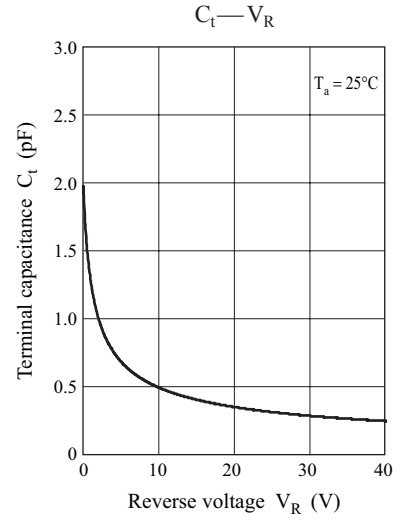
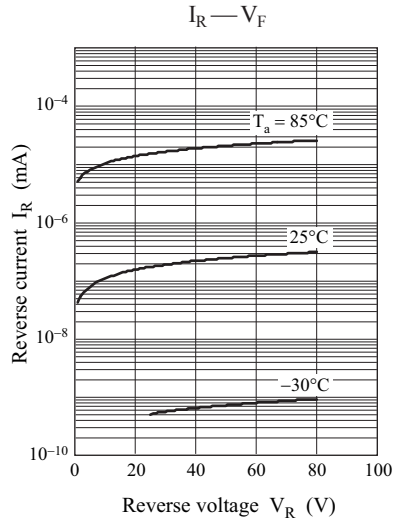
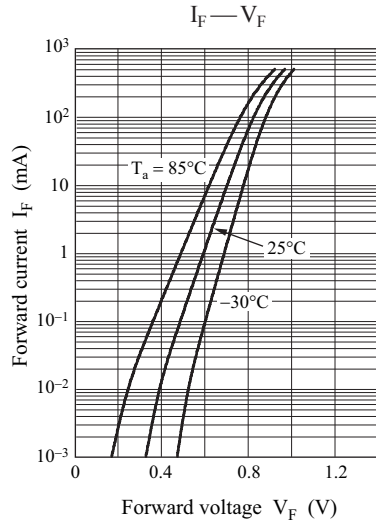
| Parameter | Symbol | Conditions | Min | Typ | Max | Unit |
|-------------------------|----------|--|-----|------|------|------|
| Forward voltage | V_F | $I_F = 200 \text{ mA}$ | | 0.90 | 1.10 | V |
| Reverse voltage | V_R | $I_R = 100 \mu\text{A}$ | 80 | | | V |
| Reverse current | I_R | $V_R = 80 \text{ V}$ | | | 500 | nA |
| Terminal capacitance | C_t | $V_R = 0 \text{ V}, f = 1 \text{ MHz}$ | | | 4 | pF |
| Reverse recovery time * | t_{rr} | $I_F = 10 \text{ mA}, V_R = 6 \text{ V}, I_{rr} = 0.25 \times I_R$ | | | 10 | ns |

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7031 measuring methods for diodes.

2. Absolute frequency of input and output is 100 MHz

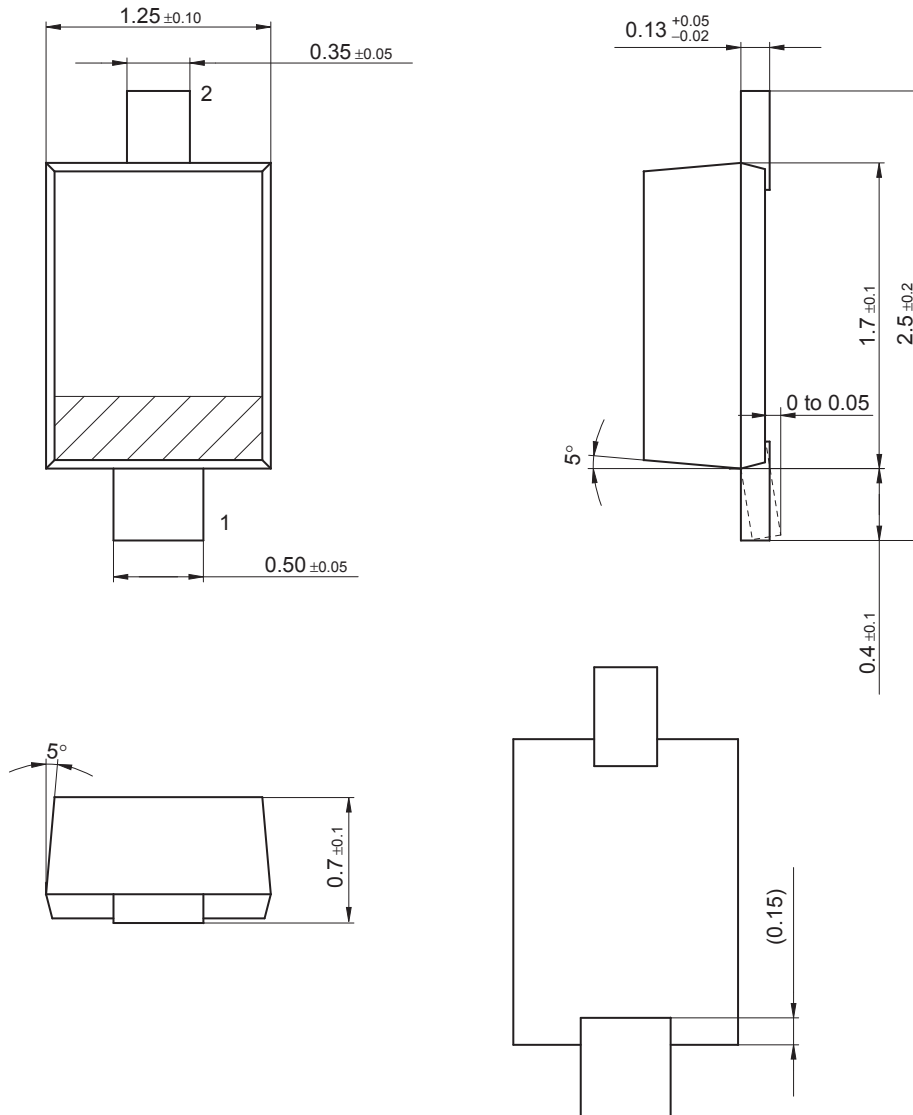
3. *: t_{rr} measurement circuit





SMini2-F5-B

Unit: mm



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