SPECIFICATION FOR LCD MODULE

Model No. <u>TM10481ACAG2</u>

Prepared by: Date: Checked by: Date: Verified by: Date: Approved by: Date:

TIANMA MICROELECTRONICS CO., LED

REVISION RECORD

Date	Ref. Page	Revision No.	Revision Items	Check & Approval

1 General Specifications:

1.1 Display type: FSTN

1.2 Display color*:

Display color: Black
Background: White

1.3 Polarizer mode: Reflective/Positive

1.4 Viewing Angle: 6:00

1.5 Driving Method: 1/81 Duty 1/10 Bias

1.6 Backlight: None

1.7 Controller: S6B0718X01-B0CZ

1.8 Data Transfer: 8 Bit Parallel

1.9 Operating Temperature: 0----+50 °C

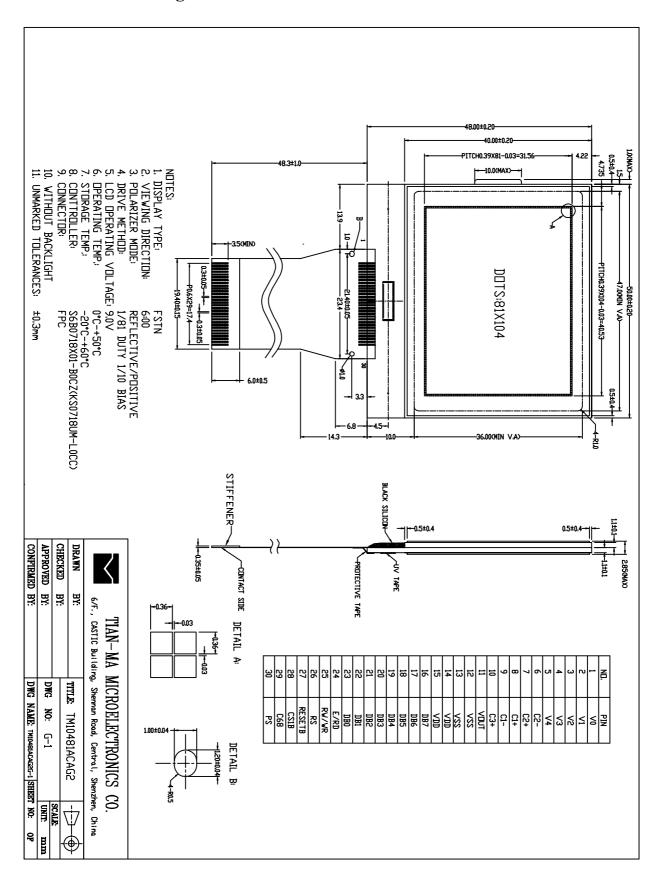
Storage Temperature: -20----+60 ℃

1.10 Outline Dimensions: Refer to outline drawing on next page

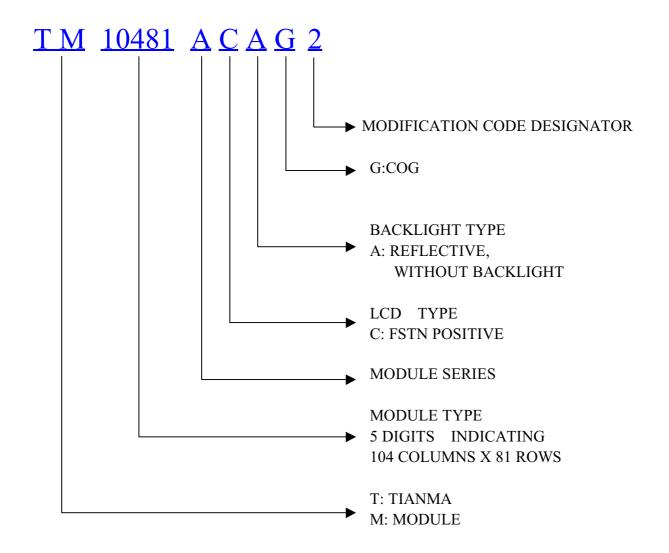
1.11 Weight: Approx. 10g

^{*} Color tone is slightly changed by temperature and driving voltage.

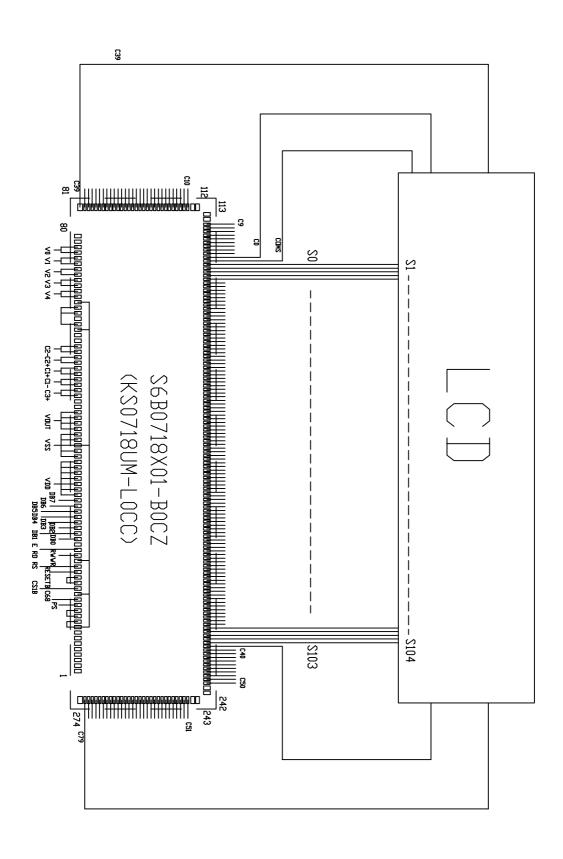
2 Outline Drawing



3 LCD Module Part Numbering System



4 Circuit Block Diagram



5 Absolute Maximum Ratings

Item	Symbol	Min.	Max.	Unit	Remark
Power Supply Voltage	V _{DD} -V _{SS}	-0.3	3.6	V	
LCD Driving Voltage	VLCD	-	25.0	V	
Operating Temperature Range	Тор	0	+50	°C	No
Storage Temperature Range	Тѕт	-20	+60		Condensation

6 Electrical Specifications and Instruction Code

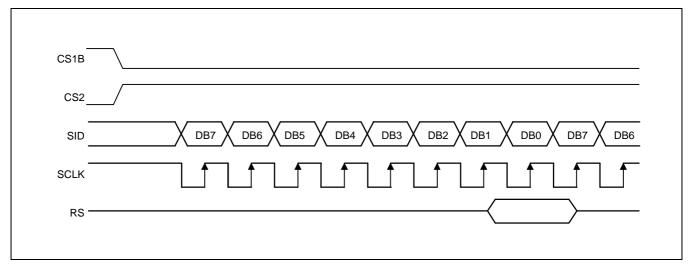
6.1 Electrical characteristics

Iter	n	Symbol	Min.	Тур.	Max.	Unit
Supply Voltage (Logic)		V _{DD} -V _{SS}	2.4	3.0	3.6	V
Supply Voltage (LCD Drive)		Vlcd	-	9.0	-	V
Input	High	$V_{\text{\tiny IH}}$ $(V_{\text{DD}}\!\!=\!\!3.0\mathrm{V})$	$0.8V_{ m DD}$	-	V _{DD} +0.3	V
Signal Voltage	Low	V_{LL} $(V_{\text{DD}}=3.0\text{V})$	0	-	0.2 V _{DD}	V
Supply current (Logic)		I_{DD} $(V_{DD}-V_{SS}=3.0V)$	-	-	100.0	uA

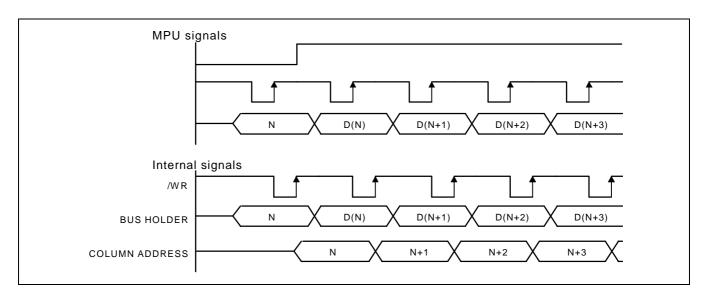
6.2 Interface Signals

Pin No.	Symbol	Level	Description
1	V0	-	Power supply voltage for LCD
2	V1	-	Power supply voltage for LCD
3	V2	-	Power supply voltage for LCD
4	V3	-	Power supply voltage for LCD
5	V4	-	Power supply voltage for LCD
6	C2-	-	Capacitor pin for voltage converter
7	C2+	-	Capacitor pin for voltage converter
8	C1+	-	Capacitor pin for voltage converter
9	C1-	-	Capacitor pin for voltage converter
10	C3+	-	Capacitor pin for voltage converter
11	VOUT	-	Voltage convert I/O port
12	VSS	0V	Ground
13	VSS	0V	Ground
14	VDD	3.0V	Power supply voltage for logic
15	VDD	3.0V	Power supply voltage for logic
16	DB7	H/L	Data bit 7
17	DB6	H/L	Data bit 6
18	DB5	H/L	Data bit 5
19	DB4	H/L	Data bit 4
20	DB3	H/L	Data bit 3
21	DB2	H/L	Data bit 2
22	DB1	H/L	Data bit 1
23	DB0	H/L	Data bit 0
24	E/RD	H/L	6800-series MPU:Enable Clock Input 8080-series MPU:Enable Clock Input
25	RW/WR	H/L	Read/Write execution control pin
26	RS	H/L	Selects registers input
27	RESETB	H/L	Reset Signal
28	CS1B	H/L	Chip select input
	C(0	H/L	H:6800 MPU BUS L:8080 MPU BUS
29	C68	H/L	11.0000 MFU BUS L.0000 MFU BUS

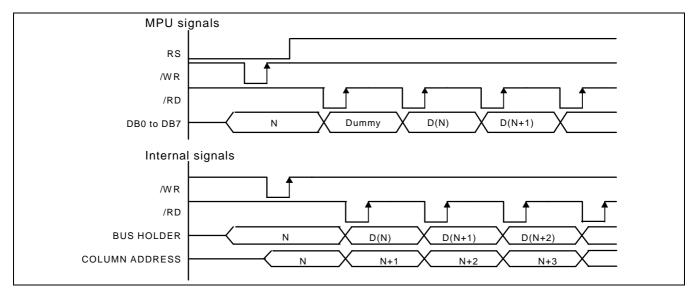
6.3 Interface Timing Chart



Serial Interface Timing



Write Timing



Read Timing

6.4 Instruction Code:

×: Don't care

Instruction	RS	RW	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	X: Don't care Description	
Read display data	1	1			l	Read	data			I	Read data from DDRAM	
Write display data	1	0				Write	data				Write data into DDRAM	
Read status	0	1	BUSY	ADC	ON	RES	0	0	0	0	Read the internal status	
Set page address	0	0	1	0	1	1	P3	P2	P1	P0	Set page address	
Set column address MSB	0	0	0	0	0	1	0	Y6	Y5	Y4	Set column address MSB	
Set column address LSB	0	0	0	0	0	0	Y3	Y2	Y1	Y0	Set column address LSB	
Set modify-read	0	0	1	1	1	0	0	0	0	0	Set modify-read mode	
Reset modify-read	0	0	1	1	1	0	1	1	1	0	Release modify-read mode	
Display ON / OFF	0	0	1	0	1	0	1	1	1	D	D = 0: display OFF D = 1: display ON	
Set initial display line	0	0	0	1	0	0	0	0	×	×	2-byte instruction to specify the	
register	0	0	×	S6	S5	S4	S3	S2	S1	S0	initial display line to realize vertical scrolling	
On the State OOM on what are	0	0	0	1	0	0	0	1	×	×	2-byte instruction to specify the	
Set initial COM0 register	0	0	×	C6	C5	C4	СЗ	C2	C1	C0	initial COM0 to realize window scrolling	
Set partial display	0	0	0	1	0	0	1	0	×	×	2-byte instruction to set partial	
duty ratio	0	0	×	D6	D5	D4	D3	D2	D1	D0	display duty ratio	
	0	0	0	1	0	0	1	1	×	×	2-byte instruction to set n-line	
Set n-line inversion	0	0	×	×	×	N4	N3	N2	N 1	N0	inversion register	
Release n-line inversion	0	0	1	1	1	0	0	1	0	0	Release n-line inversion mode	
Reverse display ON / OFF	0	0	1	0	1	0	0	1	1	REV	REV = 0: normal display REV = 1: reverse display	
Entire display ON / OFF	0	0	1	0	1	0	0	1	0	EON	EON = 0: normal display EON = 1: entire display ON	
Power control	0	0	0	0	1	0	1	VC	VR	VF	Control power circuit operation	
Select DC-DC step-up	0	0	0	1	1	0	0	1	DC1	DC0	Select the step-up of the internal voltage converter	
Select regulator resistor	0	0	0	0	1	0	0	R2	R1	R0	Select internal resistance ratio of the regulator resistor	
Set electronic volume	0	0	1	0	0	0	0	0	0	1	2-byte instruction to specify the	
register	0	0	×	×	EV5	EV4	EV3	EV2	EV1	EV0	electronic volume register	
Select LCD bias	0	0	0	1	0	1	0	B2	B1	B0	Select LCD bias	
SHL select	0	0	1	1	0	0	SHL	×	×	×	COM bi-directional selection SHL = 0: normal direction SHL = 1: reverse direction	
ADC select	0	0	1	0	1	0	0	0	0	ADC	SEG bi-directional selection ADC = 0: normal direction ADC = 1: reverse direction	
Set static indicator mode	0	0	1	0	1	0	1	1	0	SM	2-byte instruction to specify the	
Set static indicator register	0	0	×	×	×	×	×	×	S1	S0	static indicator mode	
Oscillator ON start	0	0	1	0	1	0	1	0	1	1	Start the built-in oscillator	
Set power save mode	0	0	1	0	1	0	1	0	0	Р	P = 0: standby mode P = 1: sleep mode	
Release power save mode	0	0	1	1	1	0	0	0	0	1	Release power save mode	
Reset	0	0	1	1	1	0	0	0	1	0	Initialize the internal functions	
NOP	0	0	1	1	1	0	0	0	1	1	No operation	
Test instruction	0	0	1	1	1	1	×	×	×	×	Don't use this instruction.	

7 Optical Characteristics

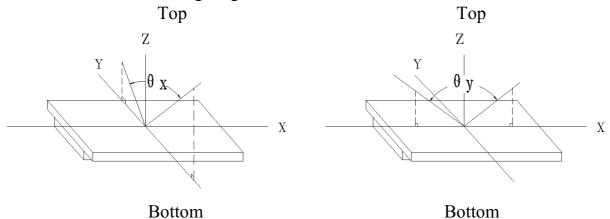
7.1 Optical Characteristics

Ta=25°	C

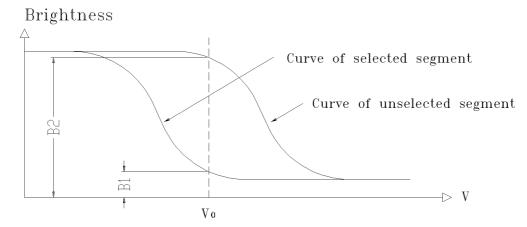
Item	Item Symbol		Condition		Min.	Тур.	Max.	Unit
Viewing Angle		$\theta_{\mathbf{X}}$	C >2	θ _y =0°	-30)	20	Dag
		$\theta_{\mathbf{y}}$	Cr≥2	$\theta_{x}=0^{\circ}$	-30		30	Deg
Contrast 1	Ratio	Cr	$\theta_{x}=0^{\circ}$ $\theta_{y}=0^{\circ}$		3.0	-	-	
Response	Turn on	Ton	$\theta_{\mathbf{x}}$	=0°	-	-	300	*****
Time	Turn off	Toff	$\theta_{\mathbf{y}^{\mathbf{z}}}$	=0°	-	-	300	ms

7.2 Definition of Optical Characteristics

7.2.1 Definition of Viewing Angle



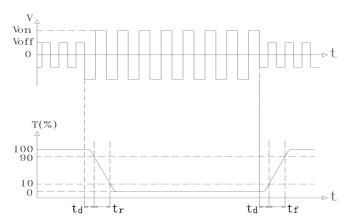
7.2.2 Definition of Contrast Ratio



Contrast Ratio = $B2/B1 = \frac{unselected state brightness}{selected state brightness}$

Measuring Conditions:

1) Ambient Temperature: 25° C; 2) Frame frequency: 85Hz 7.2.3 Definition of Response time



Turn on time: $t_{on} = t_d + t_r$

Turn off time: $t_{off} = t_d + t_f$

Measuring Condition:

1) Operating Voltage: 9.0V

2) Frame frequency: 85Hz

8 Reliability

8.1 Content of Reliability Test

Ta=25°C

No.	Test Item	Content of Test	Test condition
1	High Temperature	Endurance test applying the high	60℃
	Storage	storage temperature for a long time	96H
2	Low Temperature	Endurance test applying the low	-20°C
	Storage	storage temperature for a long time	96H
3	High Temperature Operation	Endurance test applying the electric stress (voltage & current) and the thermal stress to the element for a long time	50℃ 96H
4	Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time	0℃ 96H
5	High Temperature /Humidity Storage	Endurance test applying the high temperature and high humidity storage for a long time	40℃ 90%RH 96H
6	Temperature Cycle	Endurance test applying the low and high temperature cycle -20°C \(\rightarrow 25°C \(\rightarrow 60°C \(\rightarrow 25°C \) 30min 5min 30min 5min 1 cycle	-20°C/60°C 10 cycles
7	Vibration Test (package state)	Endurance test applying the vibration during transportation	10Hz~150Hz, 50m/s², 40min
8	Shock Test (package state)	Endurance test applying the shock during transportation	Half- sine wave, 100m/s ² , 11ms
9	Atmospheric Pressure Test	Endurance test applying the atmospheric pressure during transportation by air	40kPa 16H

8.2 Failure Judgment Criterion

Criterion	Test Item No.									Failura Judgamant Critarian	
Item	1	2	3	4	5	6	7	8	9	Failure Judgement Criterion	
Basic Specification	1	V	1	V	1	V	V	V		Out of the basic Specification	
Electrical specification	1	V	1	1	1					Out of the electrical specification	
Mechanical Specification							V	V		Out of the mechanical specification	
Optical Characteristic	1	V	1	1	1	1			√	Out of the optical specification	
Note	For test item refer to 8.1										
Remark	Basic specification = Optical specification + Mechanical specification										

9 QUALITY LEVEL

Examination	At T _a =25°C	Inspection					
or Test	(unless otherwise stated)	Min.	Max.	Unit	IL	AQL	
External Visual Inspection	Under normal illumination and eyesight condition, the distance between eyes and LCD is 25cm.	See App	pendix A	II	Major 1.0 Minor 2.5		
Display Defects	Under normal illumination and eyesight condition, display on inspection.	See Ap	pendix B		II	Major 1.0 Minor 2.5	

Note: Major defects: Open segment or common, Short, Serious damages, Leakage

Miner defects: Others

Sampling standard conforms to GB2828

10 Precautions for Use of LCD Modules

- 10.1 Handling Precautions
- 10.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 10.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.
- 10.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- 10.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- 10.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:
 - Isopropyl alcohol
 - Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents
- 10.1.6 Do not attempt to disassemble the LCD Module.
- 10.1.7 If the logic circuit power is off, do not apply the input signals.
- 10.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - a. Be sure to ground the body when handling the LCD Modules.
 - b. Tools required for assembly, such as soldering irons, must be properly ground.
 - c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
 - d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

- 10.2 Storage precautions
- 10.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.
- 10.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature: $0^{\circ}\text{C} \sim 40^{\circ}\text{C}$

Relatively humidity: $\leq 80\%$

- 10.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.
- 10.3 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

Appendix A

Inspection items and criteria for appearance defects

Items	Contents	Criteria					
Leakage		Not permitted					
Rainbow		According to the limit specimen					
	Wrong polarizer attachment	Not permitted					
Polarizer	Bubble between	Not counted		Max. 3 defects al	llowed		
	polarizer and glass	φ<0.3mm		0.3mm≤¢≤0.5r	nm		
	Scratches of polarizer	According to	the lin	nit specimen			
Black spot		Not counted	Max	. 3 spots allowed			
(in viewing area)		X<0.2mm	0.2mm≤X≤0.5mm		Max. 3		
	α	X=(a+b)/2	spots (lines)				
Black line (in viewing		Not counted	Max	. 3 lines allowed	allowed		
area)	b	a<0.02mm	0.021	mm≤a≤0.05mm b≤2.0mm			
Progressive cracks		Not permitted					

Appendix A

Inspection item and criteria for appearance defects (continued)

Items	Contents		Criteria				
	Cracks on pads	a	b		С	Max. 2	
		≤3mm	$\leq W$	V/5	≪T/2	cracks	
	b/-	≤2mm	≤W	V/5	T/2 <c<t< td=""><td>allowed</td><td></td></c<t<>	allowed	
	Cracks on contact side	a			b		
		≤3m	m		≤T/2		
		≤2m	≤2mm		Γ/2 <b<t< td=""><td rowspan="2">Max. 2 cracks</td><td rowspan="3">Max. 5 cracks allowed</td></b<t<>	Max. 2 cracks	Max. 5 cracks allowed
Glass		C shall b	e not	reac			
Cracks	Cracks on non-contact side	a		ь		allowed	
		≤3m	m	≤T/2			
		≤2mm		T/2 <b<t< td=""><td></td><td></td></b<t<>			
	- SW -	C≤0.5m	nm				
	٥"	d≤SW/3	3				
	Corner cracks	e<2.0mn	n^2			Max. 3	
	f-r	f<2.0mm	12		cracks allowed		

Appendix BInspection items and criteria for display defects

Items		Contents	Critera		
Open segment or open common			Not permitted		
Short			Not permitted		
Wrong viewing angle			Not permitted		
Contrast radio uneven			According to the limit specimen		
Crosstalk			According to the limit specimen		
Pin holes and cracks in segment (DOT)	0	+ + a	Not counted	Max.3 dots allowed	
			X<0.1mm	0.1mm≤X≤0.2mm	
			X=(a+b)/2		Max.3 dots allowed
	- - - -	Not counted	Max.2 dots allowed		
	· ·		A<0.1mm	0.1mm≤A≤0.2mm D<0.25mm	
			Not counted	May 2 anota allowed	
Black spot (in viewing area)			Not counted X<0.1mm	Max.3 spots allowed $0.1 \text{mm} \leq X \leq 0.2 \text{mm}$	-
			X = (a+b)/2		Max.3 spots
Black line (in viewing area)		Not counted	Max.3 lines allowed	(lines)	
			a<0.02mm	0.02mm≤a≤0.05mm b≤0.5mm	

Appendix B
Inspection items and criteria for display defects (continued)

Items	Content	Critera				
Transfor- mation of segment	- - 0 - 0	Not counted	Max. 2 defects allowed			
	1	x<0.1mm	0.1mm≤x≤0.2mm			
		x=(a+b)/2				
				Max.3 defects		
	D-11-a	Not counted	Max. 1 defects allowed	allowed		
		a<0.1mm	0.1mm≤a≤0.2mm D>0			
		Max.2 defects allowed 0.8W≤a≤1.2W a=measured value of width W=nominal value of width				