Errata: CS98100 Rev. B

(Reference CS98100 Data Sheet revision DS552PP3 dated Feb '02)

The following errata have been identified on our tester, but have not caused any field problems.

1) SDRAM interface A.C. timing parameters:

tmdow - DR_D output delay from DR_CKO

datasheet limit: 9 ns (min)

relax to: 10 ns (min)

2) Digital Audio Interface A.C. timing parameters:

taxcl - AUD_XCLK low time

datasheet limit: 40% (min)

relax to: 47% (min)

If there are any questions concerning this information,
Please contact: http://www.crystal.com/support/engineering/index.cfm