



**PRELIMINARY**

**Y/C MIX**

■ **FEATURE**

1. Y/C mix circuit
2. Output and Chrominance muting function
3. Output protection function
4. SAG correction function
5. A load sufficient for driving two circuits
6. Low power consumption, 60mw typical
7. Internal voltage clamp circuit

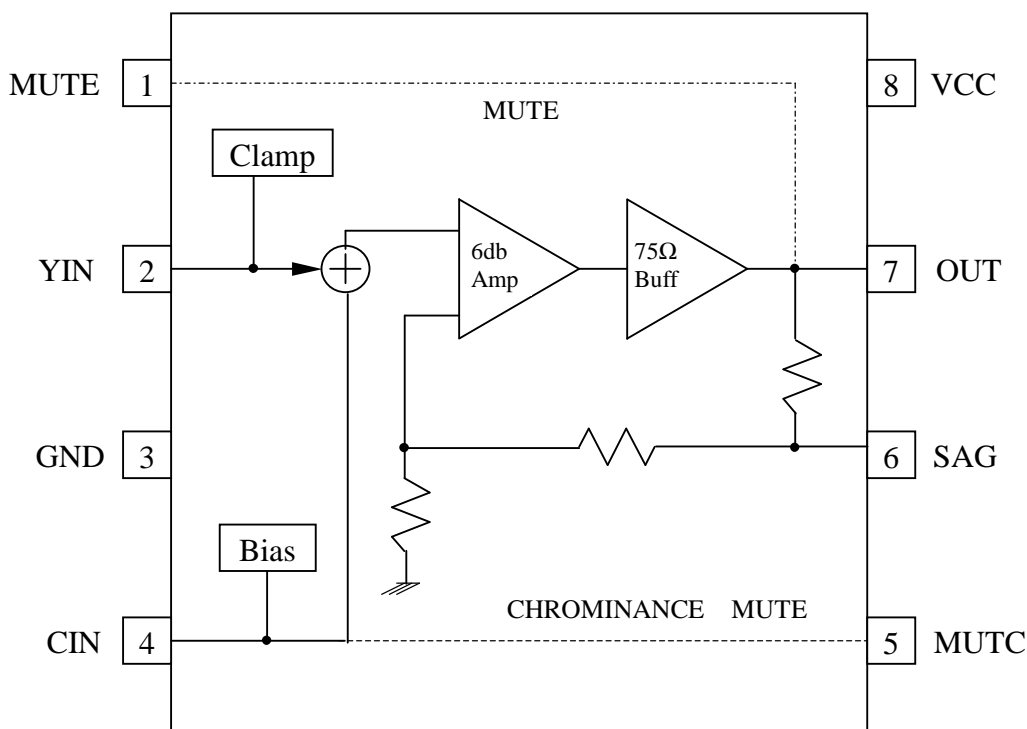
■ **APPLICATION**

- VCR
- Video Camera
- TV
- Video Player

■ **DESCRIPTION**

AA8600 is a Y/C mix with 6db gain, 75Ω output buffer and SAG correction. The 75Ω buffer is capable of driving two circuits. The SAG could reduce the capacitance of output coupling capacitor. The Y signal is clamped input, while the C signal is biased input. The power saving circuit provides the output and chrominance muting function. The IC is available in 8-pin TSSOP.

■ **BLOCK DIAGRAM & PIN ASSIGNMENT**





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■ **PIN DESCRIPTION**

Symbol	Pin-No	Type	Function
MUTE	1	I	Output is muted as this pin is set to high
YIN	2	I	Composite Y input signal is clamped input
GND	3		Ground
CIN	4	I	Chrominance C input signal is bias input
MUTC	5	I	Only chrominance output signal is muted as this pin is set to high
SAG	6	O	SAG correction to reduce the capacitance of output coupling capacitor
OUT	7	O	Y/C mix output. As it is less than 0.6V, the protect circuit is triggered to enable the power saving function
VCC	8		Power supply

■ **ABSOLUTE MAXIMUM RATING**

Ta = 25 °C

Parameter	Symbol	Rating			UNIT	CONDITION
		MIN	TYP	MAX		
Supply Voltage	Vcc	-0.3		8	V	
Operating Ambient Temperature	Ta	-25		75	°C	
Storage Temperature	Ts	-55		125	°C	

NOTE : Stress above those listed under “Absolute Maximum Rating” may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational section of this specification is not implied. Exposure to absolute maximum rating conditions for the extended periods of time may affect device reliability.



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■ **ELECTRICAL CHARACTERISTICS**

Ta = 25°C , Vcc = 5V

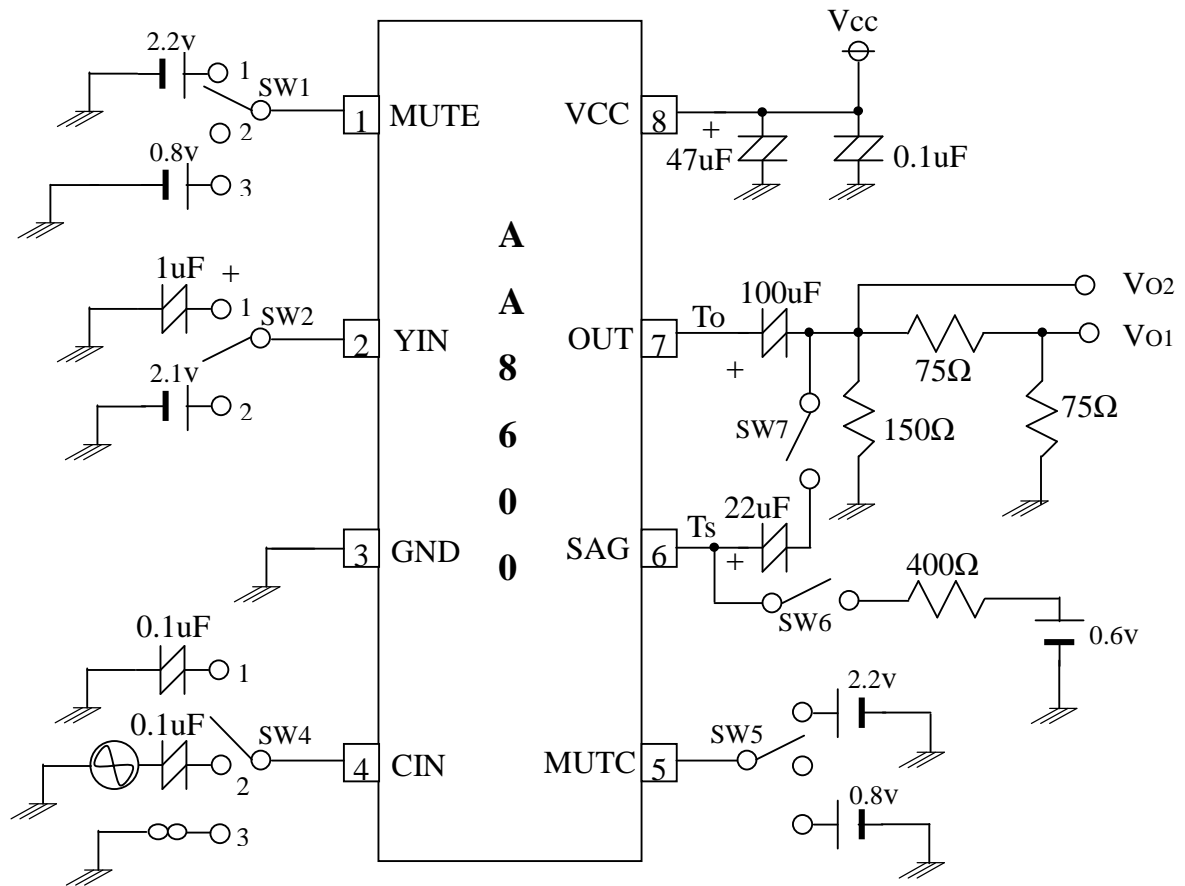
Parameter	Symbol	Min	Typ	Max	Unit	Condition
Supply Current	Icc		12.6	20	mA	No input Signal
Voltage Gain	Gv		-0.3		dB	Input Signal Freq = 4.43MHz, 1VP-P, measure VO1
Frequency Characteristic	GF	-1.5	-0.5	0.5	dB	Input Signal Freq = 7MHz/1MHz, 1VP-P, measure VO1
Muting Attenuation	MT		-64		dB	Input Signal Freq = 4.43MHz, 1VP-P, measure VO1
Muting High Level	VMH	2.2		Vcc	V	
Muting Low Level	VML	GND		0.8	V	
Input Impedance	ZIN	15	20	25	KΩ	CIN, 50uA
Muted Current	IM		1	2.5	mA	MUTE = 'H'
SAG-Terminal Gain	GSAG		48		dB	
Differential Gain	DG		1	2	%	VIN = 1VP-P, refer staircase signal
Differential Phase	DP		1	2.5	deg	VIN = 1VP-P, refer staircase signal

■ **MUTE MODE**

Function	MUTE	MUTC
Output Muting	H	NA
Chrominance Muting	L	H



■ **MEASUREMENT CIRCUIT**





# Agamem Electronics Inc. AA8600

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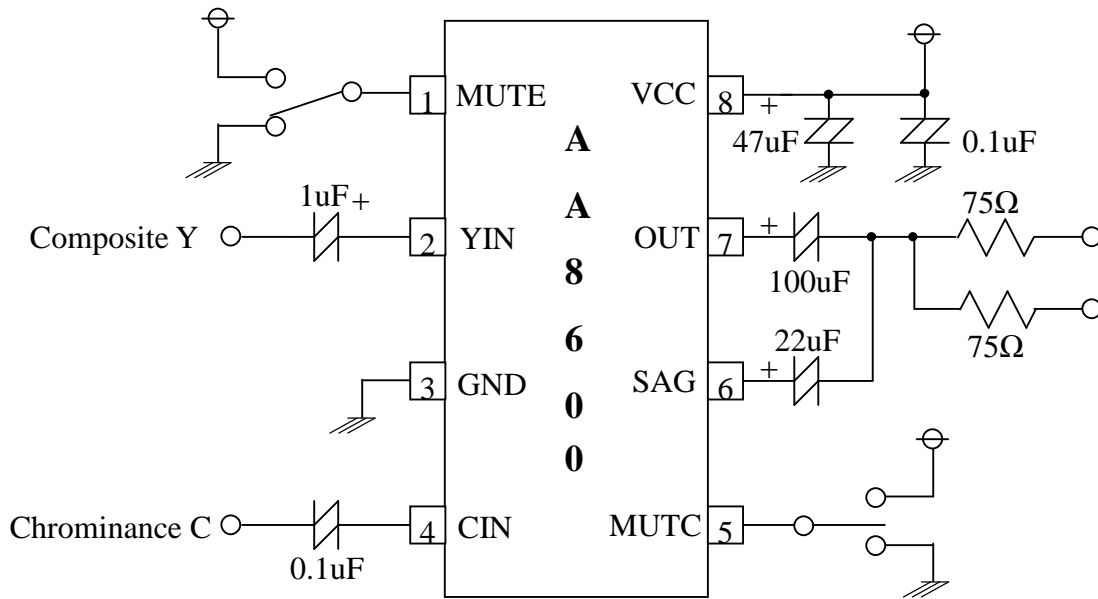
■ **MEASUREMENT CONDITIONS**

Parameter	Symbol	SW Condition						Measurement Method
		1	2	4	5	6	7	
Supply Current	I <sub>cc</sub>	2	1	1	2	0	C	No signal input
Voltage Gain	G <sub>v</sub>	3	2	2	3	0	C	Input a 4.43MHz, 1Vp-p sine wave from CIN and then measure the output level from VO1
Frequency Characteristic	G <sub>F</sub>	3	1	2	3	0	C	G <sub>V1M</sub> : voltage gain at input frequency=1MHz G <sub>V7M</sub> : voltage gain at input frequency=7MHz G <sub>F</sub> =G <sub>V7M</sub> -G <sub>V1M</sub>
Output Mute	M <sub>T</sub>	1	1	2	3	0	C	Input a 4.43MHz, 1Vp-p sine wave and then measure the output level from VO1
Chrominance Mute	M <sub>C</sub>	3	2	2	1	0	C	Same as output muting
Input Impedance	Z <sub>IN</sub>	3	1	3	3	0	C	Measure the input voltage V <sub>IS</sub> & the open voltage V <sub>IO</sub> when a 50uA is introduced. The Z <sub>IN</sub> =  V <sub>IS</sub> -V <sub>IO</sub>   / 50uA
Muted Current	I <sub>M</sub>	1	1	1	2	0	C	Measure the current of power supply when mute is set at high
AG Terminal Gain	G <sub>SAG</sub>	3	2	2	3	0	C	Measure the value of T <sub>S</sub> and T <sub>O</sub> at the first as V <sub>S1</sub> & V <sub>O1</sub> and the value at second condition as V <sub>S2</sub> & V <sub>O2</sub> , respectively. G <sub>SAG</sub> = 20log{(V <sub>S2</sub> - V <sub>S1</sub> ) / (V <sub>O2</sub> - V <sub>O1</sub> ) }
		3	2	2	3	C	O	



■ **APPLICATION**

➤ **With SAG**



➤ **Without SAG**

