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Product Information

LEAD FREE

Product Information

SAMSUNG TFT-LCD

MODEL NO. : LTN121XJ-L06

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LCD Product Planning Group 1, Marketing Team

Samsung Electronics Co . , LTD.

wise view™ SAMSUNG TFT-LCD

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GENERAL DESCRIPTION

DESCRIPTION

LTN121XJ-L06 is a color active matrix TFT (Thin Film Transistor) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching devices. This model is composed of a TFT LCD panel, a driver circuit and a backlight system. The resolution of a 12.1" contains 1024 x 768 pixels and can display up to 262,144 colors. 6 O'clock direction is the Optimum viewing angle.

FEATURES

- Ultra Thin and light weight
- High contrast ratio
- XGA (1024x768 pixels) resolution
- Low power consumption
- DE (Data enable) only mode.
- 3.3V LVDS Interface
- On board EDID chip
- PB-Free Product (RoHS compliant)

APPLICATIONS

- Notebook PC
- If the usage of this product is not for PC application but for others, please contact SEC

GENERAL INFORMATION

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Item	Specification	Unit	Note
Display area	245.76(H) X 184.32(V) (12.1”diagonal)	mm	
Driver element	a-si TFT active matrix		
Display colors	262,144		
Number of pixel	1024 x 768 (XGA)	pixel	
Pixel arrangement	RGB vertical stripe		
Pixel pitch	0.240(H) x 0.240(V)	mm	
Display Mode	Normally white		
Surface treatment	Haze 25, Hard-Coating 3H		

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MECHANICAL INFORMATION

Item		Min.	Typ.	Max.	Unit	Note
Module size	Horizontal (H)	260.5	261.0	261.5	mm	
	Vertical (V)	197.5	198.0	198.5	mm	
	Depth (D)	-	4.7	5.0	mm	(1)
Weight		-	290	300	g	

Note (1) Measurement condition of outline dimension
. Equipment : Vernier Calipers
. Push Force : 500g f (minimum)

1. ELECTRICAL ABSOLUTE RATINGS

(1) TFT LCD MODULE

V_{DD} =3.3V, V_{SS} = GND = 0V

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	V_{DD}	V_{SS} - 0.3	V_{DD} + 0.3	V	(1)
Logic Input Voltage	V_{IN}	V_{SS} - 0.3	V_{DD} + 0.3	V	(1)

Note (1) Within Ta (25 ± 2 °C)

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(2) BACK-LIGHT UNIT

Ta = 25 ± 2 °C

Item	Symbol	Min.	Max.	Unit	Note
Lamp Current	I_L	3.0	7.0	mArms	(1)
Lamp frequency	F_L	50	80	kHz	(1)

Note 1) Permanent damage to the device may occur if maximum values are exceeded
Functional operation should be restricted to the conditions described under normal operating conditions.

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2. OPTICAL CHARACTERISTICS

The following items are measured under stable conditions. The optical characteristics should be measured in a dark room or equivalent state.
Measuring equipment : TOPCON BM-5A and PR-650

* Ta = 25 ± 2 °C, V_{DD}=3.3V, fv= 60Hz, f_{DCLK} = 65MHz, I_L = 6.0 mA

Item		Symbol	Condition	Min.	Typ.	Max	Unit
Contrast Ratio (5 Points)		CR		250	300	-	-
Response Time at Ta	Rising	T_R		-	10	20	msec
	Falling	T_T	Normal Viewing Angle $\phi = 0$ $\theta = 0$	-	30	50	
Average Luminance of White (5 Points)		$Y_{L,AVE}$		170	200	-	cd/m^2
Color Chromaticity (CIE)	Red	R_X		0.565	0.596	0.625	-
		R_Y		0.310	0.340	0.370	
	Green	G_X		0.285	0.315	0.345	
		G_Y		0.505	0.535	0.565	
	Blue	B_X		0.125	0.155	0.185	
		B_Y		0.110	0.140	0.170	
	White	W_X		0.283	0.313	0.343	
		W_Y		0.299	0.329	0.359	
Viewing Angle	Hor.	θ_L	CR ≥ 10	40	45		Degrees
		θ_H		40	45		
	Ver.	ϕ_H		10	15		
		ϕ_L		25	30		
13 Points White Variation		δ_L		-	-	2.2	-

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3. ELECTRICAL CHARACTERISTICS

Product Information

3.1 TFT LCD MODULE

Ta= 25 ± 2°C

Item		Symbol	Min.	Typ.	Max.	Unit	Note
Voltage of Power Supply		V_{DD}	3.0	3.3	3.6	V	
Differential Input Voltage for LVDS Receiver Threshold	High	V_{IH}	-	-	+100	mV	V_{CM} = +1.2V
	Low	V_{IL}	-100	-	-	mV	
Vsync Frequency		fv	-	60	-	Hz	
Hsync Frequency		f_H	-	48.2	-	KHz	
Main Frequency		f_{DCLK}	-	65	-	MHz	
Rush Current		I_{RUSH}	-	-	1.5	A	
Current of Power Supply	White	I_{DD}	-	270	-	mA	
	Mosaic		-	300	-	mA	
	V. Stripe		-	350	400	mA	

3.2 BACK-LIGHT UNIT

The backlight system is an edge-lighting type with a single CCFT (Cold Cathode Fluorescent Tube).
The characteristics of a single lamp are shown in the following table.

- INVERTER : SEM SIC 130T

Ta= 25 ± 2 °C

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Item	Symbol	Min.	Typ.	Max.	Unit	Note
Lamp Current	I_L	3.0	6.0	6.5	mArms	
Lamp Voltage	V_L		565	-	Vrms	I_L=6.0mA
Frequency	f_L	50	60	65	KHz	
Power Consumption	P_L		3.4		W	I_L=6.0mA
Operating Life Time	Hr	10,000			Hour	
Startup Voltage	V_S	-	-	1080	Vrms	25°C
				1300	Vrms	0°C
Lamp startup time		-	-	1.0	sec	

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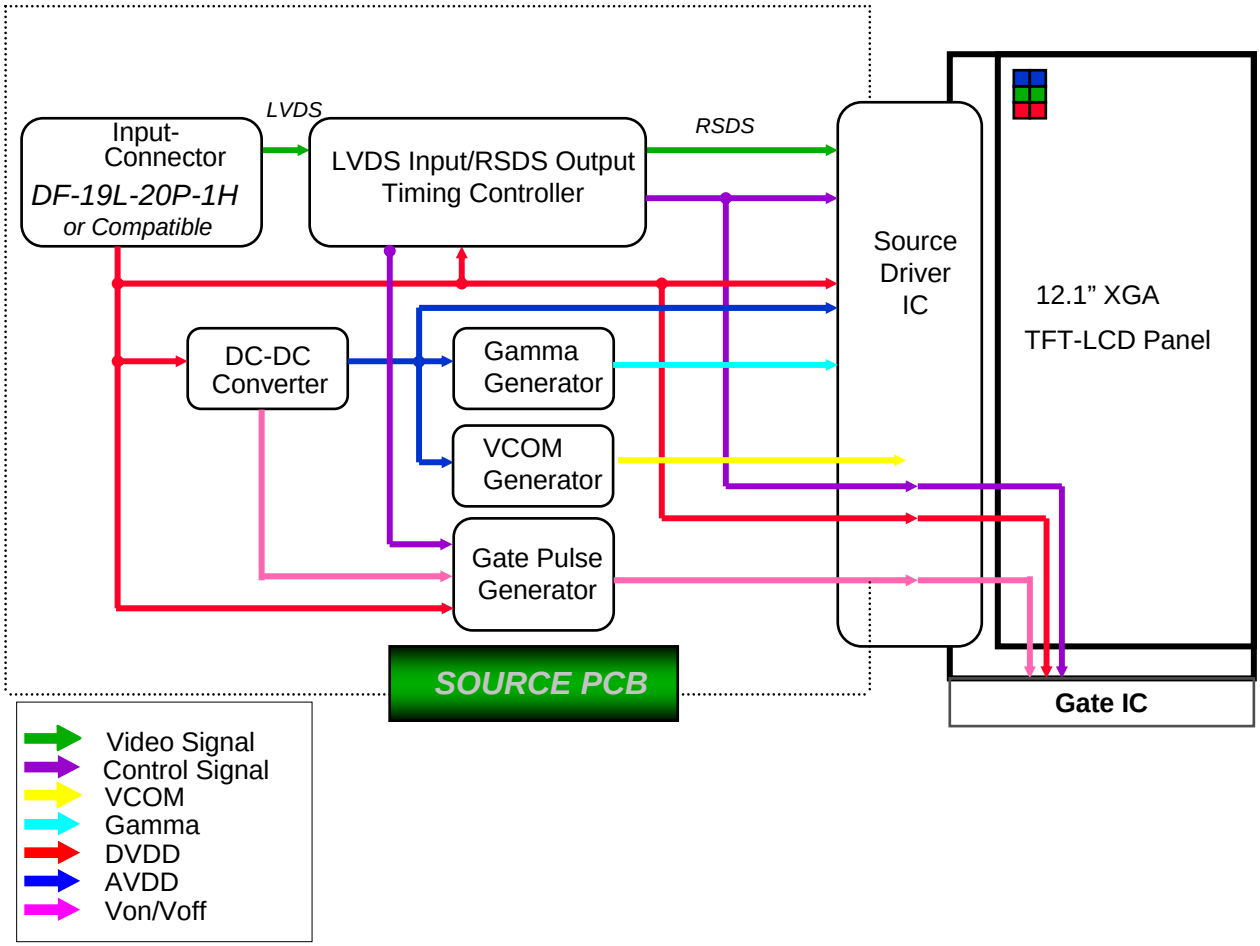
4. BLOCK DIAGRAM

4.1 TFT LCD Module



4.2 BACK-LIGHT UNIT

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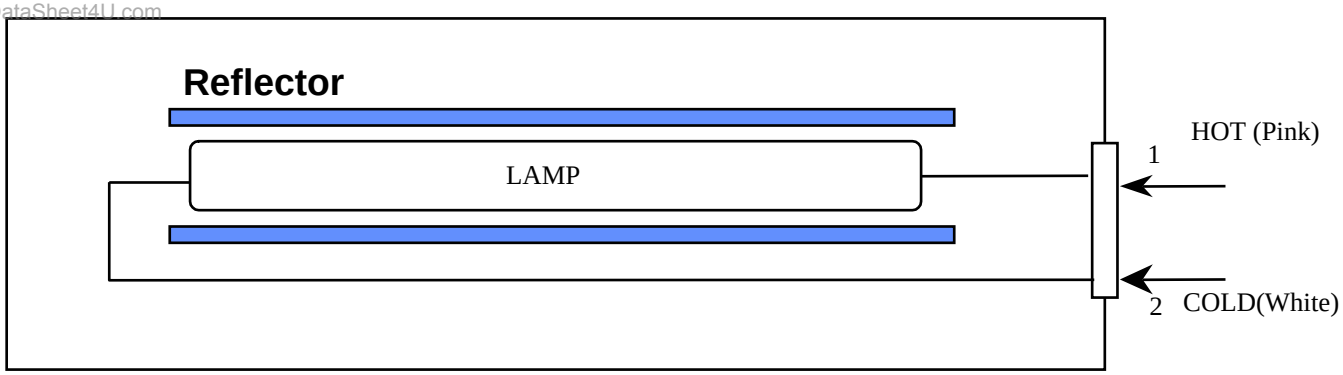

Note) The output of the inverter may change according to the material of the reflector.

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5. INPUT TERMINAL PIN ASSIGNMENT

5.1. Input Signal & Power LVDS, Connector : (Hirose, DF-19L-20P-1H or Compatible)

PIN NO	SYMBOL	FUNCTION	POLARITY	REMARK
1	VSS	Ground		
2	VDD	POWER SUPPLY +3.3V		
3	VDD	POWER SUPPLY +3.3V		
4	VEDID	DCC 3.3V Power		
5	NC	No connection		
6	CLOCKEDID	DDC Clock		
7	DATAEDID	DDC Data		
8	RxIN0-	LVDS Differential Data INPUT(R0-R5, G0)	Negative	
9	RxIN0+	LVDS Differential Data INPUT(R0-R5, G0)	Positive	
10	VSS	Ground		
11	RxIN1-	LVDS Differential Data INPUT(G1-G5, B0-B1)	Negative	
12	RxIN1+	LVDS Differential Data INPUT(G1-G5, B0-B1)	Positive	
13	VSS	Ground		
14	RxIN2-	LVDS Differential Data INPUT(B2-B5, Sync, DE)	Negative	
15	RxIN3+	LVDS Differential Data INPUT(B2-B5, Sync, DE)	Positive	
16	VSS	Ground		
17	RxCLK-	LVDS Differential Clock INPUT(Clock)	Negative	
18	RxCLK+	LVDS Differential Clock INPUT(Clock)	Positive	
19	VSS	Ground		
20	VSS	Ground		

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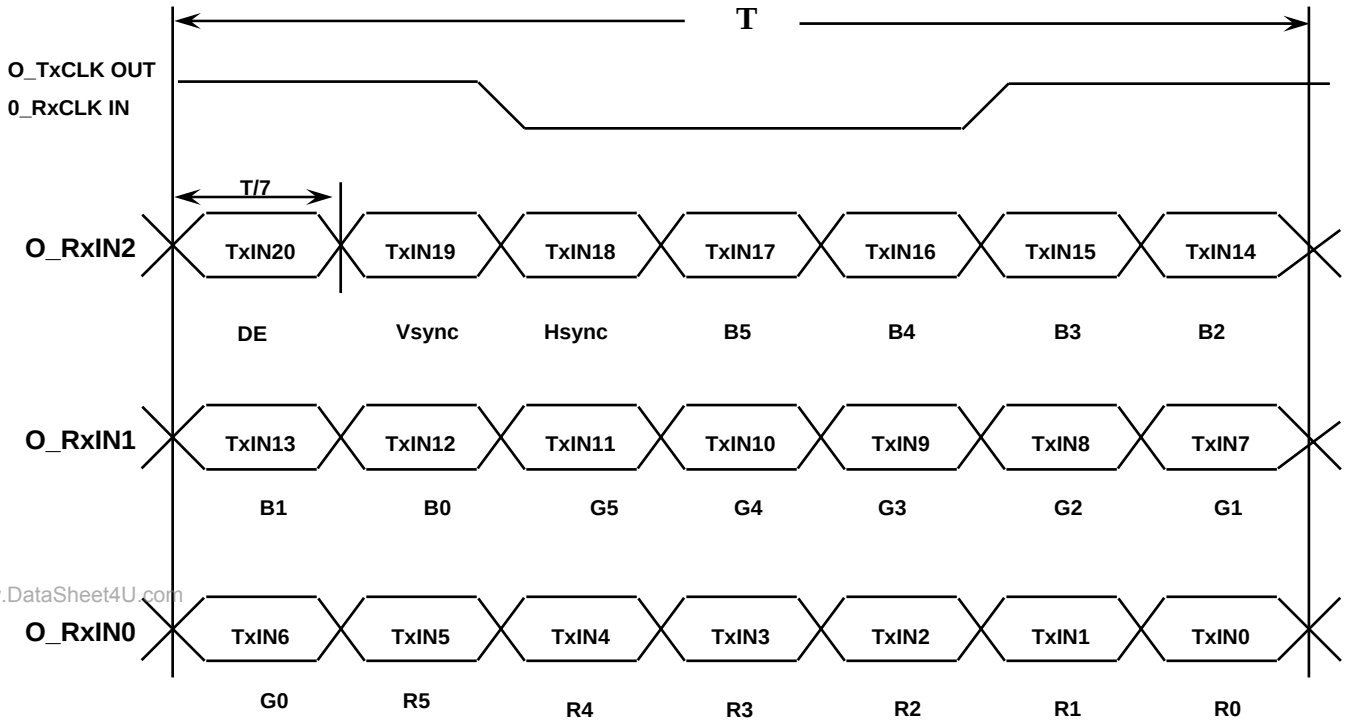
5.2 BACK LIGHT UNIT

Connector : JST BHSR - 02VS -1
Mating Connector : SM02B-BHSS-1(JST)

Pin NO.	Symbol	Color	Function
1	HOT	Pink	High Voltage
2	COLD	White	Low Voltage

5.3 Timing Diagrams of LVDS For Transmission

LVDS Receiver : Integrated T-CON



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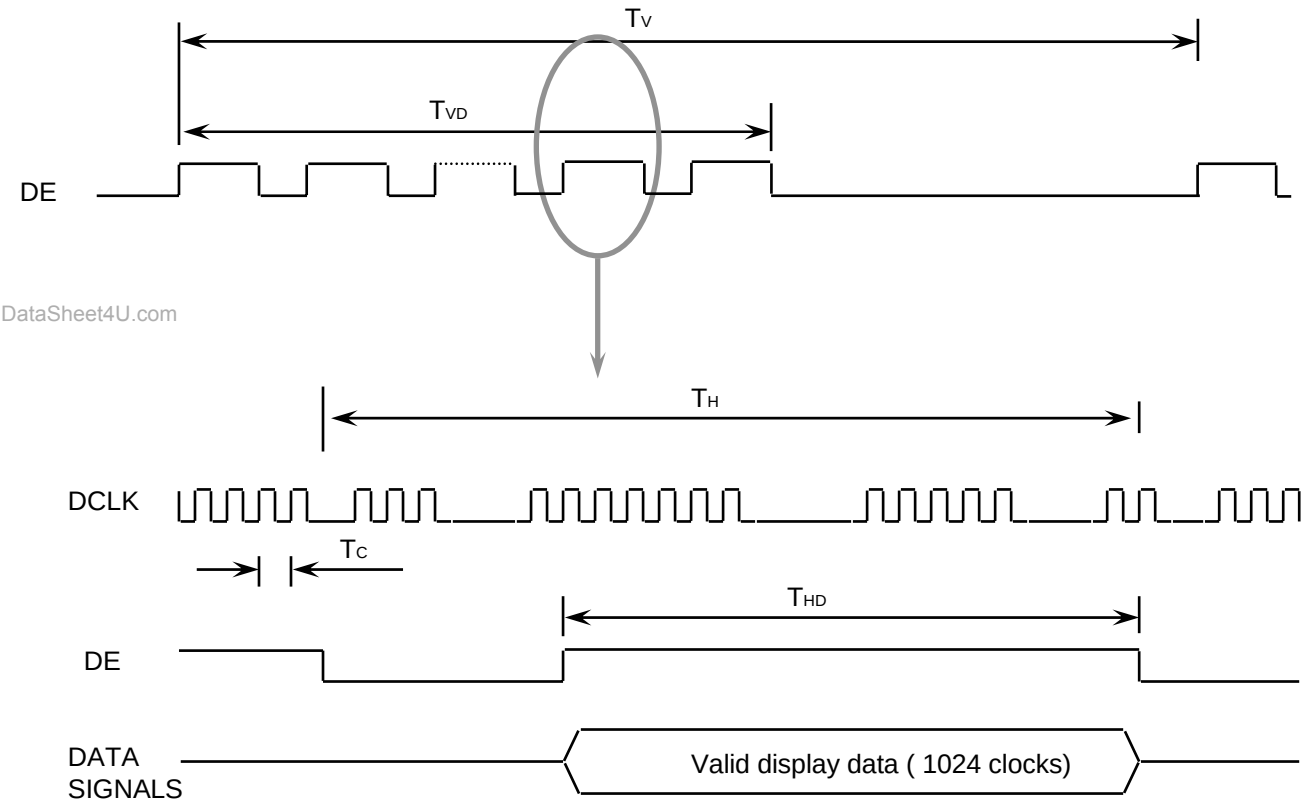
6. INTERFACE TIMING

6.1 Timing Parameters

Signal	Item	Symbol	Min.	Typ.	Max.	Unit	Note
Frame Frequency	Cycle	T_V	-	806	-	Lines	-
Vertical Active Display Term	Display Period	T_{VD}	-	768	-	Lines	-
One Line Scanning Time	Cycle	T_H	-	1344	-	Clocks	-
Horizontal Active Display Term	Display Period	T_{HD}	-	1024	-	Clocks	-

6.2 Timing diagrams of interface signal



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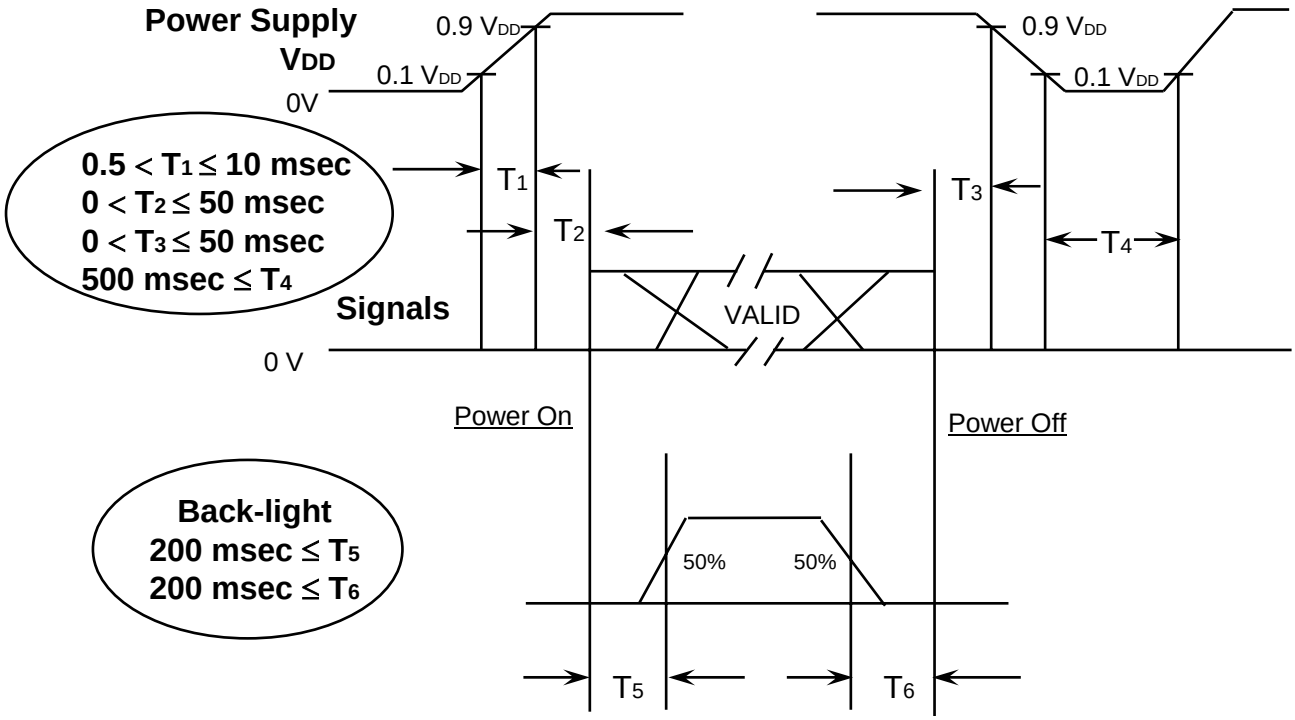
6.3 Power ON/OFF Sequence

: To prevent a latch-up or DC operation of the LCD module, the power on/off sequence should be as the diagram below.



Power ON/OFF Sequence

T1 : Vdd rising time from 10% to 90%
T2 : The time from Vdd to valid data at power ON.
T3 : The time from valid data off to Vdd off at power Off.
T4 : Vdd off time for Windows restart
T5 : The time from valid data to B/L enable at power ON.
T6 : The time from valid data off to B/L disable at power Off.

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NOTE.

(1) The supply voltage of the external system for the module input should be the same as the definition of V_{DD}.

(2) Apply the lamp voltage within the LCD operation range. When the back-light turns on before the LCD operation or the LCD turns off before the back-light turns off, the display may momentarily become white.

(3) In case of V_{DD} = off level, please keep the level of input signals on the low or keep a high impedance.

(4) T4 should be measured after the module has been fully discharged between power off and on period.

(5) Interface signal shall not be kept at high impedance when the power is on.

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7. MECHANICAL OUTLINE DIMENSION

[Refer to the next page]

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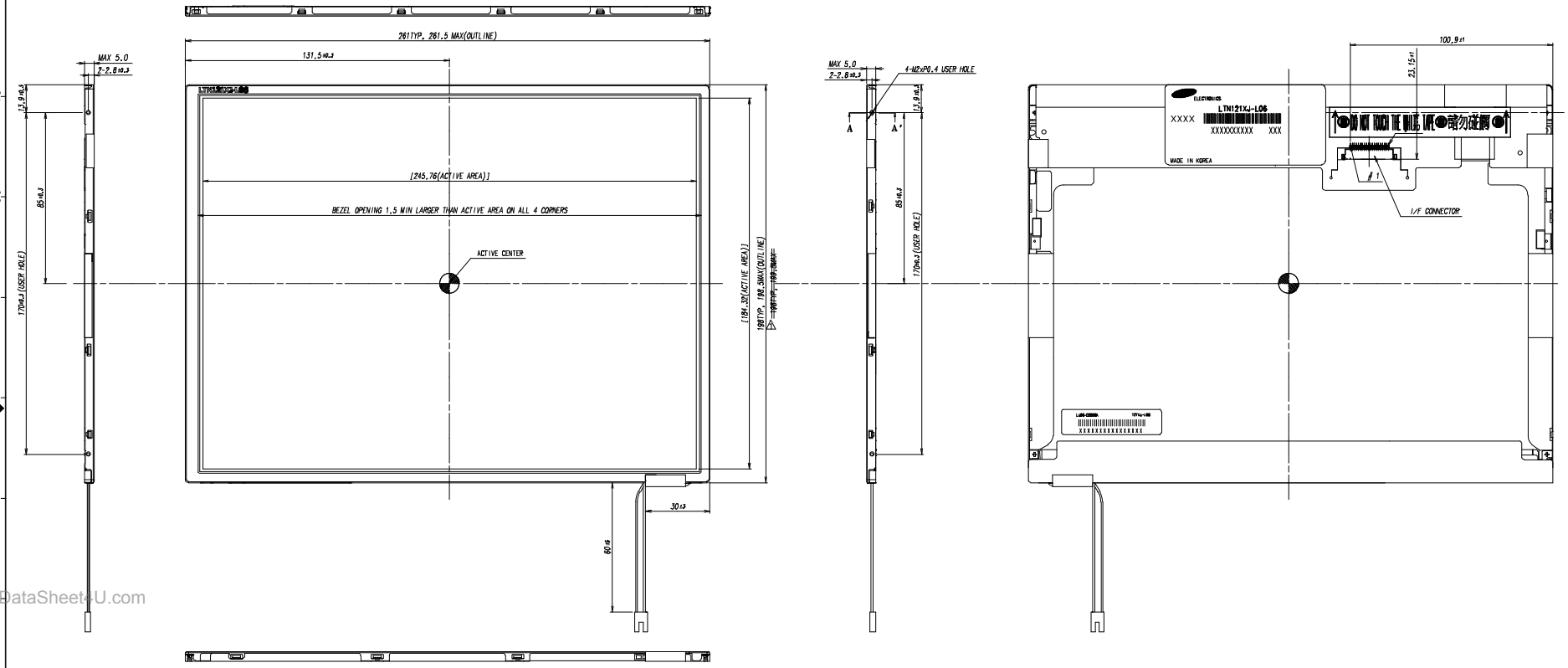
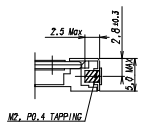
NO	PART NAME	CODE NO	SPECIFICATION	Q'TY	FINISH	MATERIAL	W. WIDTH	REMARK





SECTION A-A'(S=3/1)

* NOTE

1. INPUT CONN. SPEC
   - PART NO / MAKER : DF19L-20P-1H / HIROSE (or compatible)
2. MATHCING CONN. SPEC
   - PART NO / MAKER : DF19S-20S-1C / HIROSE (or compatible)
3. CCFT LAMP CONN. SPEC.
   - MAKER : JST
   - PART NO / MAKER : BHSR-02VS-01 / JST (or compatible)
4. CALIFERS MEASURING FORCE : 530±150gf-cm
5. USER HOLE TORQUE SPEC : 3.0kgf-cm MAX
6. WEIGHT SPEC : 300g MAX

GENERAL TOLERANCE			
STEP	LEVEL 1	LEVEL 2	LEVEL 3
0 < X ≤ 4	±0.05	±0.1	±0.2
4 < X ≤ 16	±0.08	±0.15	±0.3
16 < X ≤ 64	±0.12	±0.25	±0.5
64 < X ≤ 256	±0.25	±0.4	±0.8

REV	DATE	DESCRIPTION OF REVISION	REASON	CHG'D BY
001	05.12.29	⚠CHANGING OUTLINE SIZE DIMENSION SPEC	FOR SPEC REALIZATION	Y.S.KIMAK

UNIT	mm	DRA'N BY	DES'D BY	CHK'D BY	APP'D BY
SCALE	1/1	Y.S.KIMAK	Y.S.KIMAK	Y.J.LEE	D.C.YANG
TOLERANCE LEVEL 3		'05.07.12	'05.07.12	'05.07.12	'05.07.12

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MODEL NAME	LTN121XJ-L06
PART/SHEET NAME	OUTLINE DIMENSION
SHEET	1/1
SPEC. NO	DDS01045-001
CODE NO.	
VER.	001