

RJL5015DPK

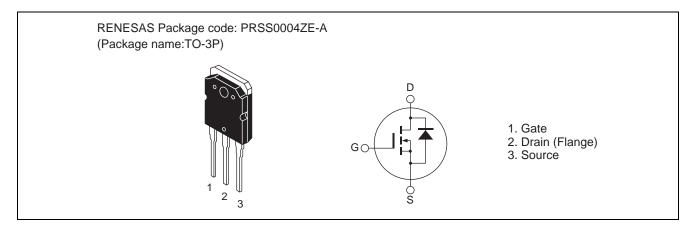
Silicon N Channel MOS FET High Speed Power Switching

REJ03G1912-0100 Rev.1.00 May 27, 2010

Features

- Built-in fast recovery diode
- Low on-resistance $R_{DS(on)} = 0.23~\Omega~typ.~(at~I_D=11~A,~V_{GS}=10~V,~Ta=25^{\circ}C)$
- Low leakage current
- High speed switching

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	500	V
Gate to source voltage	V _{GSS}	±30	V
Drain current	I _D	22	А
Drain peak current	I _{D (pulse)} Note1	66	А
Body-drain diode reverse drain current	I _{DR}	22	А
Body-drain diode reverse drain peak current	I _{DR} (pulse)	66	А
Avalanche current	I _{AP} Note3	7	А
Avalanche energy	E _{AR} Note3	2.7	mJ
Channel dissipation	Pch Note2	150	W
Channel to case thermal impedance	θch-c	0.833	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

- 2. Value at Tc = 25°C
- 3. STch = 25° C, Tch $\leq 150^{\circ}$ C

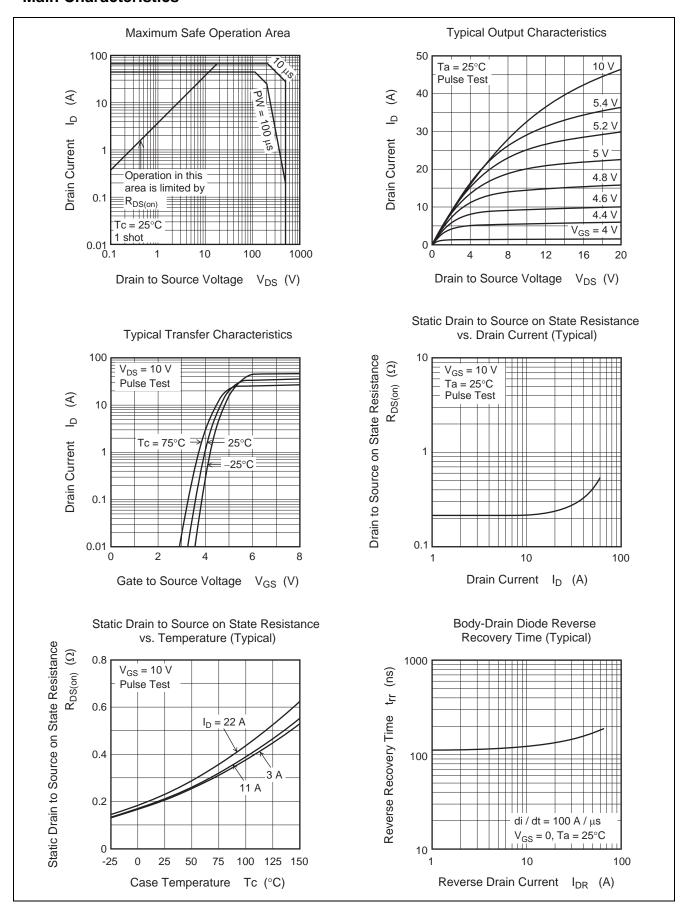
Electrical Characteristics

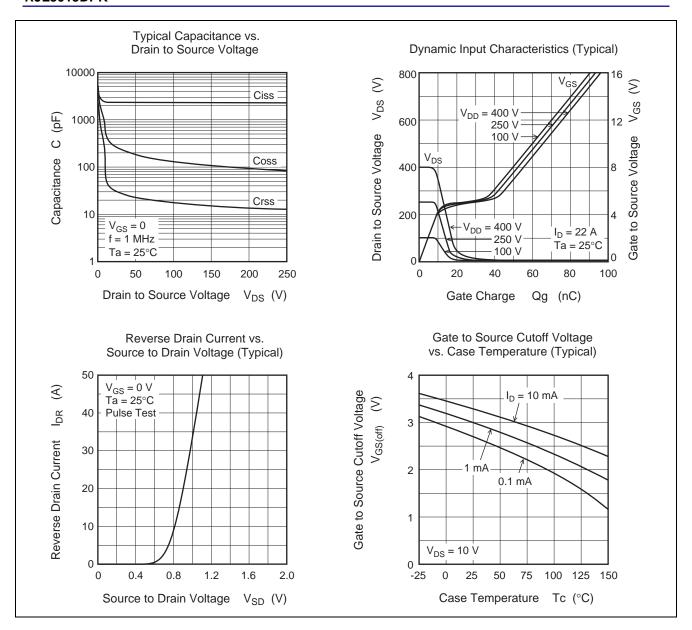
 $(Ta = 25^{\circ}C)$

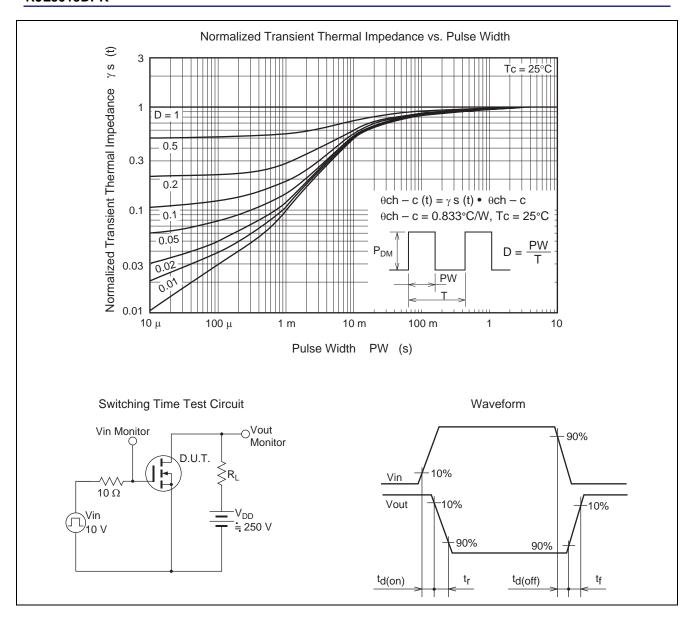
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	500	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	10	μΑ	$V_{DS} = 500 \text{ V}, V_{GS} = 0$
Gate to source leak current	I _{GSS}	_	_	±0.1	μΑ	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.5	_	4.0	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state resistance	R _{DS(on)}	l	0.23	0.27	Ω	$I_D = 11 \text{ A}, V_{GS} = 10 \text{ V}^{Note4}$
Input capacitance	Ciss	_	2400	_	pF	V _{DS} = 25 V
Output capacitance	Coss	_	264	_	pF	V _{GS} = 0 f = 1 MHz
Reverse transfer capacitance	Crss	_	32	_	pF	
Turn-on delay time	t _{d(on)}	_	36	_	ns	I _D = 11 A
Rise time	t _r	_	28	_	ns	$V_{GS} = 10 \text{ V}$ $R_{L} = 22.7 \Omega$ $Rg = 10 \Omega$
Turn-off delay time	t _{d(off)}	_	108	_	ns	
Fall time	t _f	_	50	_	ns	
Total gate charge	Qg	_	66	_	nC	V _{DD} = 400 V
Gate to source charge	Qgs	_	12	_	nC	V _{GS} = 10 V I _D = 22 A
Gate to drain charge	Qgd	_	29	_	nC	
Body-drain diode forward voltage	V_{DF}	_	0.91	1.55	V	$I_F = 22 \text{ A}, V_{GS} = 0^{\text{Note4}}$
Body-drain diode reverse recovery time	t _{rr}	_	140	_	ns	$I_F = 22 \text{ A}, V_{GS} = 0$ $di_F/dt = 100 \text{ A}/\mu\text{s}$

Notes: 4. Pulse test

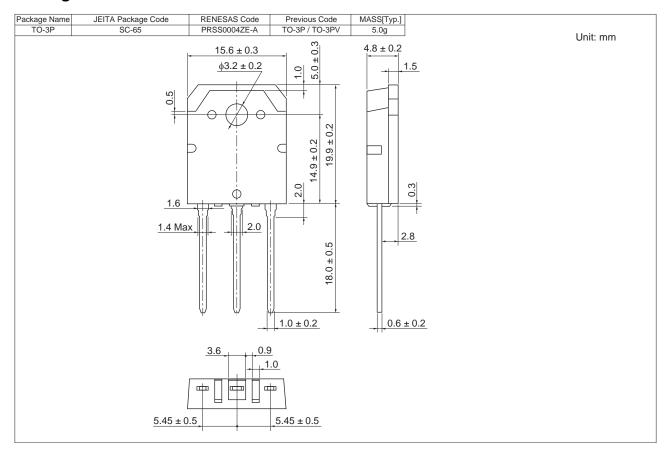
Main Characteristics







Package Dimensions



Ordering Information

Part No.	Quantity	Shipping Container
RJL5015DPK-00-T0	360 pcs	Box (Tube)

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