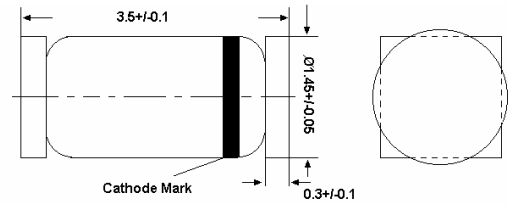


LS4448

SILICON EPITAXIAL PLANAR DIODE

fast switching diode in QuadroMELF case especially suited for automatic surface mounting.
Identical electrically to standard JEDEC 1N4448.

LS-34



QuadroMELF
Dimensions in mm

Absolute Maximum Ratings ($T_a = 25\text{ }^\circ\text{C}$)

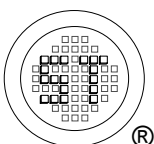
Parameter	Symbol	Value	Unit
Peak Reverse Voltage	V_{RM}	100	V
Reverse Voltage	V_R	75	V
Rectified Current (Average) Half Wave Rectification with Resist. Load ($f \geq 50\text{ Hz}$)	I_O	150 ¹⁾	mA
Surge Forward Current at $t < 1\text{ s}$ and $T_j = 25\text{ }^\circ\text{C}$	I_{FSM}	500	mA
Power Dissipation	P_{tot}	500 ¹⁾	mW
Junction Temperature	T_j	175	$^\circ\text{C}$
Storage Temperature Range	T_S	- 65 to + 175	$^\circ\text{C}$

¹⁾ Valid provided that electrodes are kept at ambient temperature.

Characteristics at $T_a = 25\text{ }^\circ\text{C}$

Parameter	Symbol	Min.	Max.	Unit
Forward Voltage at $I_F = 5\text{ mA}$ at $I_F = 100\text{ mA}$	V_F	0.62 -	0.72 1	V
Leakage Current at $V_R = 20\text{ V}$ at $V_R = 75\text{ V}$	I_R	- -	25 5	nA μA
Reverse Breakdown Voltage tested with 100 μA Pulses	$V_{(BR)R}$	100	-	V
Capacitance at $V_F = V_R = 0$	C_{tot}	-	4	pF
Reverse Recovery Time from $I_F = 10\text{ mA}$ to $I_R = 1\text{ mA}$, $V_R = 6\text{ V}$, $R_L = 100\text{ }\Omega$	t_{rr}	-	4	ns
Thermal Resistance Junction to Ambient Air	R_{thA}	-	0.35 ¹⁾	K/mW
Rectification Efficiency at $f = 100\text{ MHz}$, $V_{RF} = 2\text{ V}$	η_v	0.45	-	-

¹⁾ Valid provided that electrodes are kept at ambient temperature.



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ISO/TS 16949 : 2002
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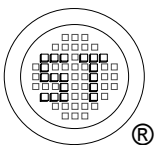
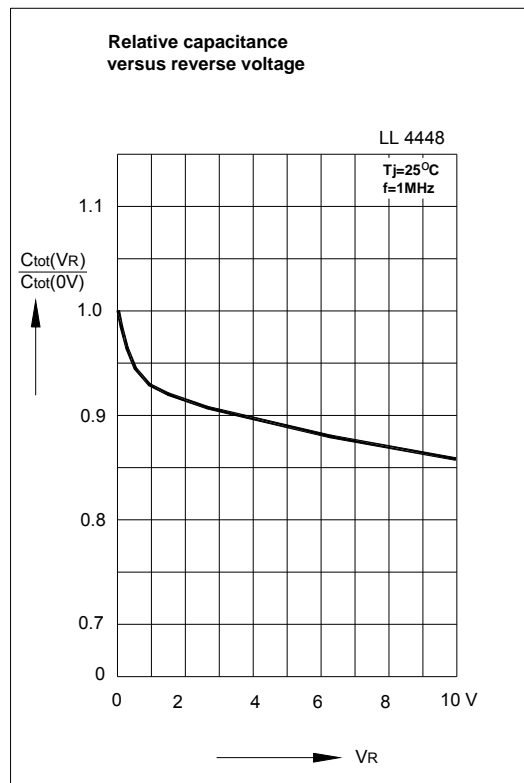
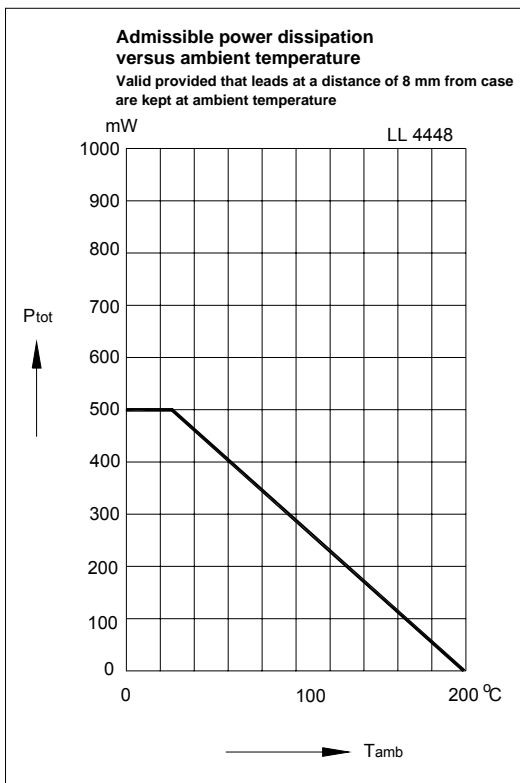
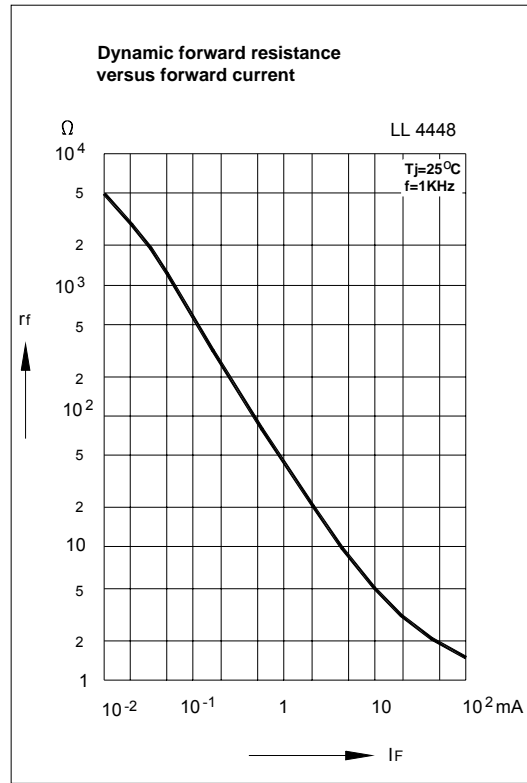
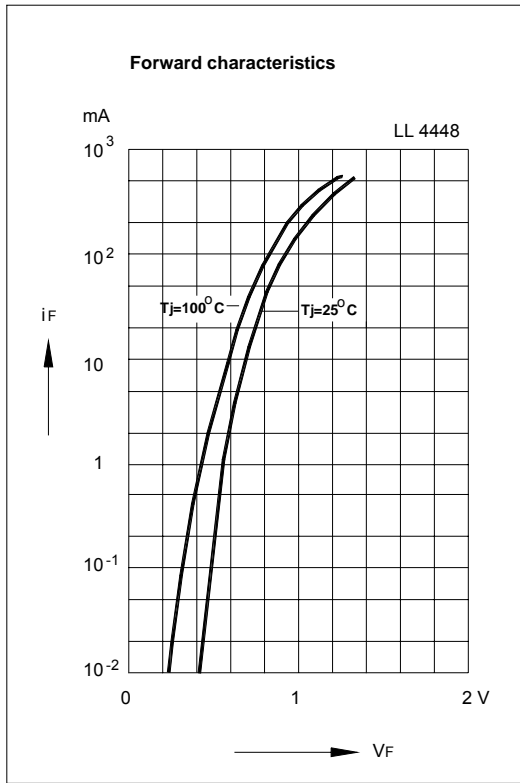


ISO 14001:2004
Certificate No. 7116



ISO 9001:2000
Certificate No. 0506098

Dated : 12/01/2007



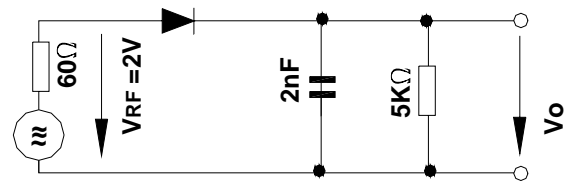
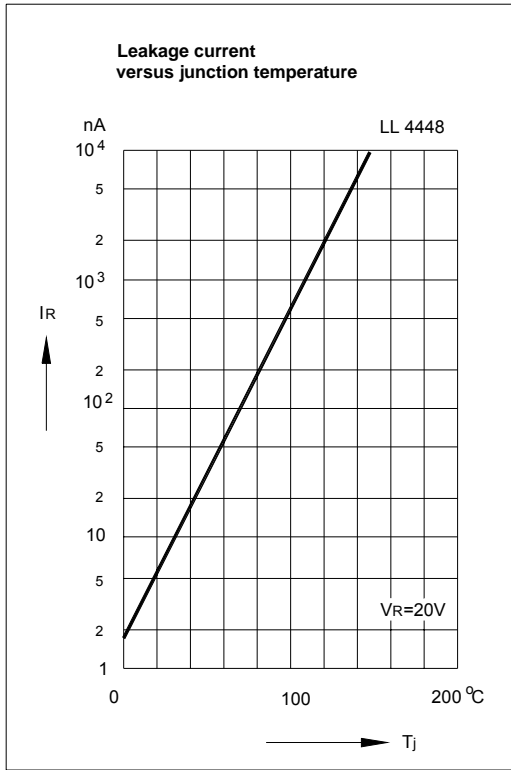
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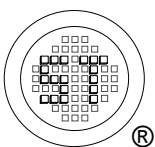
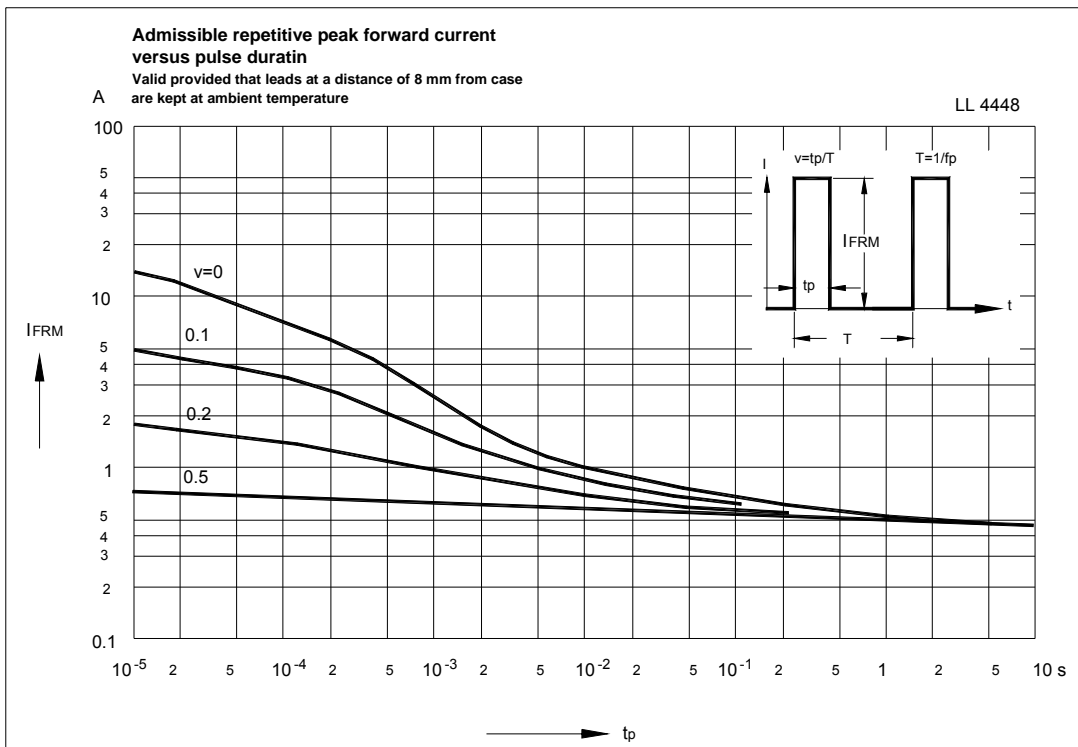
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Rectification Efficiency Measurement Circuit



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