

**Silicon PNP Power Transistors**

**2SB1335**

**DESCRIPTION**

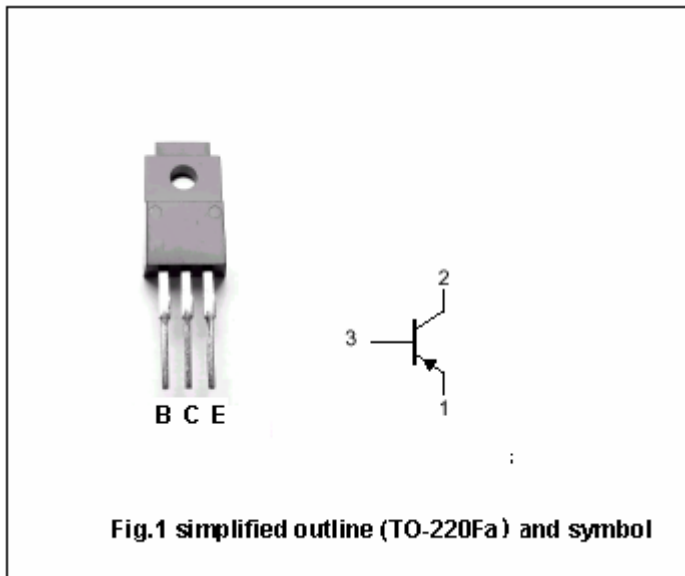
- With TO-220Fa package
- Complement to type 2SD1855
- Low collector saturation voltage

**APPLICATIONS**

- For low frequency power amplifier applications

**PINNING**

PIN	DESCRIPTION
1	Emitter
2	Collector
3	Base



**Absolute maximum ratings(Ta=25°C)**

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V <sub>CBO</sub>	Collector-base voltage	Open emitter	-80	V
V <sub>CEO</sub>	Collector -emitter voltage	Open base	-60	V
V <sub>EBO</sub>	Emitter-base voltage	Open collector	-6	V
I <sub>C</sub>	Collector current		-4	A
P <sub>C</sub>	Collector power dissipation	T <sub>C</sub> =25°C	30	W
T <sub>j</sub>	Junction temperature		150	°C
T <sub>stg</sub>	Storage temperature		-55~150	°C

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## CHARACTERISTICS

T<sub>j</sub>=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-emitter breakdown voltage	I <sub>C</sub> =-1mA; I <sub>B</sub> =0	-60			V
V <sub>(BR)CBO</sub>	Collector-base breakdown voltage	I <sub>C</sub> =-50μA; I <sub>E</sub> =0	-80			V
V <sub>(BR)EBO</sub>	Collector-emitter breakdown voltage	I <sub>E</sub> =-50μA; I <sub>C</sub> =0	-6			V
V <sub>CEsat</sub>	Collector-emitter saturation voltage	I <sub>C</sub> =-3A; I <sub>B</sub> =-0.3A			-1.0	V
V <sub>BEsat</sub>	Base-emitter saturation voltage	I <sub>C</sub> =-3A; I <sub>B</sub> =-0.3A			-1.5	V
I <sub>CBO</sub>	Collector cut-off current	V <sub>CB</sub> =-80V; I <sub>E</sub> =0			-10	μA
I <sub>EBO</sub>	Emitter cut-off current	V <sub>EB</sub> =-6V; I <sub>C</sub> =0			-10	μA
h <sub>FE</sub>	DC current gain	I <sub>C</sub> =-1A; V <sub>CE</sub> =-5V	60		320	
f <sub>T</sub>	Transition frequency	I <sub>C</sub> =-0.5A; V <sub>CE</sub> =-5V		12		MHz
C <sub>OB</sub>	Collector output capacitance	f=1MHz; V <sub>CB</sub> =-10V		100		pF

◆ h<sub>FE</sub> Classifications

D	E	F
60-120	100-200	160-320

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PACKAGE OUTLINE

