

HVD388C

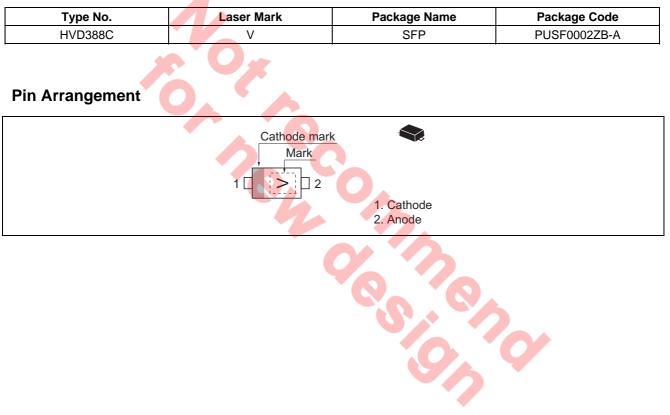
Variable Capacitance Diode for VCO

REJ03G0362-0200 Rev.2.00 Mar 30, 2006

Features

- High capacitance ratio. (n = 1.88 min)
- Low series resistance. (rs = $0.75 \Omega \text{ max}$)
- Super small Flat Lead Package (SFP) is suitable for surface mount design.

Ordering Information





Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit
Reverse voltage	V _R	15	V
Junction temperature	Tj	125	°C
Storage temperature	Tstg	–55 to +125	۵°

Electrical Characteristics

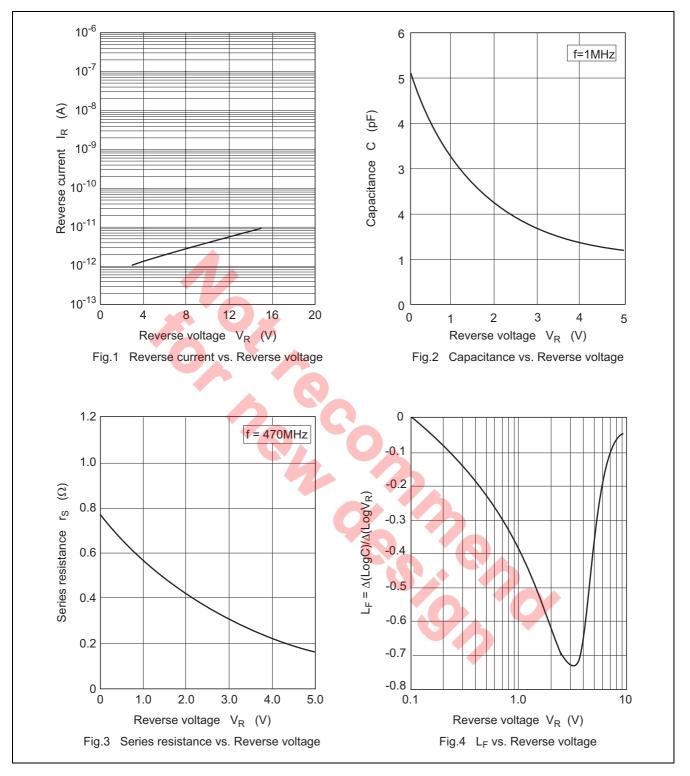
						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse current	I _{R1}		—	10	nA	V _R = 15 V
	I _{R2}		—	50		V _R = 15 V, Ta = 60°C
Capacitance	C ₁	3.162	—	3.465	pF	$V_{R} = 1 V, f = 1 MHz$
	C ₃	1.570	—	1.720		$V_{R} = 3 V, f = 1 MHz$
Capacitance ratio	n	1.880	_	2.150	_	C ₁ / C ₃
Series resistance	rs		_	0.75	Ω	V _R = 1 V, f = 470 MHz

Note: For SFP package, the material of lead is exposed for cutting plane. There for, soldering nature of lead tip part is considered as unquestioned. Please kindly consider soldering nature.



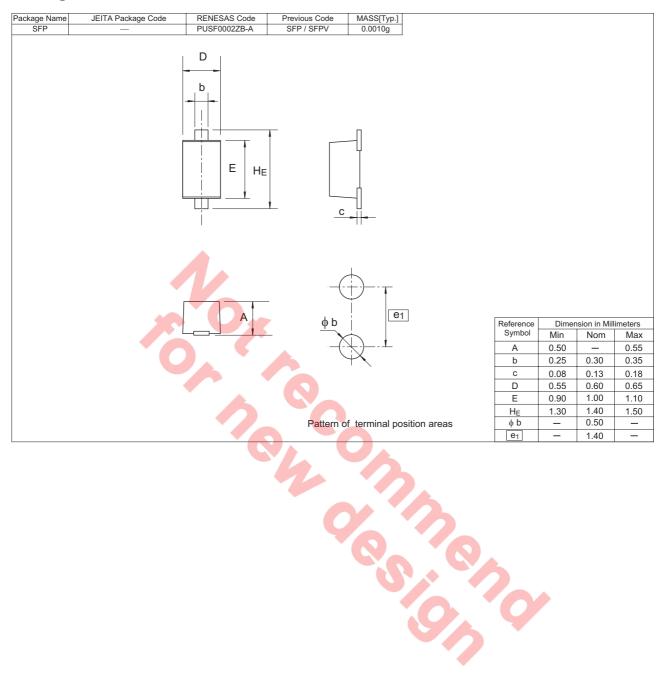


Main Characteristic





Package Dimensions





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