

ACMOS ENABLE/DISABLE TRISTATE OUTPUT

SPECIFICATIONS	JMA25R		
Frequency Range	13MHz to 70MHz		
Frequency Stability	±10ppm		
	(Inclusive of calibration tolerance at 25°C and operating temperature range)		
Temperature Range	0°C to +50°C		
Output	Waveform	CMOS Squarewave, TTL Compatible	
	Load	15pF	
	Voltage	Voh	4.4V Minimum
		Vol	0.44V Maximum
	Current	Ioh	-24mA
		Iol	24mA Max
	Duty Cycle	48/52 Maximum Measured at 2.5V	
	Rise/Fall Time	2nS	
Input	Output E/D Time	100nS Maximum	
	Enable Vih	2.0V Min	
	Disable Vil	0.8V Max	
	Oscillator output is enabled with no connection on pin 1		
Start Up Time	10mS Maximum		
Supply Voltage	+5Vdc ±5%		
Supply Current	100mA Maximum		
Package	All metal, hermetically sealed, welded package		

WARNING! Do not insert oscillator into a hot circuit. Failure to comply will result in damage to oscillator.

