TOSHIBA Transistor Silicon PNP Epitaxial Type (PCT Process)

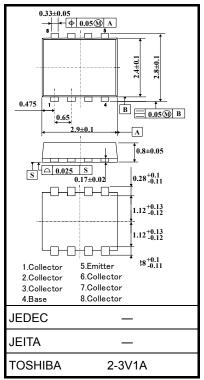
TPCP8601

High-Speed Switching Applications DC-DC Converter Applications Strobo Flash Applications

- High DC current gain: $h_{FE} = 200$ to 500 (IC = -0.6 A)
- Low collector-emitter saturation: $V_{CE (sat)} = -0.19 V (max)$
- High-speed switching: t_f = 35 ns (typ.)

Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit	
Collector-base voltage		V _{CBO}	-20	V	
Collector-emitter voltage		V _{CEO}	-20	V	
Emitter-base voltage		V _{EBO}	-7	V	
Collector current	DC (Note 1)	Ι _C	-4.0	A	
	Pulse (Note 1)	I _{CP}	-7.0		
Base current		Ι _Β	-0.5	А	
Collector power dissipation (t = 10s)	t = 10s	Do (Noto 2)	3.3	W	
	DC	Pc (Note 2)	1.3		
Junction temperature		Tj	150	°C	
Storage temperature range		T _{stg}	–55 to 150	°C	



Weight: 0.017 g (typ.)

Figure 1. Circuit Configuration (Top View)

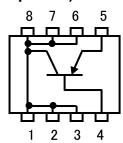
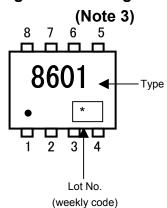


Figure 2. Marking



Note 1: Ensure that the junction temperature does not exceed 150°C during use of this device.

Note 2: Mounted on an FR4 board (glass epoxy, 1.6 mm thick, Cu area: 645 mm²)

Note 3: • on the lower left of the marking indicates Pin 1.

* Weekly code (three digits):



Week of manufacture

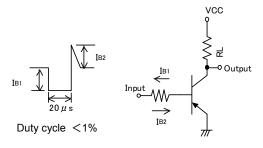
(01 for the first week of the year, continuing up to 52 or 53)

Year of manufacture (lowest-order digit of the calendar year) Unit: mm

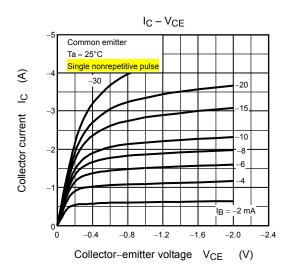
Electrical Characteristics (Ta = 25°C)

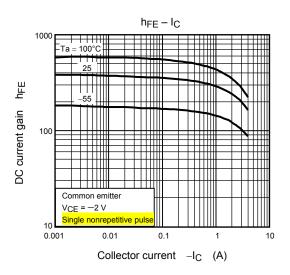
Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current		I _{CBO}	$V_{CB} = -20 \text{ V}, \text{ I}_{E} = 0$	_		-100	nA
Emitter cut-off current		I _{EBO}	$V_{EB} = -7 V, I_C = 0$	_		-100	nA
Collector-base breakdown voltage		V (BR) CBO	$I_{C} = -1 \text{ mA}, I_{B} = 0$	-20	_	_	V
Collector-emitter breakdown voltage		V (BR) CEO	$I_{C} = -10 \text{ mA}, I_{B} = 0$	-20	_	_	V
DC current gain		h _{FE} (1)	$V_{CE} = -2 V$, $I_C = -0.6 A$	200	_	500	
		h _{FE} (2)	$V_{CE} = -2 V$, $I_C = -2.0 A$	100	_	_	
Collector-emitter saturation voltage		V _{CE (sat)}	$I_{C} = -2 \text{ A}, I_{B} = -67 \text{ mA}$	—	_	-0.19	V
Base-emitter saturation voltage		V _{BE (sat)}	$I_{C} = -2 \text{ A}, I_{B} = -67 \text{ mA}$	—	_	-1.1	V
Switching time	Rise time	tr	See Figure 3 circuit diagram $V_{CC} \simeq 12$ V, $R_L = 6 \Omega$ $I_{B1} = -I_{B2} = -67$ mA	_	72		
	Storage time	t _{stg}		_	170		ns
	Fall time	t _f			35		

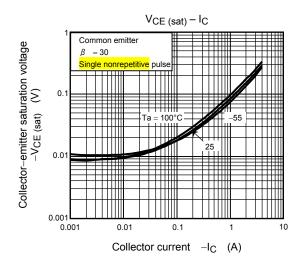
Figure 3. Switching Time Test Circuit & Timing Chart

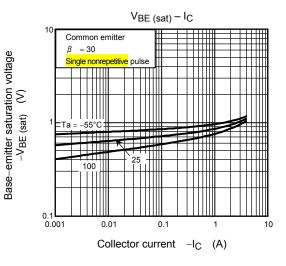


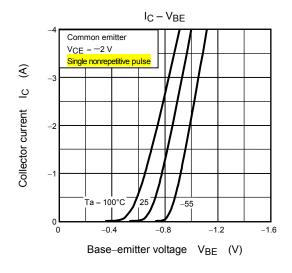
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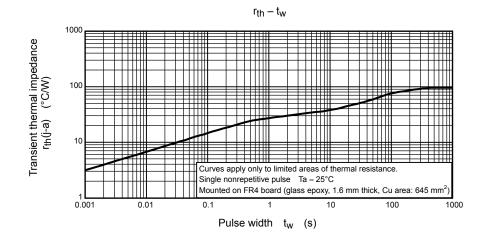


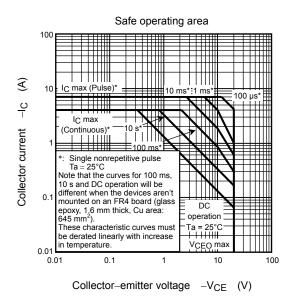












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