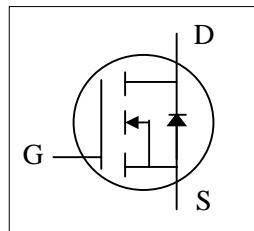




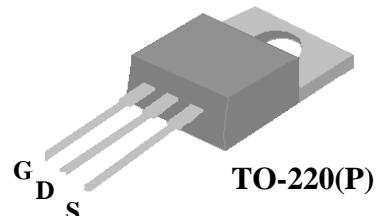
- ▼ Simple Drive Requirement
- ▼ Lower On-resistance
- ▼ Fast Switching Characteristic



$BV_{DSS}$	75V
$R_{DS(ON)}$	5mΩ
$I_D$	80A

## Description

Advanced Power MOSFETs from APEC provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.



The TO-220 package is widely preferred for commercial-industrial power applications and suited for low voltage applications such as DC/DC converters.

## Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	75	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D @ T_C = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^3$	80	A
$I_D @ T_C = 100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	70	A
$I_{DM}$	Pulsed Drain Current <sup>1</sup>	320	A
$P_D @ T_C = 25^\circ C$	Total Power Dissipation	300	W
	Linear Derating Factor	2	W/°C
$E_{AS}$	Single Pulse Avalanche Energy <sup>4</sup>	450	mJ
$T_{STG}$	Storage Temperature Range	-55 to 175	°C
$T_J$	Operating Junction Temperature Range	-55 to 175	°C

## Thermal Data

Symbol	Parameter	Value	Units
$R_{thj-c}$	Maximum Thermal Resistance, Junction-case	0.5	°C/W
$R_{thj-a}$	Maximum Thermal Resistance, Junction-ambient	62	°C/W

**Electrical Characteristics@ $T_j=25^\circ\text{C}$ (unless otherwise specified)**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_{\text{D}}=1\text{mA}$	75	-	-	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_j$	Breakdown Voltage Temperature Coefficient	Reference to $25^\circ\text{C}$ , $I_{\text{D}}=1\text{mA}$	-	0.01	-	$\text{V}/^\circ\text{C}$
$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{\text{GS}}=10\text{V}$ , $I_{\text{D}}=60\text{A}$	-	-	5	$\text{m}\Omega$
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$ , $I_{\text{D}}=250\text{\mu A}$	2	-	4	V
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=10\text{V}$ , $I_{\text{D}}=60\text{A}$	-	57	-	S
$I_{\text{DSS}}$	Drain-Source Leakage Current ( $T_j=25^\circ\text{C}$ )	$V_{\text{DS}}=75\text{V}$ , $V_{\text{GS}}=0\text{V}$	-	-	10	$\text{\mu A}$
	Drain-Source Leakage Current ( $T_j=150^\circ\text{C}$ )	$V_{\text{DS}}=60\text{V}$ , $V_{\text{GS}}=0\text{V}$	-	-	250	$\text{\mu A}$
$I_{\text{GSS}}$	Gate-Source Leakage	$V_{\text{GS}}= \pm 20\text{V}$	-	-	$\pm 100$	nA
$Q_g$	Total Gate Charge <sup>2</sup>	$I_{\text{D}}=80\text{A}$	-	85	135	nC
$Q_{\text{gs}}$	Gate-Source Charge	$V_{\text{DS}}=40\text{V}$	-	25	-	nC
$Q_{\text{gd}}$	Gate-Drain ("Miller") Charge	$V_{\text{GS}}=10\text{V}$	-	36	-	nC
$t_{\text{d}(\text{on})}$	Turn-on Delay Time <sup>2</sup>	$V_{\text{DS}}=40\text{V}$	-	22	-	ns
$t_r$	Rise Time	$I_{\text{D}}=80\text{A}$	-	160	-	ns
$t_{\text{d}(\text{off})}$	Turn-off Delay Time	$R_G=3.3\Omega$ , $V_{\text{GS}}=10\text{V}$	-	38	-	ns
$t_f$	Fall Time	$R_D=0.5\Omega$	-	165	-	ns
$C_{\text{iss}}$	Input Capacitance	$V_{\text{GS}}=0\text{V}$	-	4290	6870	pF
$C_{\text{oss}}$	Output Capacitance	$V_{\text{DS}}=25\text{V}$	-	985	-	pF
$C_{\text{rss}}$	Reverse Transfer Capacitance	f=1.0MHz	-	390	-	pF
$R_g$	Gate Resistance	f=1.0MHz	-	1.2	1.8	$\Omega$

**Source-Drain Diode**

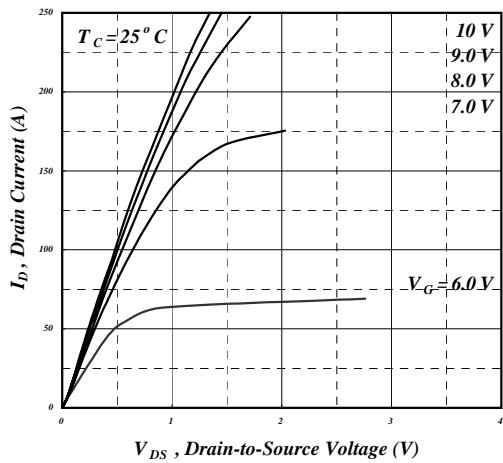
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$V_{\text{SD}}$	Forward On Voltage <sup>2</sup>	$I_{\text{S}}=60\text{A}$ , $V_{\text{GS}}=0\text{V}$	-	-	1.3	V
$t_{\text{rr}}$	Reverse Recovery Time <sup>2</sup>	$I_{\text{S}}=40\text{A}$ , $V_{\text{GS}}=0\text{V}$	-	75	-	ns
$Q_{\text{rr}}$	Reverse Recovery Charge	$dI/dt=100\text{A}/\mu\text{s}$	-	190	-	nC

**Notes:**

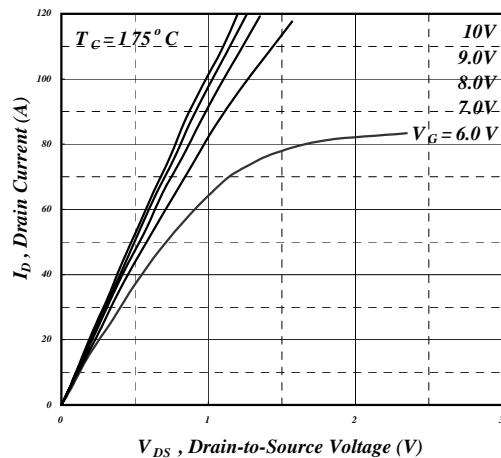
- 1.Pulse width limited by Max. junction temperature.
- 2.Pulse test
- 3.Package limitation current is 80A, calculated continuous current based on maximum allowable junction temperature is 169A.
- 4.Starting  $T_j=25^\circ\text{C}$  ,  $L=1\text{mH}$  ,  $I_{\text{AS}}=30\text{A}$ .

THIS PRODUCT IS AN ELECTROSTATIC SENSITIVE, PLEASE HANDLE WITH CAUTION.

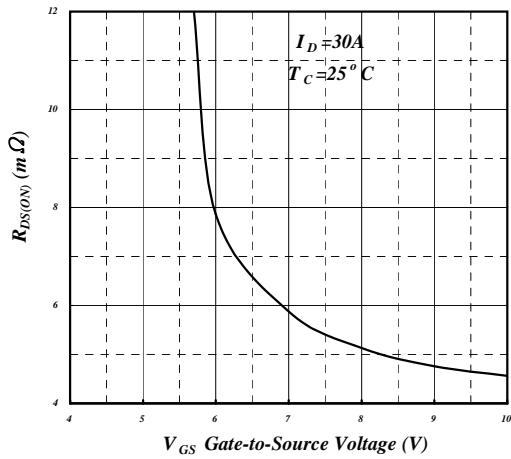
THIS PRODUCT HAS BEEN QUALIFIED FOR CONSUMER MARKET. APPLICATIONS OR USES AS CRITERIAL COMPONENT IN LIFE SUPPORT DEVICE OR SYSTEM ARE NOT AUTHORIZED.



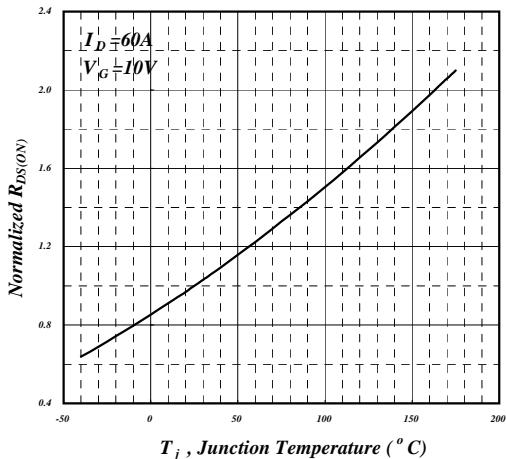
**Fig 1. Typical Output Characteristics**



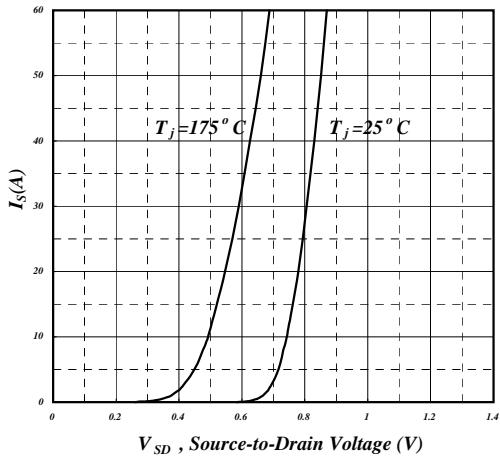
**Fig 2. Typical Output Characteristics**



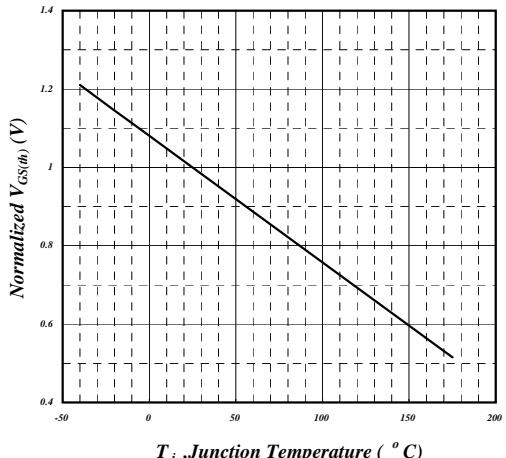
**Fig 3. On-Resistance v.s. Gate Voltage**



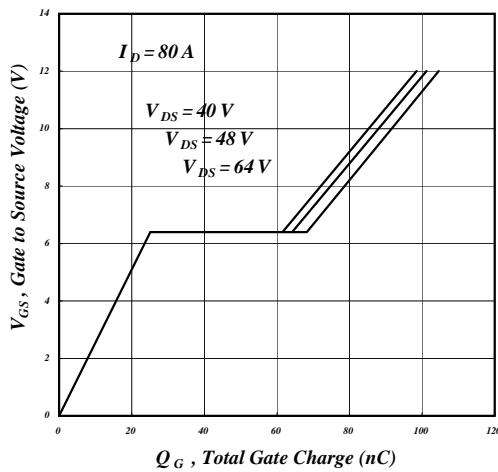
**Fig 4. Normalized On-Resistance v.s. Junction Temperature**



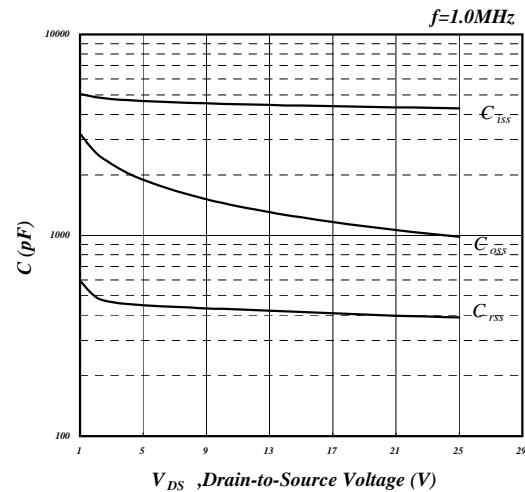
**Fig 5. Forward Characteristic of Reverse Diode**



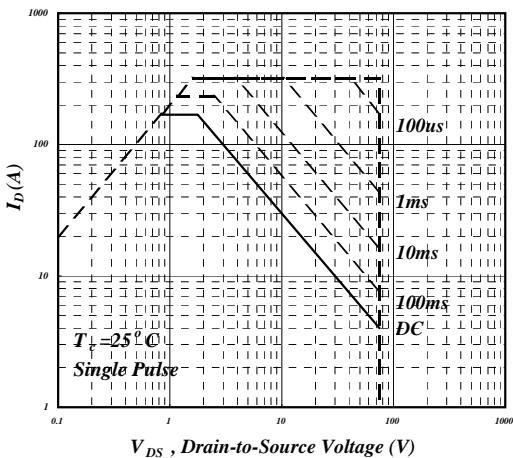
**Fig 6. Gate Threshold Voltage v.s. Junction Temperature**



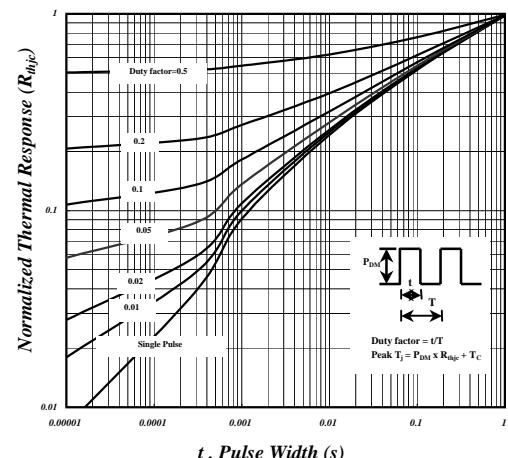
**Fig 7. Gate Charge Characteristics**



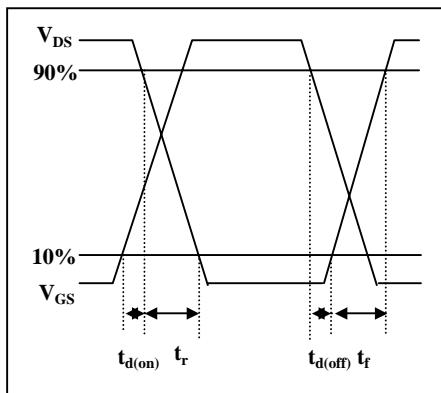
**Fig 8. Typical Capacitance Characteristics**



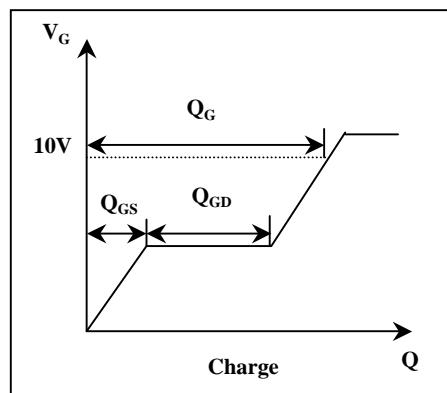
**Fig 9. Maximum Safe Operating Area**



**Fig 10. Effective Transient Thermal Impedance**



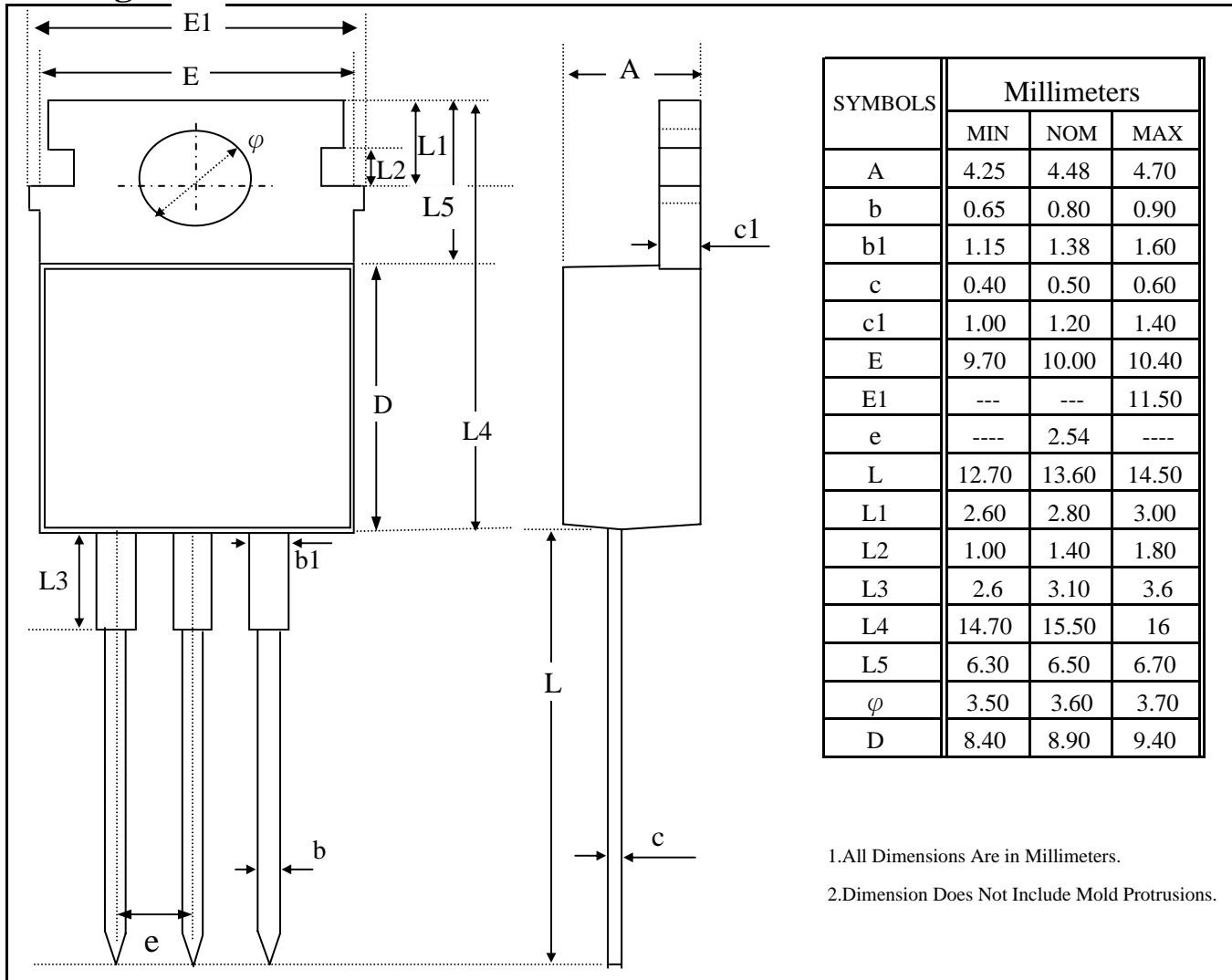
**Fig 11. Switching Time Waveform**



**Fig 12. Gate Charge Waveform**



## Package Outline : TO-220



## Part Marking Information & Packing : TO-220

