TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π -MOSV)

2SK2838

Chopper Regulator, DC–DC Converter and Motor Drive Applications

- Low drain-source ON resistance $: RDS (ON) = 0.84 \Omega (typ.)$
- High forward transfer admittance $|Y_{fs}| = 4.4 \text{ S (typ.)}$
- Low leakage current $: I_{DSS} = 100 \ \mu A \ (max) \ (V_{DS} = 400 \ V)$
- Enhancement mode $: V_{th} = 2.0 \sim 4.0 \text{ V} (V_{DS} = 10 \text{ V}, \text{ ID} = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	400	V	
Drain-gate voltage (R _{GS} = 20 kΩ)		V _{DGR}	400	V	
Gate-source voltage		V _{GSS}	±30	V	
Drain current	DC (Note 1)	۱ _D	5.5	А	
	Pulse (Note 1)	I _{DP}	22	А	
Drain power dissipatio	n (Tc = 25°C)	PD	40	W	
Single pulse avalanche energy (Note 2)		E _{AS}	223	mJ	
Avalanche current		I _{AR}	5.5	А	
Repetitive avalanche e	energy (Note 3)	E _{AR}	4.0	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55~150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Thermal Characteristics

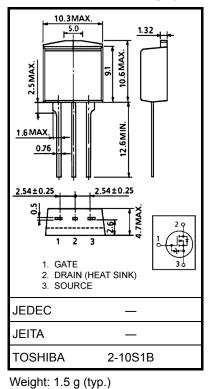
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	3.125	°C / W
Thermal resistance, channel to ambient	R _{th (ch−a)}	83.3	°C / W

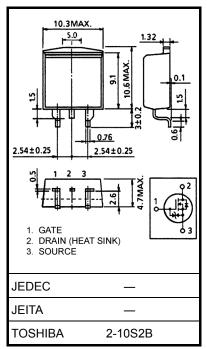
Note 1: Ensure that the channel temperature does not exceed 150°C.

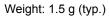
Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 12.0 mH, R_G = 25 Ω , I_{AR} = 5.5 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.







Unit: mm

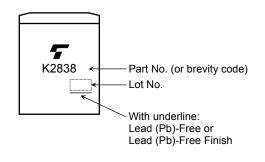
Electrical Characteristics (Ta = 25°C)

Charac	teristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rrent	I _{GSS}	V_{GS} = ±25 V, V_{DS} = 0 V	—	_	±10	μA
Gate-source bre	eakdown voltage	V _(BR) GSS	I _G = ±10 μA, V _{DS} = 0 V	±30	_	_	V
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 400 V, V _{GS} = 0 V	_	_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	400	_	_	V
Gate threshold v	voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	_	4.0	V
Drain-source O	N resistance	R _{DS (ON)}	V _{GS} = 10 V, I _D = 3 A	—	0.84	1.2	Ω
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 3 A	2.0	4.4	_	S
Input capacitance	e	C _{iss}		_	720	_	
Reverse transfer capacitance		C _{rss}	V_{DS} = 10 V, V_{GS} = 0 V, f = 1 MHz	—	80	—	pF
Output capacitance		C _{oss}			250	_	
Switching time	Rise time	tr	$V_{GS} \stackrel{10V}{}_{0V} \stackrel{I_{D}=2A}{}_{RL} = 100\Omega$ $V_{DD} = 200V$	_	15	_	
	Turn-on time	t _{on}		_	30	_	ns
	Fall time	t _f		_	25	_	115
	Turn-off time	t _{off}	Duty \leq 1%, t _w =10 μ s	_	110 —		
Total gate charge (gate-source plus gate-drain)		Qg	V _{DD} =320 V, V _{GS} = 10 V, I _D = 5.5 A		17	_	
Gate-source charge		Q _{gs}			10	_	nC
Gate-drain ("miller") Charge		Q _{gd}			7		

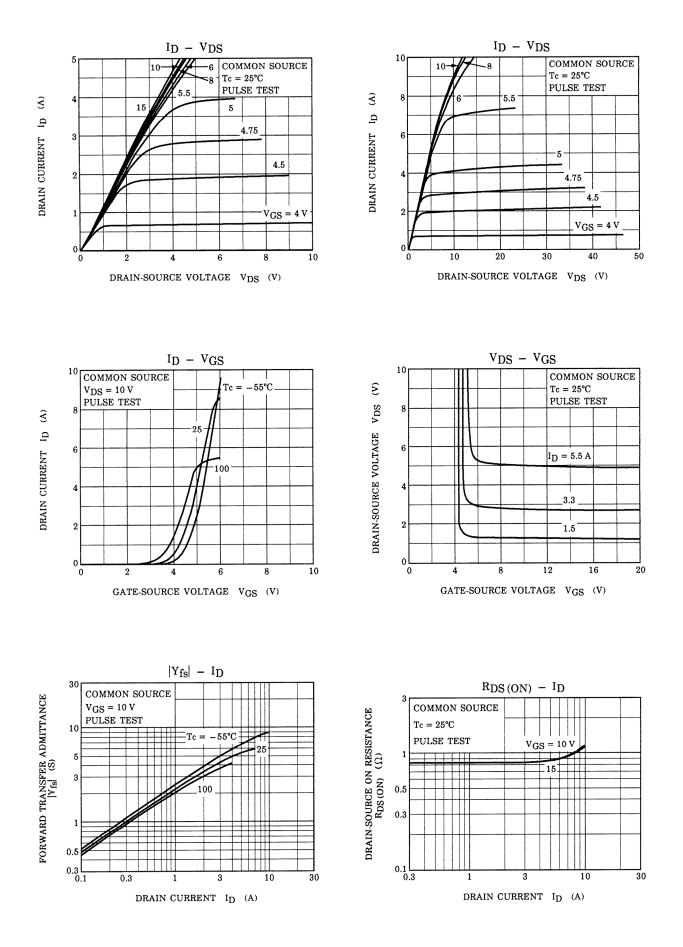
Source–Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	—	_	_	5.5	А
Pulse drain reverse current (Note 1)	I _{DRP}	—	_	_	22	А
Forward voltage (diode)	V _{DSF}	I _{DR} = 5.5 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 5.5 A, V _{GS} = 0 V dI _{DR} / dt = 100 A / μs		350		ns
Reverse recovery charge	Q _{rr}			2.1		μC

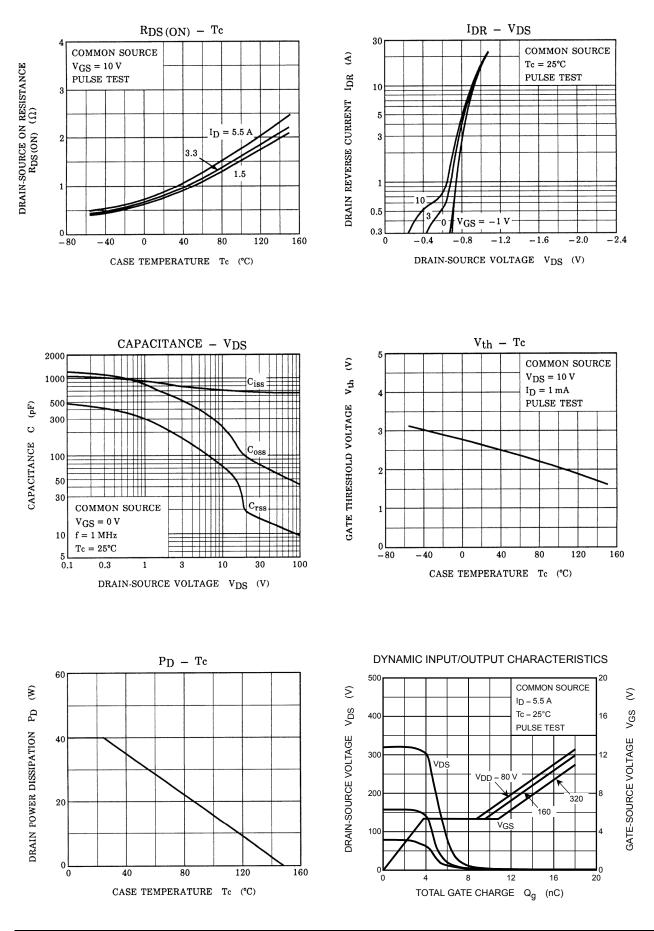
Marking

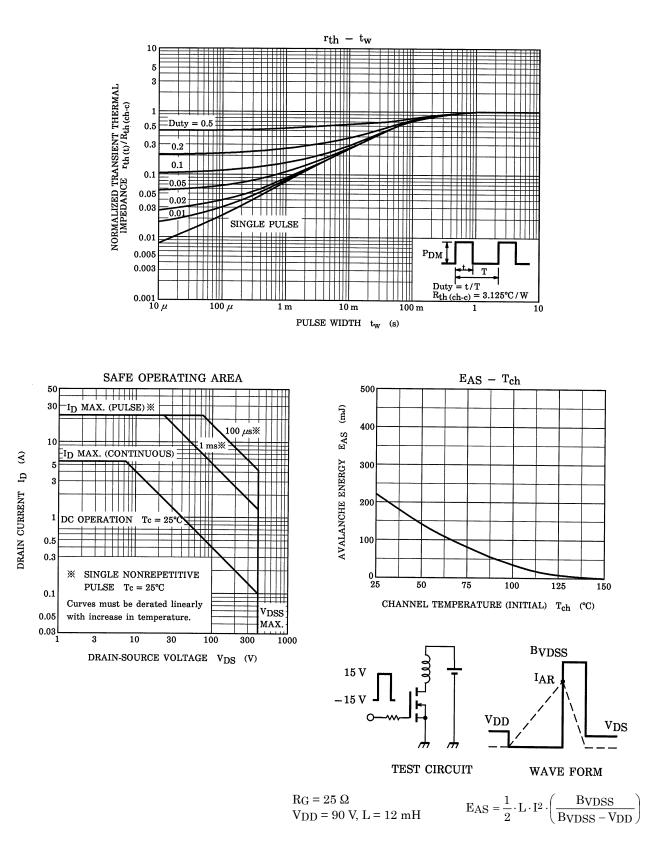


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