

Description

- High speed switching application.
- Analog switch application.

Features

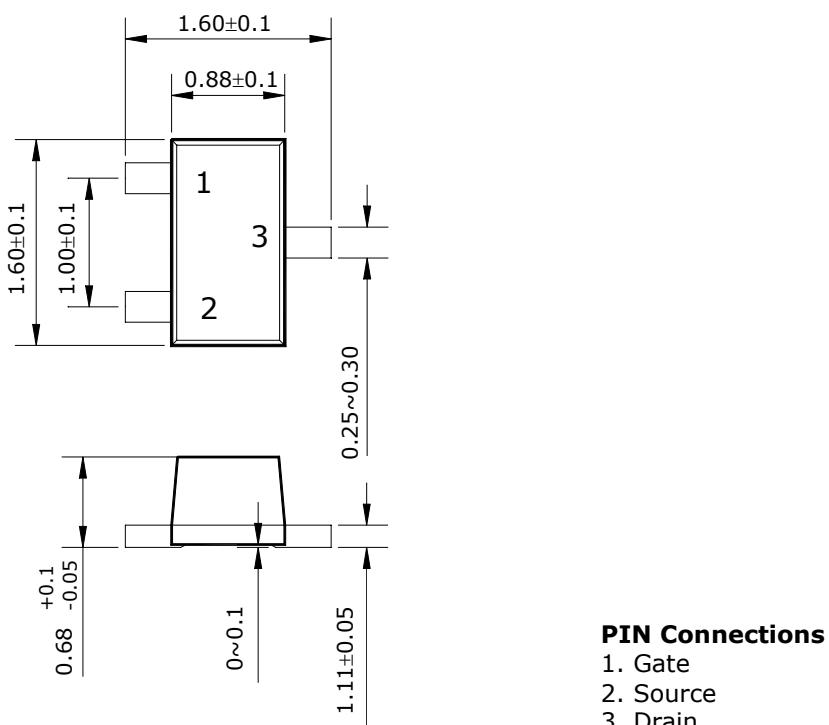
- -2.5V Gate drive.
- Low threshold voltage : $V_{th} = -0.5 \sim -1.5V$.
- High speed.

Ordering Information

Type NO.	Marking	Package Code
STJ828EF	J8	SOT-523F

Outline Dimensions

unit : mm



Absolute maximum ratings

(Ta=25°C)

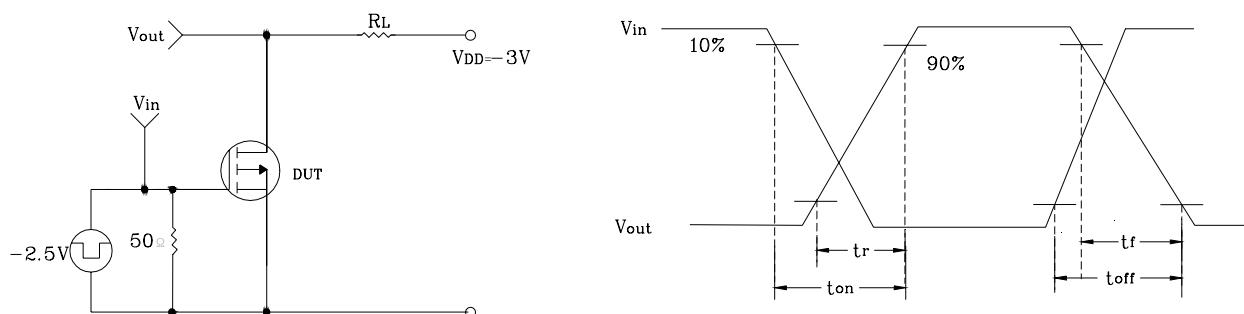
Characteristic	Symbol	Ratings	Unit
Drain-Source voltage	V _{DS}	-20	V
Gate-Source voltage	V _{GSS}	±7	V
DC Drain current	I _D	-50	mA
Drain Power dissipation	P _D	150	mW
Channel temperature	T _{ch}	150	°C
Storage temperature range	T _{stg}	-55~150	°C

Electrical Characteristics

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Drain-Source breakdown voltage	BV _{DSS}	I _D =-100μA, V _{GS} =0	-20			V
Gate-Threshold voltage	V _{th}	I _D =-0.1mA, V _{DS} =-3V	-0.5		-1.5	V
Drain cut-off current	I _{DSS}	V _{DS} =-20V, V _{GS} =0			-1	μA
Gate leakage current	I _{GSS}	V _{GS} =±7V, V _{DS} =0			±1	μA
Drain-Source on-resistance	R _{DS(ON)}	V _{GS} =-2.5V, I _D =-10mA			40	Ω
Forward transfer admittance	Y _{fs}	V _{DS} =-3V, I _D =-10mA	15			μS
Input capacitance	C _{iss}	V _{DS} =-3V, V _{GS} =0, f=1MHz		10.4		pF
Output capacitance	C _{oss}	V _{DS} =-3V, V _{GS} =0, f=1MHz		8.4		pF
Reverse Transfer capacitance	C _{rss}	V _{DS} =-3V, V _{GS} =0, f=1MHz		2.8		pF
Turn-on time	t _{ON}	V _{DD} =-3V, I _D =-10mA V _{GEN} =0~ -2.5V		0.15		μs
Turn-off time	t _{OFF}	V _{DD} =-3V, I _D =-10mA V _{GEN} =0~ -2.5V		0.13		μs

*. Switching Time Test Circuit



Electrical Characteristic Curves

Fig1 Id - VDS

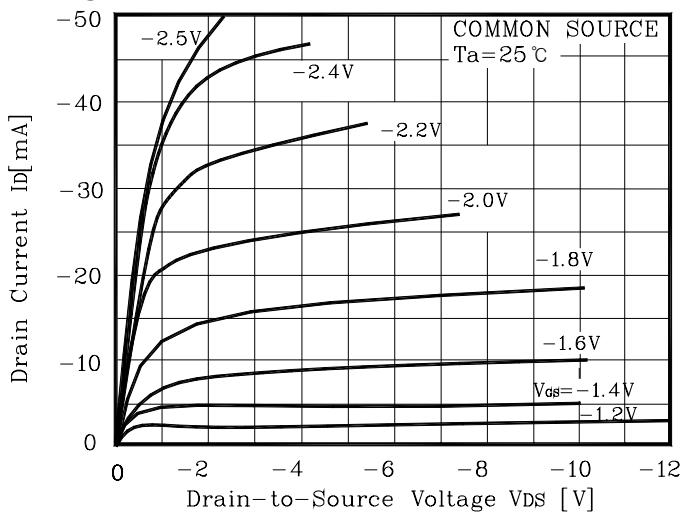


Fig2 Id - VDS

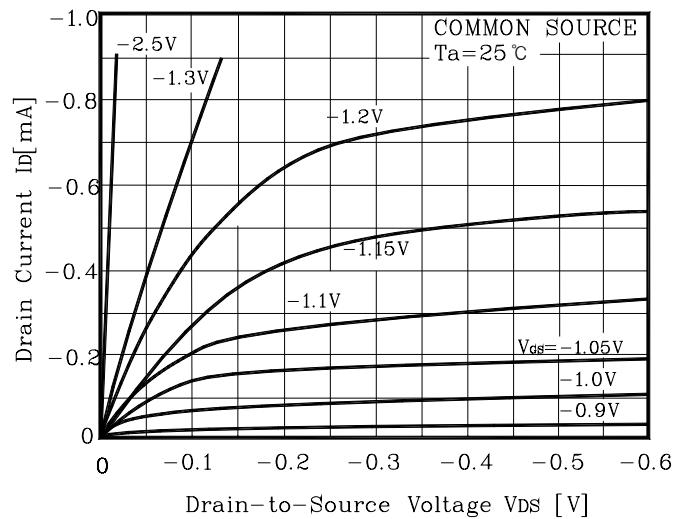


Fig3 IDR - VDS

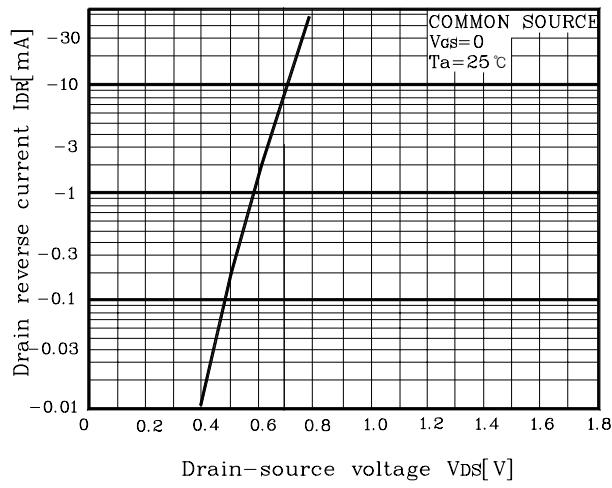


Fig4 Id - VGS

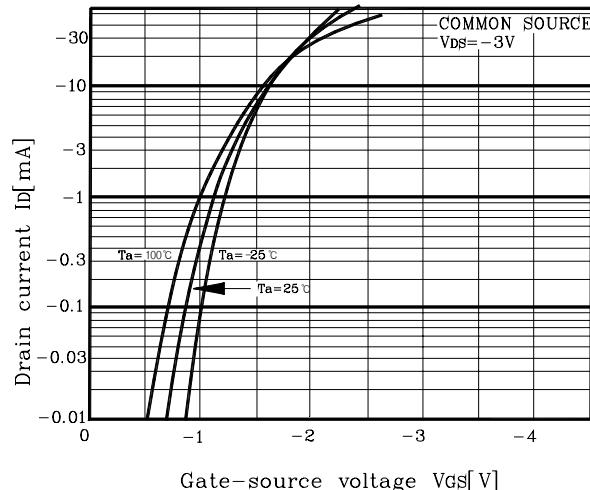


Fig5 | Yfs | - Id

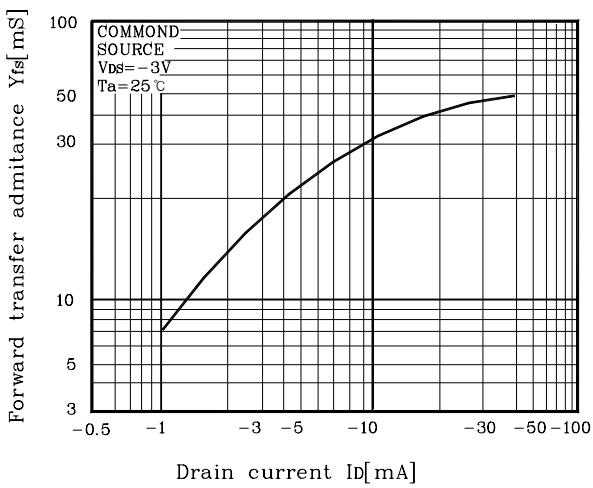


Fig6 C - VDS

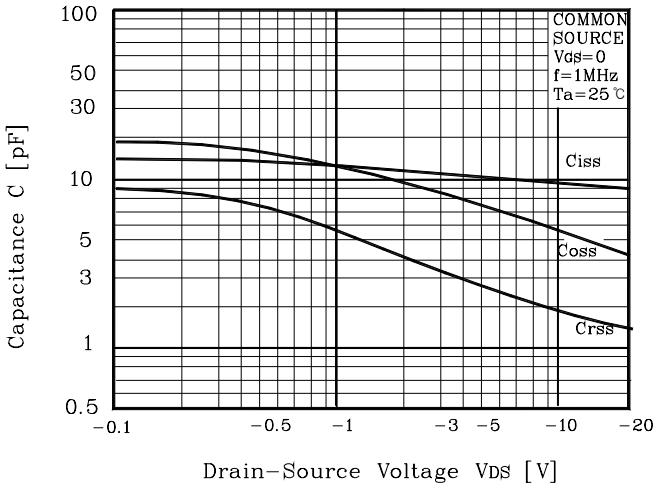


Fig7 V_{DS(on)} - I_D

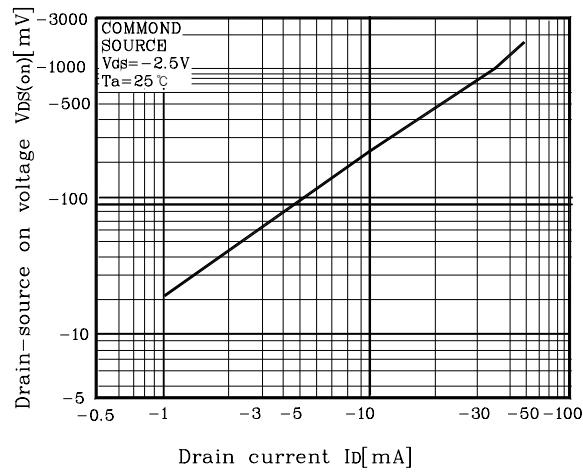


Fig8 t - I_D

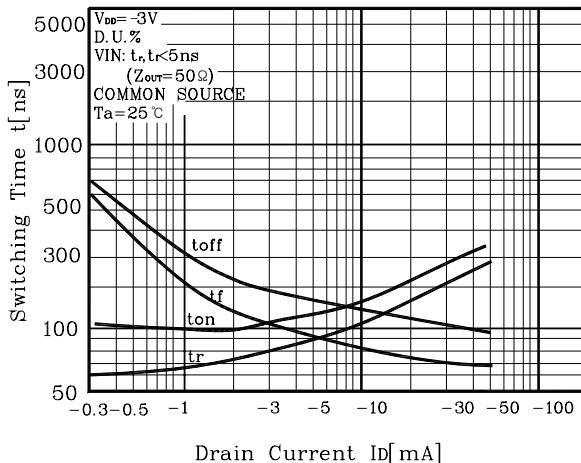


Fig9 P_D - T_a

