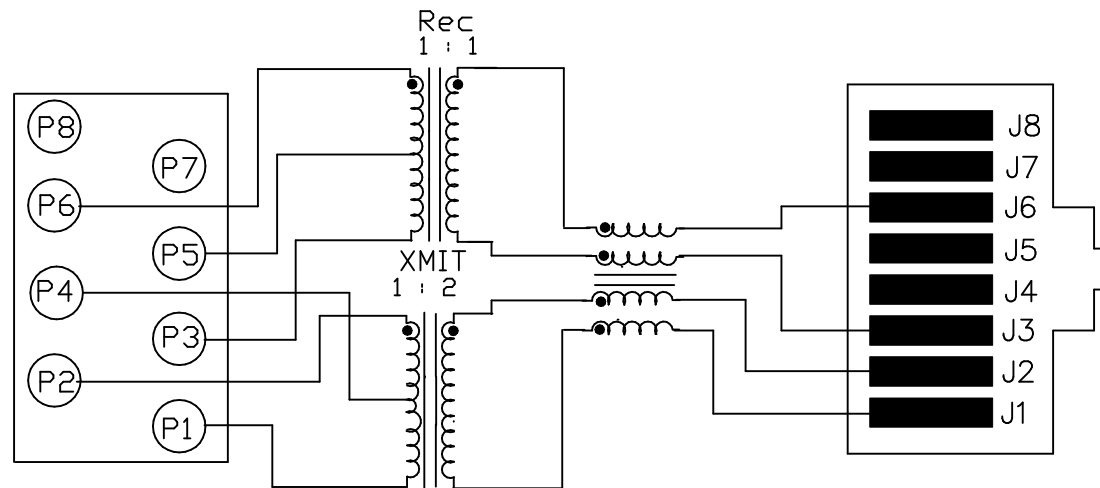


PORT 1





ELECTRICAL SPECIFICATIONS:

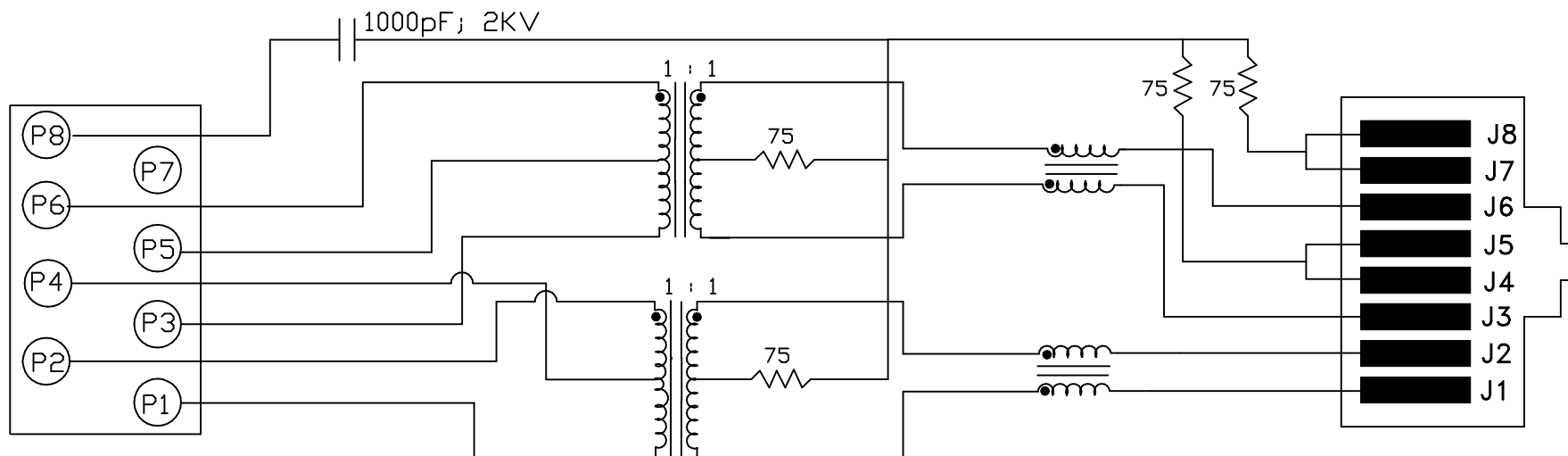
1.0 TURNS RATIO	(P6-P5-P3) : (J6-J3)	: 1CT : 1 ± 3%
	(P2-P4-P1) : (J2-J1)	: 1CT : 2 ± 3%
2.0 INDUCTANCE	(P6-P3)=(J6-J3)	: 98uH MIN. @ 0.01V , 10KHz
	(P2-P1)	: 18uH MIN. @ 0.01V, 10KHz
3.0 LEAKAGE INDUCTANCE	P6-P3 (WITH J6 AND J3 SHORT)	: 0.3uH MAX. @ 1MHz
	P2-P1 (WITH J2 AND J1 SHORT)	: 0.08uH MAX. @ 1MHz
4.0 INTERWINDING CAPACITANCE	(P6,P3) TO (J6,J3)	: 8pf MAX @ 1MHz
	(P2,P1) TO (J2,J1)	: 6pf MAX. @ 1MHz
5.0 DC RESISTANCE	(J6-J3)=(J2-J1)	: 0.7 ohms Max.
	(P6-P8)=(P8-P3)	: 0.3 ohms Max.
	(P2-P7)=(P7-P1)	: 0.3 ohms Max.
6.0 DIELECTRIC WITHSTAND	(P6,P3) TO (J6,J3)	: 1500VAC
	(P2,P1) TO (J2,J1)	: 1500VAC

NOTES

- 1.0 PINS WITHOUT ELECTRICAL CONNECTION ARE OMITTED.
- 2.0 Ref. MG06

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PORT 2





ELECTRICAL SPECIFICATIONS:

- | | | | |
|---|------------------------------|---|------------------|
| 1.0 TURNS RATIO (P3-P5-P6) : | (J3-J6) | : 1CT : 1CT ± 3% | |
| | (P1-P4-P2) : | (J1-J2) | : 1CT : 1CT ± 3% |
| 2.0 INDUCTANCE (P1-P2) | | : 350uH MIN. @ 0.1V , 100KHz, 8mA DC Bias | |
| | (P3-P6) | : 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias | |
| 3.0 LEAKAGE INDUCTANCE P6-P3 (WITH J6 AND J3 SHORT) | | : 0.3 MAX. @ 1MHz | |
| | P2-P1 (WITH J2 AND J1 SHORT) | : 0.3 MAX. @ 1MHz | |
| 4.0 INTERWINDING CAPACITANCE (P6,P5,P3) TO (J6,J3) | | : 30pf MAX @ 1MHz | |
| | (P2,P4,P1) TO (J2,J1) | : 30pf MAX. @ 1MHz | |
| 5.0 DC RESISTANCE (J6-J3)=(J2-J1) | | : 1.2 ohms Max. | |

NOTES

1.0 PINS WITHOUT ELECTRICAL CONNECTION ARE OMITTED.

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6.0 RETURN LOSS: $\langle P6-P3 \rangle = 100 \text{ OHMS}$ AND $\langle P1-P2 \rangle = 100 \text{ OHM REF.}$
1MHz TO 30MHz : 18dB MIN.
60MHz TO 80MHz : 12dB MIN.

NOTE: 100 OHMS CONNECTED TO $\langle J2-J1 \rangle$ OR $\langle J6-J3 \rangle$.



7.0 VOLTAGE WITHSTAND:
 $\langle J1, J2 \rangle$ TO $\langle P1, P2 \rangle$: 1500 VAC
 $\langle J3, J6 \rangle$ TO $\langle P3, P6 \rangle$: 1500 VAC

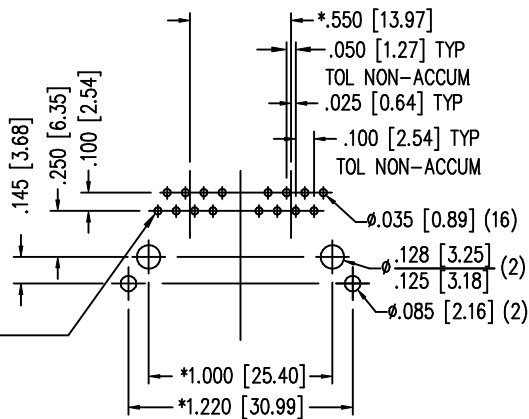
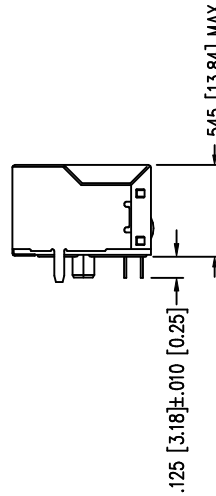
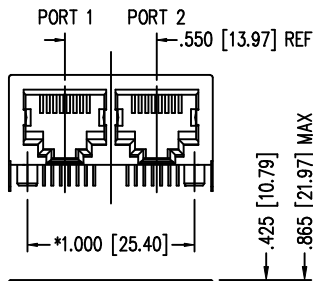
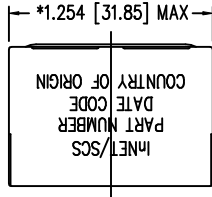
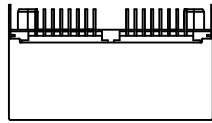
8.0 INSERTION LOSS: $R_S = R_L = 100 \text{ ohms}$
100KHz TO 100MHz 1.1 dB TYP

9.0 RISE TIME: $R_S = 100 \text{ OHMS}$ AND $R_L = 100 \text{ OHMS}$
OUTPUT VOLTAGE = 1 V peak 3.0 nS MAX
PULSE WIDTH = 112nS 3.0 nS MAX

10.0 CROSS TALK:
1MHz TO 100MHz 40 dB TYP

11.0 COMMON TO COMMON MODE ATTENUATION:
30MHz TO 100MHz 35dB TYP

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P.C.B. RECOMMENDED HOLE LAYOUT
 SEEN FROM COMPONENT SIDE
 TOLERANCE ± 0.003 [0.08] UNLESS OTHERWISE SPECIFIED



EMI-RFI ONE PIECE SHIELDED, ESD GROUNDED
 2 PORT PCB HARMONICA JACK PAC
 "-NF" NON-FLANGE

NOTES:

- TOLERANCE COMPLY WITH F.C.C. DIMENSION REQUIREMENTS ALL OTHERS ± 0.005 [0.13] UNLESS OTHERWISE SPECIFIED.
- DIMENSIONS SHOWN WITH "*" TO BE CENTRAL ABOUT CENTER LINE
- PINS NOT ELECTRICALLY CONNECTED MAY BE OMITTED. SEE ELECTRICAL DRAWING FOR OMITTED PINS.
- 50 MICRO-INCH SELECTIVE GOLD PLATING

Ref: SS-668802S-A-NF

CT660001C1/24-0031

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