

DAC9349 Complete 12-Bit CMOS DAC

FEATURES

- 12-Bit Binary or 3-Decade BCD Coding
- ±2.5V, ±5V, ±10V, 0 tq -5V, 0 to -10V Output Ranges
- Complete
- Low Power
- Low Cost
- Reliable

DESCRIPTION

The DAC9349 is a versatile, economically-priced complete 12-Bit D/A converter intended for use in fixed reference applications. The unit combines CMOS switches, precision laser-trimmed ladder, internal precision reference and gain-selectable output amplifier in a unique, low-cost 24-pin double DIP1. An external fixed reference of -10V ±10% may be used for ratiometric applications. Features of the DAC9349 include:

Choice of Coding - Units can be supplied in either 12-Bit binary (-12 models) or 3 decade BCD coding (-3D models).

Pin Selectable Output Ranges - External pin jumpers provide unipolar output ranges of 0 to -5V or 0 to -10V (binary models), or ±2.5V, ±5V and ±10V for (offset binary coded) bipolar outputs.

Completeness - No additional external components are required for a 12-Bit D/A conversion.

1, U.S. Patent Pending

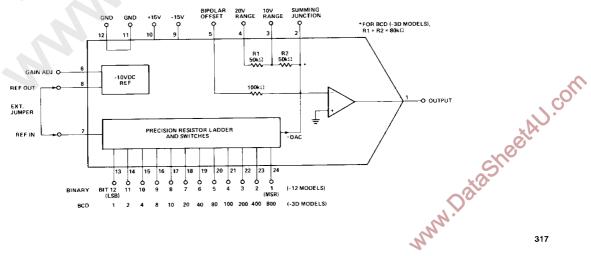


Good Dynamic Performance - Reduced settling time and higher slew rate compared to many 12-Bit converters.

Reliability - Like other converters in the commercial product line, the DAC9349 is offered in a unique package which has undergone the same extensive environmental testing to assure its reliability.

Reliability is enhanced by batch-processed, precision laser-trimmed resistor networks fabricated in our own facility. Similar to monolithic circuits, the networks are processed and functionally trimmed to assure consistent performance. Networks are glass passivated to assure reliability under adverse environmental conditions.

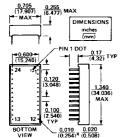
FUNCTIONAL DIAGRAM



SPECIFICATIONS

(Typical @ 25°C Using Internal Reference and V_{DD} =±15V unless

MODEL	DAC9349 Fixed Reference, Voltage Out	
TYPE		
DIGITAL INPUT		
Resolution	12 Bits	
Coding	n: 2 DEC BCD	
Unipolar	Binary; 3 DEC. BCD	
Bipolar	Offset Binary V _{II} =1.0V max	
Logic Levels (Threshold)	V _{IH} =3.5V min	
ANALOG OUTPUT		
Scale Factor ¹	0.1% F.S.R. max	
Initial Offset 1		
Unipolar	±0.2% F.S.R. max	
Bipolar	±0.1% F.S.R. max	
Voltage Range ²	0 to -5V, 0 ta -10V	
Unipolar	±2.5V, ±5V, ±10V	
Bipolar Current Compliance	±10mA typ ±5mA min	
Output Impedance	0.1 Q max	
REFERENCE		
Internal ³	-10VDC	
External (D.C. Only)	-10VDC ±10% @ 1mA	
STATIC PERFORMANCE	• • • • • • • • • • • • • • • • • • • •	
	±1/2 LSB max	
Linearity Differential Linearity	1 LSB max	
DYNAMIC PERFORMANCE	(202 ())	
Settling Time (worst case)	15µs max	
Slew Rate	1 V /μs	
STABILITY	•	
(Over Specified Temperature R	lange)	
Linearity	5opm/°C max	
Differential Linearity	2ppm/°C max	
Transfer Characteristics ⁴	30ppm/°C max	
POWER SUPPLY		
Requirements	+15V @ 5mA typ 10mA max	
B 1 2 B 22	-15V @ 15mA typ 20mA max	
Rejection Ratio	0.005%/% ma×	
TEMPERATURE		
Operating	0°C to +70°	
Storage	0°C to +85°C	
MECHANICAL		
Case Style	24-pin Double-DIP	



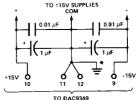
Case Dimensions

PIN	FUNCTION	PIN	FUNCTION	
NO.		NO.	BINARY	BCD
1	QUTPUT	24	BIT 1 (MSB)	800
2	SUMMING JCT	23	BIT 2	400
3	10V RANGE	22	BIT 3	200
4	20V RANGE	21	BIT 4	100
5	BIPOLAR OFFSET	20	BIT 5	80
6	GAIN ADJ	19	BIT 6	40
7	REFIN	18	BIT 7	20
8	REF OUT	17	BIT 8	10
9	-15V	16	BIT 9	8
10	+15V	15	BIT 10	4
11	GND	14	BIT 11	2
12	GND	13	BIT 12 ILSB)	- 1

NOTES:

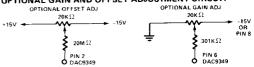
- 1 Or 10mV max, whichever is greater. Externally adjustable (see Figure 3).
- 2 The -3D models have a 3-decade BCD format with a unipolar FS output range of 0 to -9.99V.
- 3 For specified overall performance, external loading of the reference output (Pin 8) must not exceed 1.0mA.
- 4 Total effect of linearity, offset and gain temperature coefficients on the transfer characteristic of the unit.

APPLICATIONS INFORMATION



*SINGLE POINT GROUND WHERE POSSIBLE

OPTIONAL GAIN AND OFFSET ADJUSTMENT CIRCUIT



CALIBRATION PROCEDURE

(for optional external Gain & Offset adjustment)

Unipolar operation:

- Apply a 0 0 0 . . . 0 input code and set the OFFSET ADJ pot for 0V out.
- Apply a 1 1 1 . . . 1 input code and set the GAIN ADJ pot for -(F.S. -1LSB).

 Bipolar operation:
- Apply a 0 0 0 . . . 0 input code and set the OFFSET ADJ pot for a +F.S. output.
- Apply a 1 1 1 . . . 1 input code and set the GAIN ADJ pot for -(F.S. -1LBS).

BCD Unipolar operation:

- 1. Apply a 0 0 0 . . . 0 input code and set the OFFSET ADJ pot for
- Apply 1001 1001 1001 input and set the GAIN ADJ pot for -9.990V.

TRANSFER CHARACTERISTICS

Unipolar Operation

Digital Input	Analog Output
1111	-(F.S1 LSB) -F.S./2
0000	0V

Digital Input	Analog Outpu	
1111	-(F.S1 LSB)	
1000	0V	
ann n	l +⊢.S.	

Bipolar Operation

BCD Unipolar Operation

Digital Input	Analog Output
1001 1001 1001	-9.99
0101 0000 0000	-5.00
0000 0000 0000	0V

OUTPUT RANGE SCALING**

Output Voltage Range	Jumper These Pins	Connect Pin 5 to Pin	Coding	
0 to -5V	1&3, 2&4	11	Binary	
0 to -10V	1 & 3	11		
0 to -9.99V	1 & 4	*	BCD	
±2.5V	1 & 3 2 & 4	7	Offset Binary	
±5V	1 & 3	7		
±10V	1 & 4	7		

^{*}No Connection on BCD (-3D) Models.

CAUTION: ESD (Electro-Static Discharge) sensitive device. Permanent damage may occur when unconnected devices are subjected to high energy electrostatic fields. Unless otherwise noted, the voltage at any digital input should never exceed the supply voltage by more than 0.5 volts or go below -0.5 volts.

ORDERING INFORMATION

MODEL	DESCRIPTION	
DAC9349-12	Complete, 12-Bit Binary DAC	
D V C 0 3 4 0 - 3 D	Complete 3-Decade BCD DAC	

Specifications subject to change without notice.

^{**}Pin 7 must be connected to either the internal reference (Pin 8) or to an external -10VDC reference source.