

unit : mm

Epitaxial planar PNP silicon transistor

Description

• Dual chip digital transistor

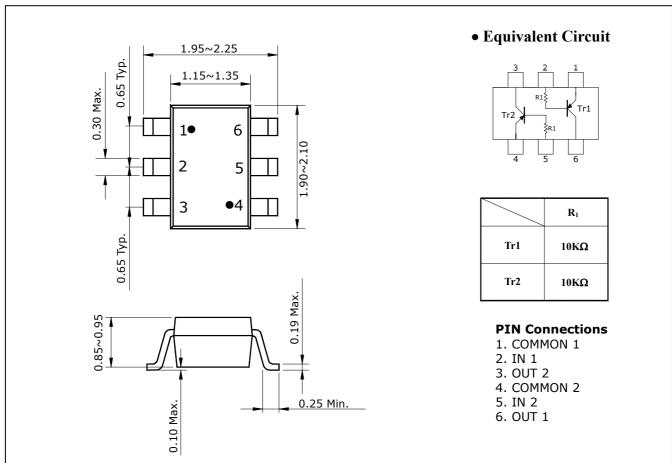
Features

- Two SRA2211 chips in SOT-363 package
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process

Ordering Information

Type NO.	Marking	larking Package Code	
SUR549J	SHS	SOT-363	

Outline Dimensions



SUR549J

(Ta=25°C)

Absolute Maximum Ratings [Tr1,Tr2]

Characteristic	Symbol	Rating	Unit
Output voltage	Vo	-50	V
Input voltage	VI	-30, 5	V
Output current	I _O	-100	mA
Power dissipation	P _D *	200	mW
Junction temperature	۲յ	150	°C
Storage temperature range	T _{stg}	-55 ~ 150	°C

*: Total rating

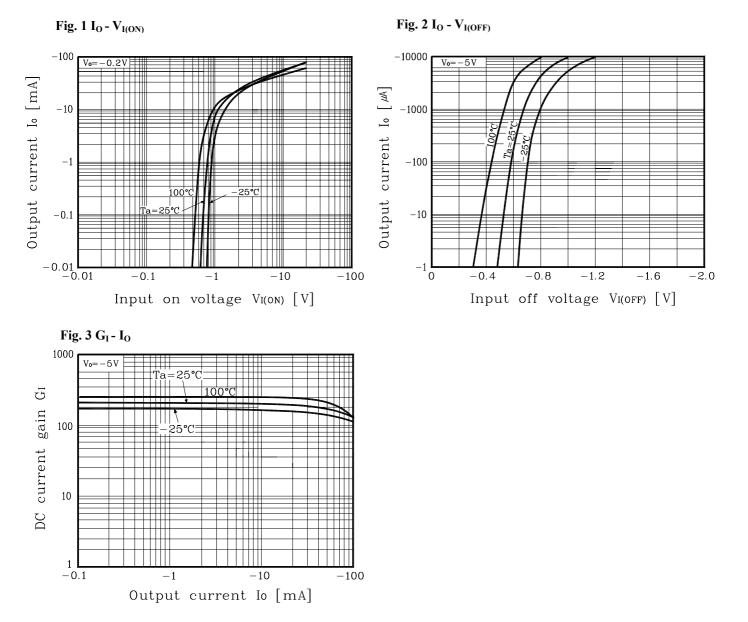
Electrical Characteristics [Tr1,Tr2]

(Ta=25°C) Characteristic Symbol **Test Condition** Min. Max. Typ. Unit -500 Output cut-off current $V_0 = -50V, V_I = 0$ _ nA $I_{O(OFF)}$ DC current gain G_{I} V_0 =-5V, I_0 =-10mA 120 ---_ V Output voltage I_0 =-10mA, I_I =-0.5mA -0.1 -0.3 V_{O(ON)} V_0 =-0.2V, I_0 =-5mA -0.9 -1.4 V Input voltage (ON) $V_{I(ON)}$ -V V₀=-5V, I₀=-0.1mA -0.3 _ Input voltage (OFF) V_{I(OFF)} -0.55 f_{T}^{*} V_0 =-10V, I_0 =-5mA, f=1MHz Transition frequency -200 -MHz Input current V_{I} =-5V, I_{O} =0 -0.88 mΑ I_{I} _ _ 7 Input resistor (Input to base) R_1 -10 13 KΩ

* : Characteristic of transistor only

SUR549J

Electrical Characteristic Curves [Tr1,Tr2]



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