

SANYO

No.1627B

2SC3643

NPN Triple Diffused Planar Silicon Transistor
VERY HIGH-DEFINITION DISPLAY
HORIZONTAL DEFLECTION OUTPUT APPLICATIONS

Features

- . High reliability (Adoption of HVP process)
- . Fast speed.
- . High breakdown voltage.
- . Adoption of MBIT process.

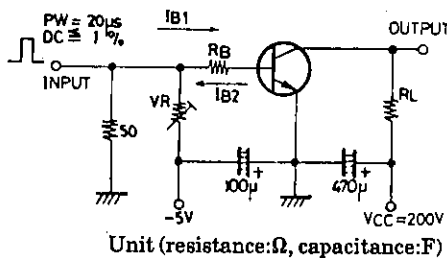
Absolute Maximum Ratings at Ta=25°C

			unit
Collector-to-Base Voltage	V _{CB0}	1200	V
Collector-to-Emitter Voltage	V _{CEO}	800	V
Emitter-to-Base Voltage	V _{EBO}	7	V
Collector Current	I _C	8	A
Peak Collector Current	i _{cp}	16	A
Collector Dissipation	P _C	140	W
Junction Temperature	T _J	150	°C
Storage Temperature	T _{stg}	-55 to +150	°C

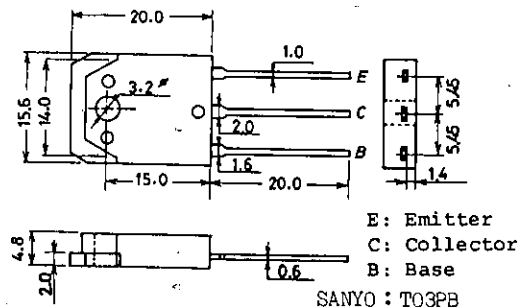
Electrical Characteristics at Ta=25°C

			min	typ	max	unit
Collector Cutoff Current	I _{CB0}	V _{CB} =800V, I _E =0			10	μA
Collector-to-Emitter Sustain Voltage	I _{CES} V _{CEO(sus)}	V _{CE} =1200V, R _{BE} =0 I _C =100mA, I _B =0	800		0.5	V
Emitter Cutoff Current	I _{EBO}	V _{EB} =5V, I _C =0			1	mA
Collector-to-Emitter Saturation Voltage	V _{CE(sat)}	I _C =6A, I _B =1.2A			5	V
Base-to-Emitter Saturation Voltage	V _{BE(sat)}	I _C =6A, I _B =1.2A			1.5	V
DC Current Gain	h _{FE}	V _{CE} =5V, I _C =1.2A	8			
Storage Time	t _{stg}	I _C =6A, I _{B1} =1.2A, I _{B2} =-2.4A			3.0	μs
Fall Time	t _f	I _C =6A, I _{B1} =1.2A, I _{B2} =-2.4A		0.1	0.2	μs

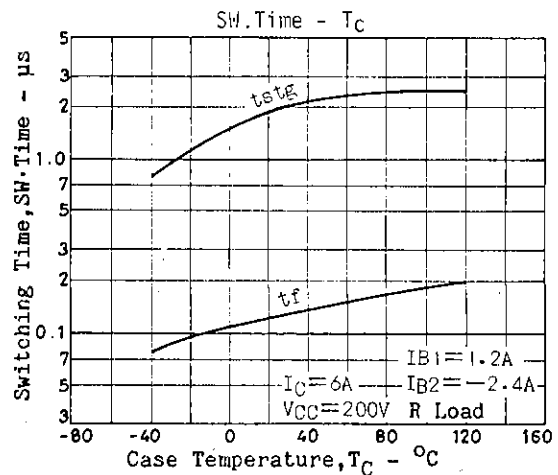
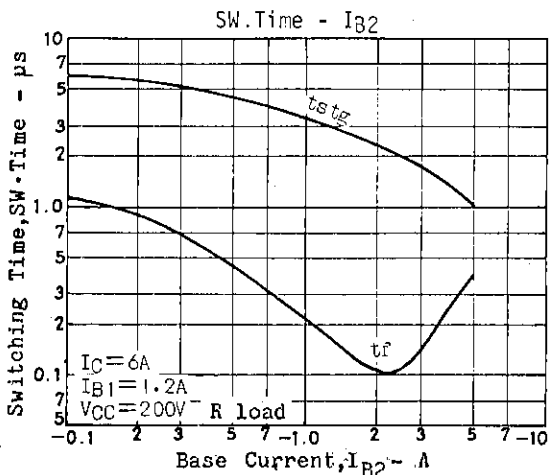
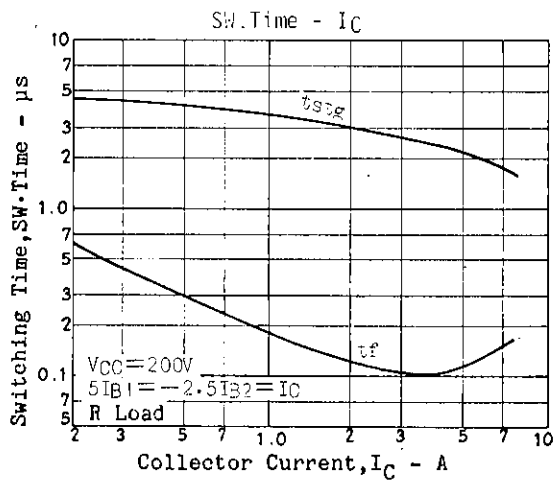
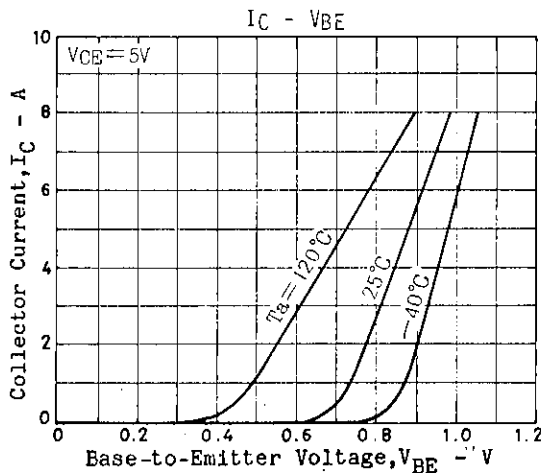
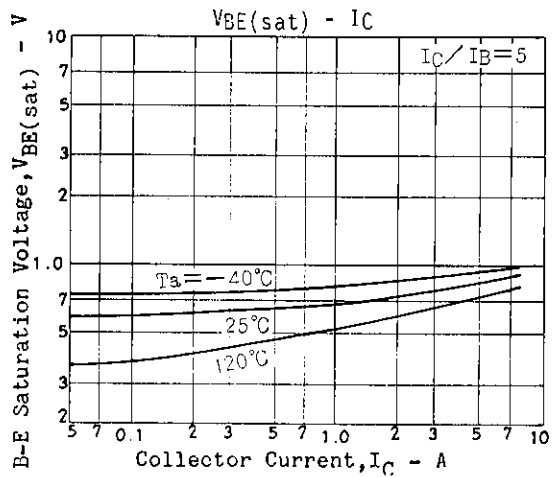
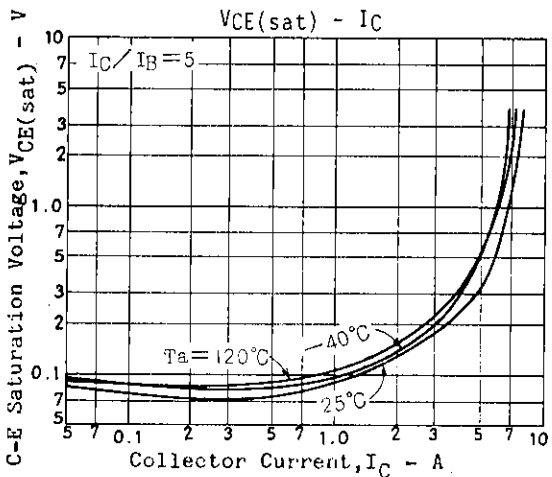
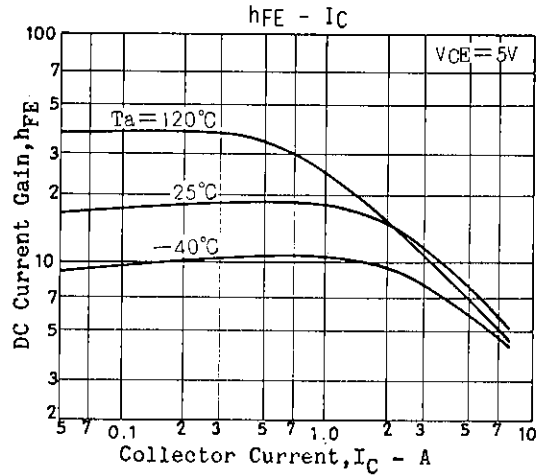
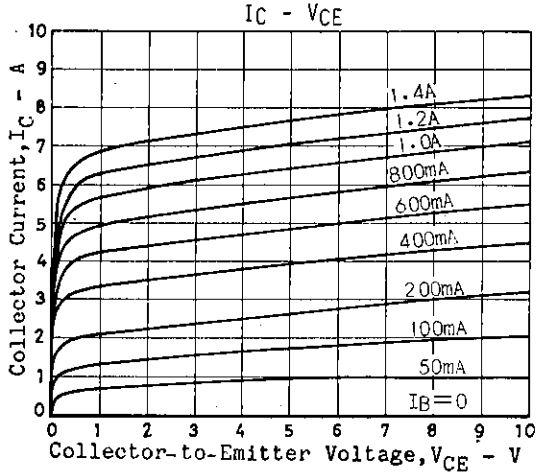
Switching Time Test Circuit

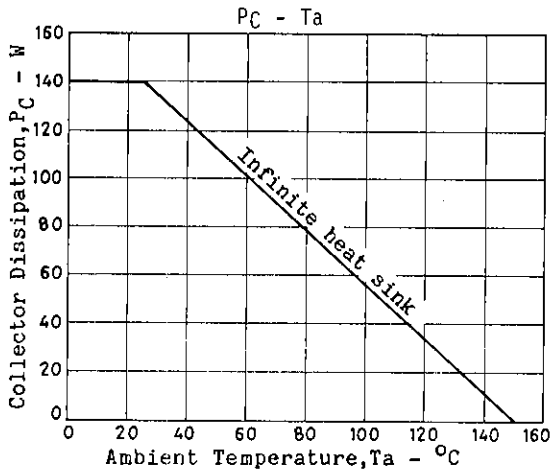
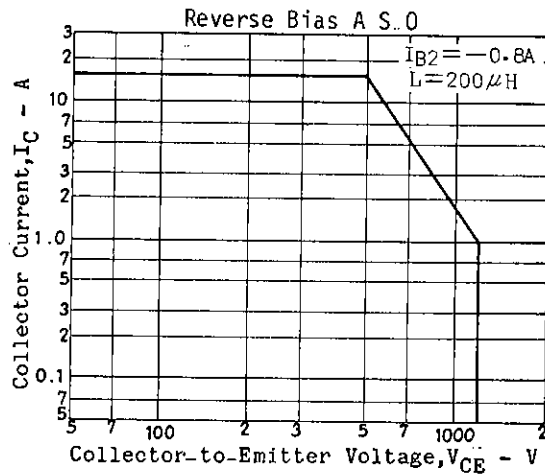
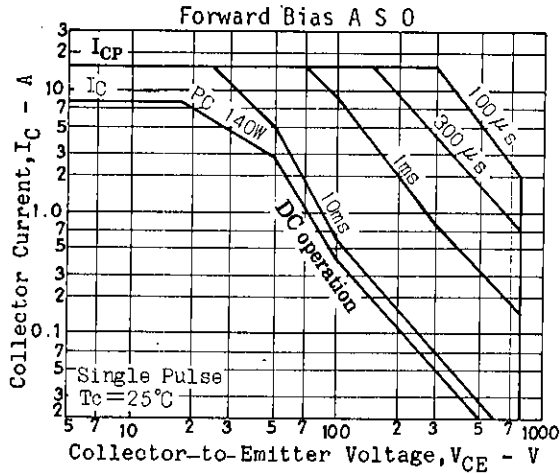


Package Dimensions 2022
(unit: mm)



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