

2N3878, 2N3879, 2N5202, 2N6500

File Number **766**

**High-Speed, Epitaxial-Collector
Silicon N-P-N Planar Transistors**

For High-Speed Switching and Linear-Amplifier Applications

Features:

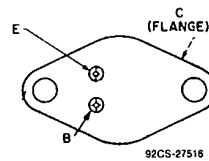
- Maximum-area-of-operation curves for dc and pulse operation
- High sustaining voltage
- Total saturated transition time less than 1 μ s for 2N3879, 2N5202, and 2N6500

RCA-2N3878, 2N3879, 2N5202, and 2N6500* are epitaxial silicon n-p-n transistors. The 2N3878 is an amplifier type intended for audio-, ultrasonic-, and radio-frequency circuits. Types 2N3879, 2N5202, and 2N6500 are switching transistors intended for use in high-current, high-speed switching circuits.

Typical applications for these transistors include: low-distortion power amplifiers, oscillators, switching regulators, series regulators, converters, and inverters.

*Formerly RCA Dev. Type Nos. TA2509, TA2509A, TA7285, and TA8932, respectively.

TERMINAL DESIGNATIONS



JEDEC TO-213AA

MAXIMUM RATINGS, Absolute-Maximum Values:

	2N3878	2N3879	2N5202	2N6500		
*COLLECTOR-TO-BASE VOLTAGE	V_{CB0}	120	120	100	120	V
COLLECTOR-TO-EMITTER SUSTAINING VOLTAGE:						
With external base-to-emitter resistance (R_{BE}) = 50 Ω .	$V_{CER(sus)}$	65	90	75*	110*	V
With base open.	$V_{CEO(sus)}$	50*	75*	50	90*	V
*EMITTER-TO-BASE VOLTAGE	V_{EBO}	7	7	6	7	V
*CONTINUOUS COLLECTOR CURRENT	I_C	4	7	4	4	A
PEAK COLLECTOR CURRENT	I_{CM}	10	10	5	5	A
*CONTINUOUS BASE CURRENT	I_B	4	5	2	3	A
*TRANSISTOR DISSIPATION	P_T					W
At case temperature (T_C) = 25°C		35	35	35	35	
At case temperatures above 25°C		Derate linearly at 0.2 W/°C				
For other conditions		See Figs. 1, 3 and 4				
*TEMPERATURE RANGE:						°C
Storage & operating (Junction)			-65 to 200			
*PIN TEMPERATURE:						°C
1/32 in. (0.8 mm) from seating plane for 10 s max.		235	235	235	235	

* In accordance with JEDEC registration data format JS-6 RDF-2 (2N3878); JS-6 RDF-1 (2N3879, 2N5202, 2N6500).

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ELECTRICAL CHARACTERISTICS, At Case Temperature (T_C) = 25°C unless otherwise specified:

CHARACTERISTIC	SYMBOL	TEST CONDITIONS				LIMITS								UNITS	
		VOLTAGE V dc		CURRENT A dc		2N3878		2N3879		2N5202		2N6500			
		V _{CE}	V _{BE}	I _C	I _B	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.		
* Collector Cutoff Current: With base-emitter junction reverse-biased	I _{CEV}	100	-1.5			-	-	-	-	-	10	-	-	mA	
		110	0			-	25	-	25	-	-	-	5		
* With base-emitter junction reverse-biased and $T_C = 150^\circ\text{C}$	I _{CEV}	100	-1.5			-	4	-	4	-	10	-	-	10	
With base open	I _{CEO}	40			0	-	5*	-	5	-	-	-	-	5	
		70			0	-	-	-	-	-	-	-	-	5	
* Emitter Cutoff Current	I _{EBO}		-6			-	10	-	10	-	10	-	-	25	
Collector-to-Emitter Sustaining Voltage With base open	V _{CEO(sus)}			0.2	0	50 ^a	-	75 ^a	-	50 ^a	-	90 ^a	-	V	
With external base-to-emitter resistance (R_{BE}) = 50 Ω	V _{CER(sus)}			0.2	0	65 ^a	-	90 ^a	-	75 ^a	-	110 ^a	-	V	
DC Forward-Current Transfer Ratio	h _{FE}	1.2		4 ^b		-	-	-	-	10*	100*	-	-		
		2		0.5 ^b		40*	200*	-	-	-	-	-	-		
		2		3 ^b		-	-	-	-	-	-	-	15*		60*
		2		4 ^b		8*	-	12*	100*	-	-	-	-		-
		5		4 ^b		20*	-	20	80	-	-	-	-		-
		5		0.5 ^b		50*	200*	40	-	-	-	-	-		-
* Collector-to-Emitter Saturation Voltage	V _{CE(sat)}			3 ^b 4 ^b	0.3 0.4	-	2	-	1.2	-	1.2	-	-	1.5	
* Base-to-Emitter Voltage	V _{BE}	2		4 ^b	-	-	2.5	-	-	-	-	-	-	V	
* Base-to-Emitter Saturation Voltage	V _{BE(sat)}			3 ^b 4 ^b	0.3 0.4	-	-	-	2	-	2	-	-	2.5	
Collector-to-Base Output Capacitance (f = 1 MHz, V _{CB} = 10 V)	C _{ob}					-	175*	-	175	-	175	-	175	pF	
Second Breakdown Collector Current With base forward-biased and 1-s nonrepetitive pulse	I _{S/b}	40				750	-	500	-	400	-	400	-	mA	
* Magnitude of Common Emitter, Small-Signal, Short-Circuit, Forward-Current Transfer Ratio (f = 10 MHz)	h _{fe}	10		0.5		4	-	4	-	6	-	6	-		
* Common-Emitter, Small-Signal, Short-Circuit, Forward-Current Transfer Ratio (f = 1 kHz)	h _{fe}	30		0.1		40	-	-	-	-	-	-	-		
Thermal Resistance Junction-to-case	R _{θJC}					-	5	-	5	-	5	-	5	°C/W	

* In accordance with JEDEC registration data format JS-6 RDF-2 (2N3878); JS-6 RDF-1 (2N3879, 2N5202, 2N6500).

^a CAUTION: Sustaining voltages V_{CEO(sus)} and V_{CER(sus)} MUST NOT be measured on a curve tracer.

^b Pulsed, pulse duration = 300 μ s, duty factor \leq 2 %.

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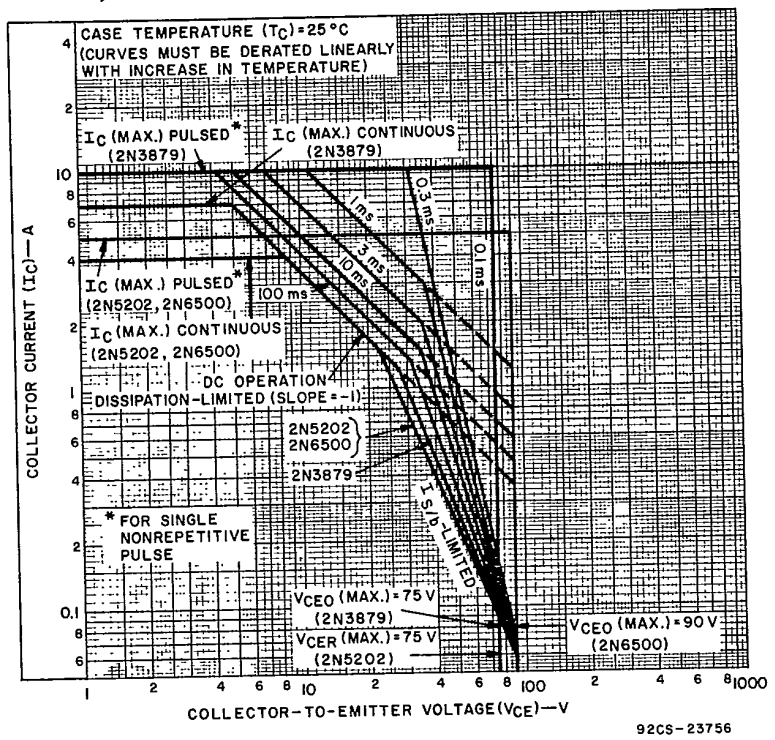


Fig. 1 - Maximum operating areas for 2N3879, 2N5202, and 2N6500.

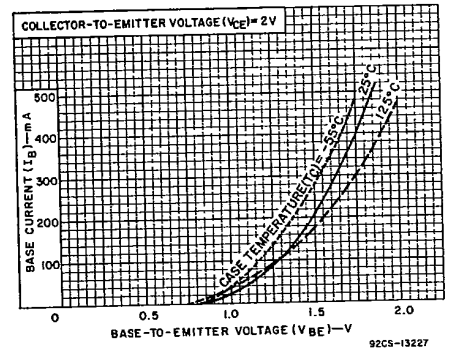


Fig. 2 - Typical input characteristics for all types.

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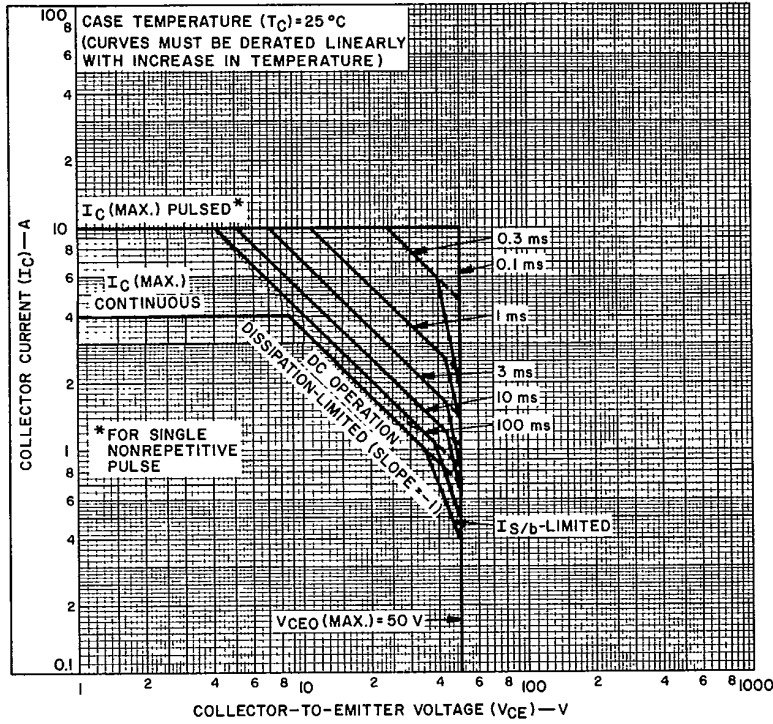


Fig. 3 - Maximum operating areas for 2N3878.

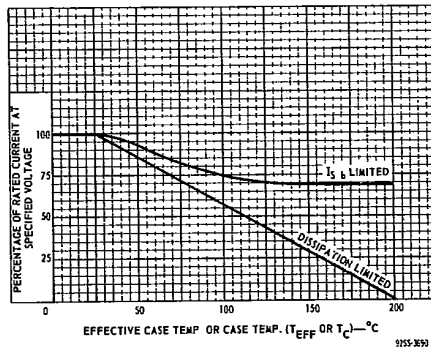


Fig. 4 - Dissipation derating for all types.

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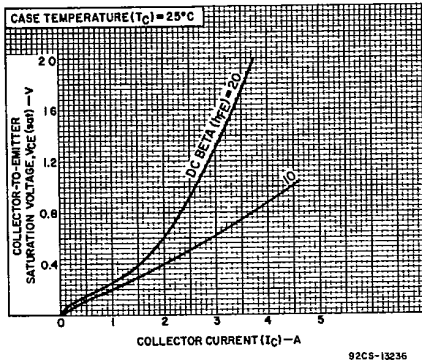


Fig. 5 - Typical saturation-voltage characteristics for 2N3878, and 2N3879.

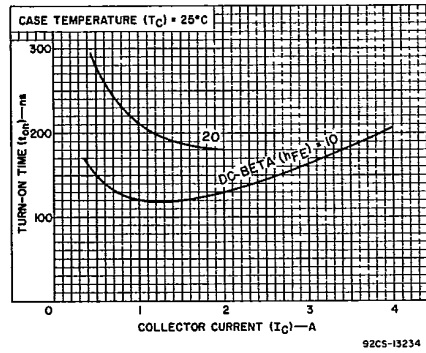


Fig. 6 - Typical turn-on time for 2N3879, 2N5202, and 2N6500.

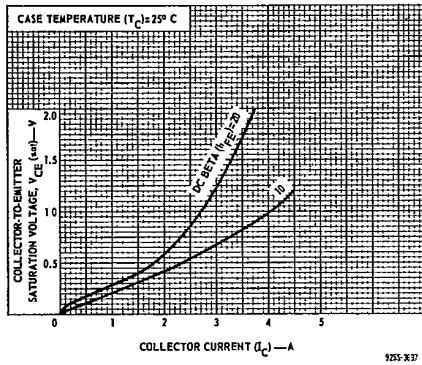


Fig. 7 - Typical saturation-voltage characteristics for 2N5202.

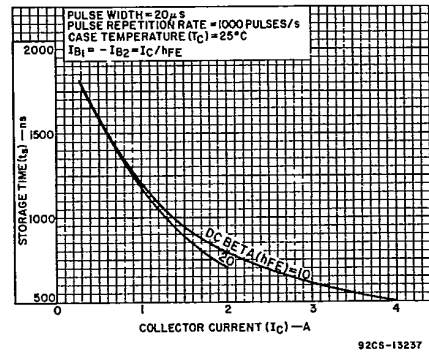


Fig. 8 - Typical storage time for 2N3879, 2N5202, and 2N6500.

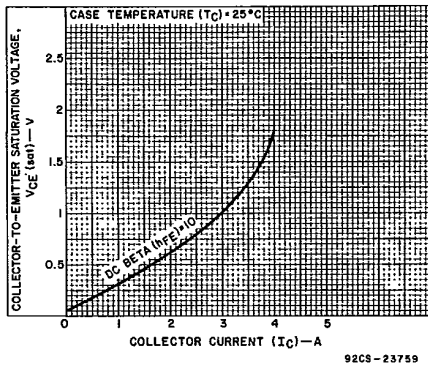


Fig. 9 - Typical saturation-voltage characteristics for 2N6500.

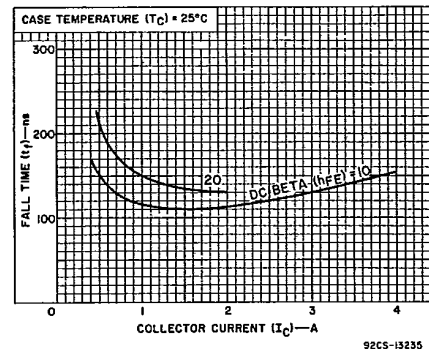


Fig. 10 - Typical fall time for 2N3879, 2N5202, and 2N6500.

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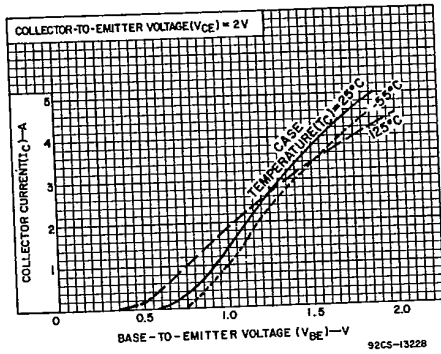


Fig. 11 - Typical transfer characteristics for all types.

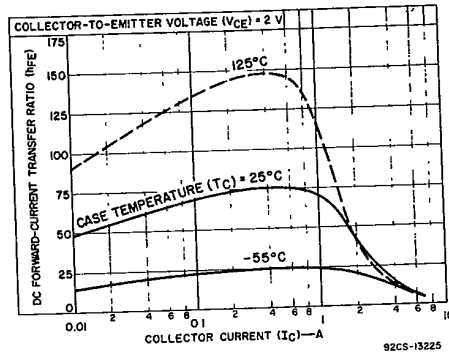


Fig. 12 - Typical dc beta characteristics for 2N3878 and 2N3879.

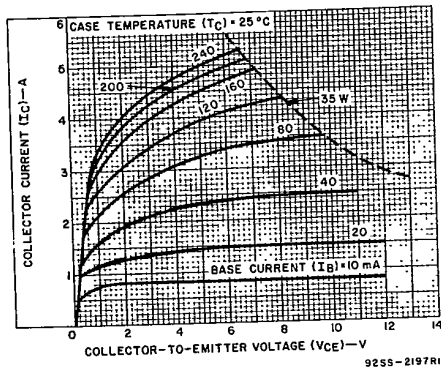


Fig. 13 - Typical output characteristics for 2N3878, 2N3879 and 2N5202.

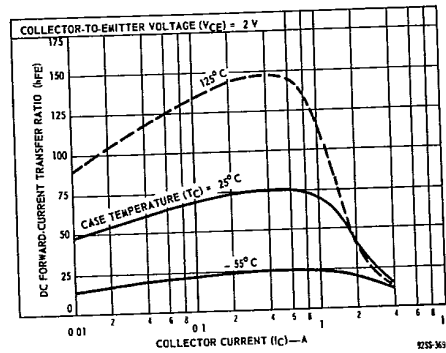


Fig. 14 - Typical dc beta characteristics for 2N5202.

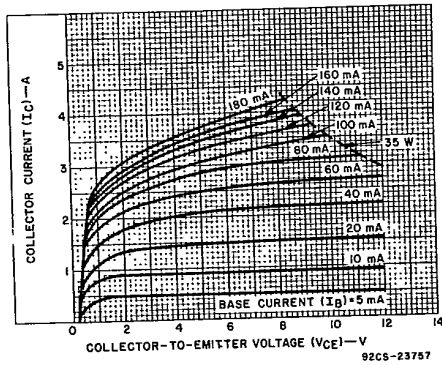


Fig. 15 - Typical output characteristics for 2N6500.

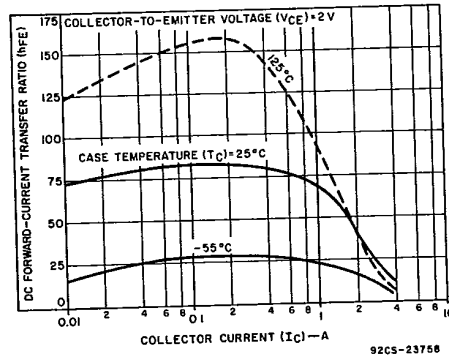


Fig. 16 - Typical dc beta characteristics for 2N6500.

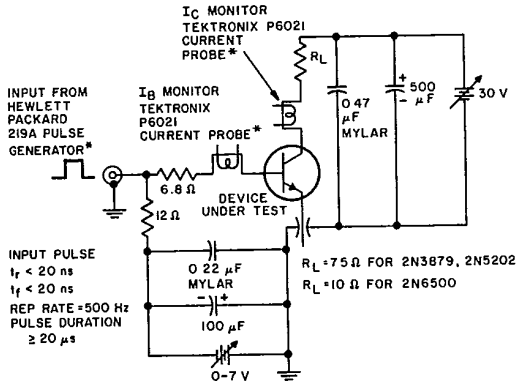
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TRANSITION AND STORAGE-TIME CHARACTERISTICS FOR SWITCHING TYPES, At Case Temperature (T_C) = 25°C:

CHARACTERISTIC	SYMBOL	TEST CONDITIONS			LIMITS						UNITS	
		VOLTAGE V dc		CURRENT A dc		2N3879		2N5202		2N6500		
		VCC	I _C	I _B	Min.	Max.	Min.	Max.	Min.	Max.		
Saturated Switching Time	t _d	30	3	0.3 ^a	-	-	-	-	-	40	ns	
		30	4	0.4 ^a	-	40	-	-	-	-		
		30	4	0.8 ^a	-	-	-	40	-	-		
Delay time	t _d	30	3	0.3 ^a	-	-	-	-	-	400	ns	
		30	4	0.8 ^a	-	400	-	-	-	-		
Rise time	t _r	30	4	0.4 ^a	-	-	-	-	-	-	ns	
		30	4	0.8 ^a	-	-	-	400	-	-		
Storage time	t _s	30	3	0.3 ^a	-	-	-	-	-	1000	ns	
		30	4	0.4 ^a	-	800	-	-	-	-		
Fall time	t _f	30	4	0.8 ^a	-	-	-	1200	-	-	ns	
		30	3	0.3 ^a	-	-	-	-	-	500		
Fall time	t _f	30	4	0.4 ^a	-	-	-	-	-	-	ns	
		30	4	0.8 ^a	-	400	-	-	-	-		
Fall time	t _f	30	3	0.3 ^a	-	-	-	-	-	-	ns	
		30	4	0.8 ^a	-	-	-	400	-	-		

* In accordance with JEDEC registration data format (JS-6, RDF-1)

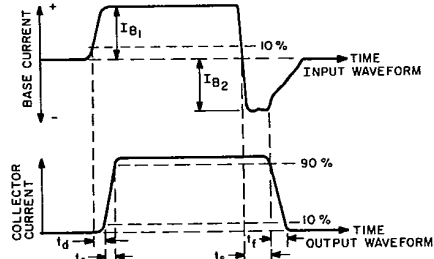
^a I_{B1} = I_{B2}



*OR EQUIVALENT

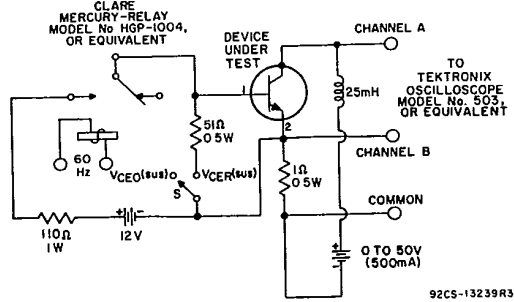
92CS-23754

Fig. 17 - Circuit used to measure switching times for 2N3879, 2N5202, and 2N6500.



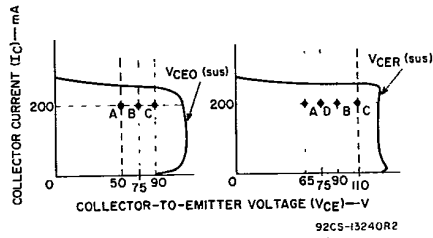
92CS-23760

Fig. 18 - Oscilloscope display for measurement of switching times. (Circuit shown in Fig. 1).



92CS-13239R3

Fig. 19 - Circuit used to measure sustaining voltages, V_{CE0}(sus) and V_{CER}(sus) for all types.



92CS-13240R2

The sustaining voltages V_{CE0}(sus) and V_{CER}(sus) are acceptable when the traces fall to the right and above point "A" for types 2N3878, 40375, and 2N5202; point "B" for type 2N3879; and point "C" for type 2N6500. The sustaining voltage V_{CER}(sus) is acceptable when the trace falls to the right and above point "D" for type 2N5202.

Fig. 20 - Oscilloscope display for measurement of sustaining voltages.