

GaAs MMIC DBS 4X2 IF SWITCH

UPG183GR

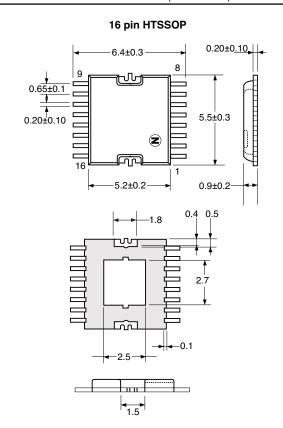
FEATURES

- · FOUR INDEPENDENT IF CHANNELS
- INTEGRAL SWITCHING TO CHANNEL INPUT TO EITHER CHANNEL OUTPUT
- INSERTION LOSS PER CHANNEL: 7 dB TYP (Zo = 50Ω)
- FREQUENCY RANGE: 950 MHz to 2150 MHz
- CHANNEL TO CHANNEL ISOLATION: 26.5 dB TYP
- SMALL 16 PIN HTSSOP PACKAGE

DESCRIPTION

The UPG183GR is intended for use in Direct Broadcast Satellite (DBS) applications within the Low Noise Block (LNB) down converter, for systems where at least two LNB outputs are required. It offers four intermediate frequency amplifier channels that can independently select 1 of 4 IF inputs. It is housed in a very small 16 pin plastic HTSSOP package, available on tapeand-reel, easy to install and contributes to miniaturizing the systems.

OUTLINE DIMENSIONS (Units in mm)



ELECTRICAL CHARACTERISTICS

(TA = +25°C, unless otherwise specified, VCONT1 to VCONT8 = 0/+5 V, PIN = 0 dBm, Zo = 50 Ω , Each Port)

PART NUMBER PACKAGE OUTLINE			UPG183GR 16 pin HTSSOP		
SYMBOLS	MBOLS PARAMETERS AND CONDITIONS UNITS		MIN	TYP	MAX
LINS 1	Insertion Loss 1, f = 0.95 GHz to 1.5 GHz	dB	_	7.0	9.0
LINS 2	Insertion Loss 2, f = 1.5 GHz to 2.15 GHz	dB	_	8.0	10.0
ΔLINS	Insertion Loss Flatness, I LINS (0.95 GHz)—LINS (2.15 GHz)I	dB	_	1.5	3.0
ISOL 1	Channel Isolation 1, f = 0.95 GHz to 1.5 GHz	dB	24	26.5	_
ISOL 2	Channel Isolation 2, f = 1.5 GHz to 2.15 GHz	dB	22	23.5	_
RL(out)	Output Return Loss, f = 0.95 GHz to 2.15 GHz	dB	10	14	_
ICONT	Control Current ¹ , VCONT = +5 V/0 V, RFoff	mA	_	_	0.5

Notes:

1. Per one control pin.

ABSOLUTE MAXIMUM RATINGS¹ (TA = +25°C)

SYMBOLS	PARAMETERS	UNITS	RATINGS
VCONT1-8	Control Voltage 1-8	V	-1.0 to +6.0
Ртот	Total Power Dissipation ²	W	2
Pin	Input Power	dBm	+ 10
Та	Operating Ambient Temp.	°C	-40 to +85
Тѕтс	Storage Temperature	ô	-65 to +150

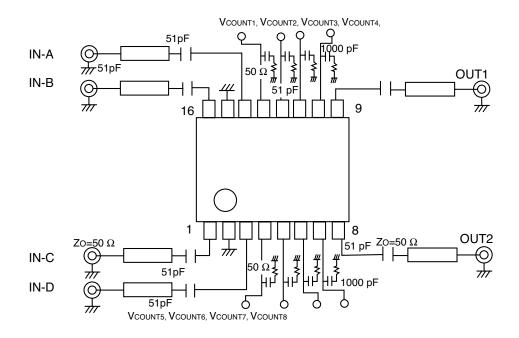
RECOMMENDED OPERATING CONDITIONS (TA = +25°C)

SYMBOLS	PARAMETERS	UNITS	MIN	TYP	MAX
VCONT(H)	Control Voltage (High)	V	+4.5	+5	+5.5
VCONT(L)	Control Voltage (Low)	V	-0.5	0	+0.5

Notes:

- Operation in excess of any one of these parameters may result in permanent damage.
- 2. Mounted on a 50x50x1.6 mm double copper clad epoxy glass PWB, TA = +85°C.

EVALUATION CIRCUIT (VCONT1 to VCONT8 = 0/+5 V, PIN = 0d Bm, Zo = 50 Ω, DC Blocking Capacitor = 51 pF)



CHANNEL SELECT TRUTH TABLE

ON CH	ANNEL	CONTROL PIN							
OUT1	OUT2	Vcont1	VCONT2	Vсонтз	Vcont4	VCONT5	V CONT6	VCONT7	Vconт8
IN-A	_	High	Low	High	Low	_	-	-	_
IN-B	-	High	Low	Low	High	_	-	-	_
IN-C	-	Low	High	High	Low	-	-	-	_
IN-D	-	Low	High	Low	High	-	_	_	_
_	IN-A	_	_	-	_	High	Low	High	Low
_	IN-B	_	_	_	_	High	Low	Low	High
_	IN-C	_	_	-	_	Low	High	High	Low
_	IN-D	-	-	-	_	Low	High	Low	High

PIN CONNECTION AND INTERNAL BLOCK DIAGRAM

PIN NO.	CONNECTION	PIN NO.	CONNECTION
1	IN-C	9	OUT1
2	GND	10	VCONT4
3	IN-D	11	VCONT3
4	VCONT5	12	VCONT2
5	VCONT6	13	Vcont1
6	VCONT7	14	IN-A
7	VCONT8	15	GND
8	OUT2	16	IN-B

16 15 14 13 12 11 10 9 SW SW 1 2 3 4 5 6 7 8

TOP VIEW

ORDERING INFORMATION

PART NUMBER	PACKAGE	QUANTITY
UPG183GR-E1	16-pin Plastic HTSSOP	Qty. 3k pcs/Reel

Notes:

1. Carrier tape, 12 mm wide.