

# AT90SC0802R

## PRELIMINARY INFORMATION

## CONTACTLESS RFID CHIP WITH AVR<sup>®</sup> MICROPROCESSOR

### FEATURES

- ISO 14443-B, 13.56 MHz RF interface
- Serial-contacted interface
- Atmel 8515 AVR microcontroller core
- 2 kilobyte EEPROM, 8 kilobyte program ROM, 256 bytes of RAM
- Single metal mask programmable ROM for fast turnaround
- 70 pF on-chip tuning capacitor



The AT90SC0802R is based on Atmel's powerful AVR 8-bit RISC microprocessor, which offers single-cycle instruction execution with better code densities than most other eight-bit architectures. The AT90SC0802R is intended for use in ISO/IEC 14443 type B contactless cards and includes all those circuits necessary to build the complete card/tag with the exception of the coil antenna.

The chip includes 8 kilobytes of ROM, 2 kilobytes of EEPROM, 256 bytes of SRAM and 32 registers. It is based on the complete 8515 AVR processor core that supports around 120 instructions. Programs can be executed out of either the EEPROM or ROM. Contact your local Atmel sales office for AVR programming information, as well as compiler, assembler and other development aids. The chip supports a hardware random-number generator as well as memory mapping options for high-security applications.



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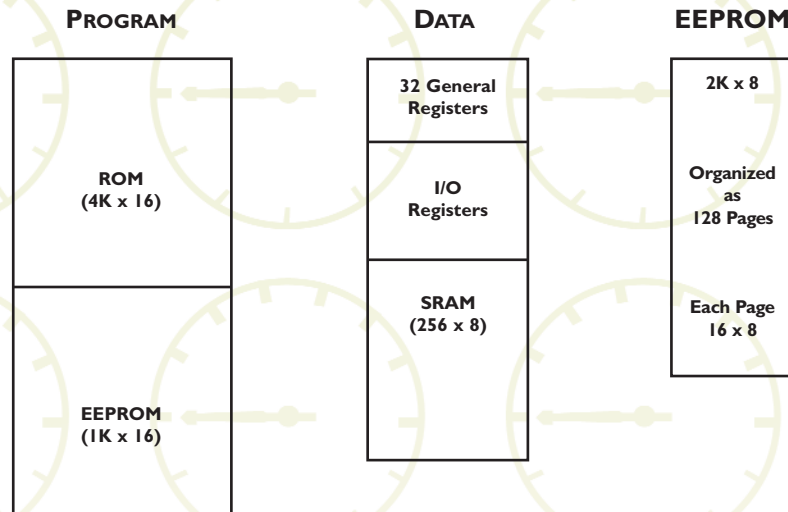


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2000A-12/00/10M

The memory map shown below illustrates how these various memories are accessed. The AVR processor core can access the program and data spaces directly.



The mask programmable ROM memory used for the default operating program is organized as 16 bits wide and is programmable via single metal mask updates to expedite orders to reduce the amount of time from order submission to first shipment.

The analog front-end is optimized for communications at 13.56 MHz and is compatible with ISO 14443-2 type B. There is a serial port configurable by processor software that allows a contacted interface. The software communicates with the analog circuitry through a digital interface block. This interface software controls most of the aspects of the I/O protocol, including data encoding for both transmission and reception, anti-collision, error detection/correction and encryption.

The front-end power generation circuitry is designed to generate a maximum current of 5 mA, in order to permit operation of the processor at 3.4 MHz. The chip includes a 70 pF tuning capacitor across the coil input pins—only an antenna coil is needed to make a complete RFID tag. An internal filter capacitor of about 2000 pF is integrated on the chip to smooth noise in the power supply.

In contact mode, the AVR processor speed is equal to one-half the clock speed. Contact mode communications are compliant with ISO/IEC 7816 and include a parity bit after each 8 data bits during both read and write operations. Contact mode VDD is 5.0 volts.

## APPLICATIONS

**SmartCards:** Designed for either contactless or contacted smart cards, this chip enables applications such as e-purse, customer rewards, transit fare collection and smart credit cards where high-security identification and encryption are required.

**Remote Sensing Data Collection:** RFID built into machinery allows an operator to poll system status, monitor and change calibration values and run built-in self-tests without the need for a connector. This allows data collection from systems in harsh or inconvenient locations. Automotive applications use Atmel RFID to record vital system conditions, enabling an alert before a failure and diagnostics after failure has occurred.

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